

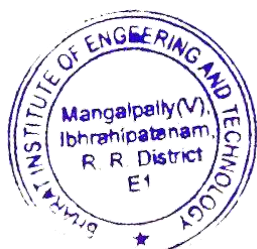
BHARAT INSTITUTE OF ENGINEERING AND TECHNOLOGY

Mangalpally (Village), Ibrahimpatnam (Mandal), Ranga Reddy (District), Telangana-501510

1.3.2. Average percentage of courses that include experiential learning through project work/field work/internship during last five years

Summery Sheet for Last 5 years

Academic year/Branch	CSE	Civil	ECE	EEE	Mech	IT	H&S	MBA	M.Tech	Total
2018-19	35	22	28	27	28	9	10	4	78	241
2017-18	33	23	29	21	26	6	13	5	78	234
2016-17	13	19	27	25	24	18	7	10	77	220
2015-16	31	22	24	26	28	9	10	10	96	256
2014-15	34	25	29	22	27	17	5	8	96	263
Total	146	111	137	121	133	59	45	37	425	1214



Vatthi Rao Babu
PRINCIPAL
Principal
Bharat Institute of Engg. and Tech
Mangalpally(V), Ibrahimpatnam(M)
Ranga Reddy (Dist)-Telangana-501510

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. (ELECTRICAL POWER SYSTEMS/ POWER SYSTEM CONTROL AND AUTOMATION/
ELECTRICAL POWER ENGINEERING)**

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Power System Analysis	25	75	4	0	0	4
PC-2	Advanced Power System Protection	25	75	4	0	0	4
PC-3	Modern Control Theory	25	75	4	0	0	4
PE-1	1. EHV AC Transmission 2. High Voltage Engineering 3. Advanced Digital Signal Processing	25	75	3	0	0	3
PE-2	1. Power Quality 2. Microcontrollers and applications 3. Distribution Automation	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Power & Energy Systems Lab - I	25	75	0	0	3	2
Seminar I	Seminar – I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Power System Dynamics and Control	25	75	4	0	0	4
PC-5	Flexible AC Transmission Systems (FACTS)	25	75	4	0	0	4
PC-6	Power System Operation and Deregulation	25	75	4	0	0	4
PE-3	1. Gas Insulated Systems(GIS) 2. Programmable Logic Controllers and applications 3. Energy Auditing Conservation and Management	25	75	3	0	0	3
PE4	1. Reactive Power Compensation and Management 2. Power System Reliability 3. Voltage Stability	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Power & Energy Systems Lab - II	25	75	0	0	3	2
Seminar II	Seminar –II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

ADVANCED POWER SYSTEM ANALYSIS
(Professional Core - I)

Prerequisite: Computer Methods in Power Systems

Course Objectives:

- To analyze a Power System Network using graph theory.
- To interpret the formation of Network matrices.
- To construct the necessity of load flow studies and various methods of Analysis.
- To examine short circuit analysis using Z_{BUS} .

Course Outcomes: Upon the completion of the subject, the student will be able to

- Remember proper mathematical models for analysis.
- Conclude methodologies of load flow studies for the power network.
- Apply contingency Analysis.
- Analyze power system studies.

UNIT-I:

Admittance Model and Network Calculations, Branch and Node Admittances, Mutually Coupled Branches in Y_{BUS} , An Equivalent Admittance Network, Modification of Y_{BUS} , Network Incidence Matrix and Y_{BUS} , Method of Successive Elimination, Node Elimination, Triangular Factorization, Sparsity and Near Optimal Ordering.

UNIT-II:

Impedance Model and Network Calculations, the BUS Admittance and Impedance Matrices, Thevenin's Theorem and Z_{BUS} , Algorithms for building Z_{BUS} , Modification of existing Z_{BUS} , Calculation of Z_{BUS} elements from Y_{BUS} , Power Invariant Transformations, Mutually Coupled Branches in Z_{BUS} .

UNIT-III:

Gauss Seidel method, N-R Method, Decoupled method, fast decoupled method, comparison between power flow solutions. DC load flow.

UNIT-IV:

Z_{BUS} Method in Contingency Analysis, Adding and Removing Multiple Lines, Piecewise Solution of Interconnected Systems, Analysis of Single Contingencies, Analysis of Multiple Contingencies, Contingency Analysis of DC Model, System Reduction for Contingency and Fault Studies.

UNIT-V:

Fault Analysis: Symmetrical faults-Fault calculations using Z_{BUS} - Fault calculations using Z_{BUS} equivalent circuits –Selection of circuit breakers- Unsymmetrical faults-Problems on various types of faults.

TEXT BOOKS:

1. John J. Grainger and W. D. Stevenson, "Power System Analysis"- T.M.H. Edition.
2. Modern Power System Analysis– by I. J. Nagrath & D. P. Kothari Tata McGraw – Hill Publishing Company Ltd, 2nd edition.

REFERENCE BOOKS:

1. Power System Analysis and Design by J. Duncan Glover and M.S. Sarma., Cengage 3rd Edition.
2. Olle. L.Elgard, "Electrical Energy Systems Theory"-T.M.H. Edition.
3. Power systems stability and control, Prabha Kundur, The McGraw – Hill companies.
4. Power System Operation and Control, Dr. K. Uma Rao, Wiley India Pvt. Ltd.
5. Operation and Control in Power Systems, PSR Murthy, Bs Publications.
6. Power System Operation, Robert H. Miller, James H. Malinowski, The McGraw – Hill companies.
7. Power Systems Analysis, operation and control by Abhijit Chakrabarti, Sunitha Halder, PHI 3/e , 2010

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

**ADVANCED POWER SYSTEM Protection
(Professional Core - II)**

Prerequisite: Switch Gear and Protection

Course Objectives:

- To distinguish all kinds of circuit breakers and relays for protection of Generators, Transformers and feeder bus bars from Over voltages and other hazards.
- To generalize neutral grounding for overall protection.
- To illustrate the phenomenon of Over Voltages and its classification.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Understand the basic function of a circuit breaker, all kinds of circuit breakers and differentiate fuse and circuit breakers under fault condition.
- Describe the necessity for the protection of alternators, transformers and feeder bus bars from over voltages and other hazards
- Illustrate neutral grounding, and how over voltages can be generated and how system can be protected against lightning and switching transient over voltages with various protective means
- Identify operation and control of microprocessor based relays.

UNIT-I:

Static Relays: Advantages of static relays-Basic construction of static relays-Level detectors-Replica impedance –Mixing circuits-General equation for two input phase and amplitude comparators-Duality between amplitude and phase comparators.

Amplitude Comparators: Circulating current type and opposed voltage type- rectifier bridge comparators, Direct and Instantaneous comparators.

UNIT-II:

Phase Comparators: Coincidence circuit type- block spike phase comparator, techniques to measure the period of coincidence-Integrating type-Rectifier and Vector product type- Phase comparators.

Static Over Current Relays: Instantaneous over-current relay-Time over-current relays-basic principles –definite time and Inverse definite time over-current relays.

UNIT-III:

Static Differential Relays: Analysis of Static Differential Relays –Static Relay schemes –Duo bias transformer differential protection –Harmonic restraint relay.

Static Distance Relays: Static impedance-reactance–MHO and angle impedance relay-sampling comparator –realization of reactance and MHO relay using sampling comparator.

UNIT-IV:

Multi-Input Comparators: Conic section characteristics-Three input amplitude comparator –Hybrid comparator-switched distance schemes –Poly phase distance schemes- phase fault scheme –three phase scheme – combined and ground fault scheme.

Power Swings: Effect of power swings on the performance of distance relays –Power swing analysis-Principle of out of step tripping and blocking relays-effect of line and length and source impedance on distance relays.

UNIT-V:

Microprocessor based Protective Relays: (Block diagram and flowchart approach only)-Over current relays-impedance relays-directional relay-reactance relay .Generalized mathematical expressions for distance relays-measurement of resistance and reactance –MHO and offset MHO relays-Realization of MHO characteristics- Realization of offset MHO characteristics -Basic principle of Digital computer relaying, Introduction to wide area control(qualitative).

TEXT BOOKS:

1. Badri Ram and D.N. Vishwakarma, "Power system protection and Switch gear ", TMH publication New Delhi 1995.
2. T.S. Madhava Rao , "Static relays", TMH publication, second edition 1989.

REFERENCE:

1. Protection and Switchgear, Bhavesh Bhalja, R. P. Mahesheari, Nilesh G. Chothani, Oxford University Press.
2. Electrical Power System Protection, C. Christopoulos and A. Wright, Springer International.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

**MODERN CONTROL THEORY
(Professional Core - III)**

Prerequisite: Control Systems

Course Objectives

- To explain the concepts of basic and modern control system for the real time analysis and design of control systems.
- To Explain and apply concepts of state variables analysis.
- To study and analyze non linear systems.
- To analyze the concept of stability of nonlinear systems and categorization.
- To apply the comprehensive knowledge of optimal theory for Control Systems.

Course Outcomes: Upon completion of this course, students should be able to:

- Apply the knowledge of basic and modern control system for the real time analysis and design of control systems.
- Understand the concepts of state variables analysis.
- Analyze the concept of stability of nonlinear systems and optimal control.

UNIT-I:

Mathematical Preliminaries: Fields, Vectors and Vector Spaces – Linear combinations and Bases – Linear Transformations and Matrices – Scalar Product and Norms – Eigen-values, Eigen Vectors and a Canonical form representation of Linear operators – The concept of state – State Equations for Dynamic systems – Time invariance and Linearity – Non-uniqueness of state model – State diagrams for Continuous-Time State models.

UNIT-II:

State Variable Analysis: Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and its properties. General concept of controllability – General concept of Observability – Controllability tests for Continuous-Time Invariant Systems – Observability tests for Continuous-Time Invariant Systems – Controllability and Observability of State Model in Jordan Canonical form – Controllability and Observability Canonical forms of State model.

UNIT-III:

Non Linear Systems: Introduction – Non Linear Systems - Types of Non-Linearities – Saturation – Dead-Zone - Backlash – Jump Phenomenon etc;– Singular Points – Introduction to Linearization of nonlinear systems, Properties of Non-Linear systems – Describing function–describing function analysis of nonlinear systems – Stability analysis of Non-Linear systems through describing functions. Introduction to phase-plane analysis, Method of Isoclines for Constructing Trajectories, singular points, phase-plane analysis of nonlinear control systems.

UNIT-IV:

Stability Analysis: Stability in the sense of Lyapunov, Lyapunov's stability, and Lyapunov's instability theorems - Stability Analysis of the Linear continuous time invariant systems by Lyapunov second method – Generation of Lyapunov functions – Variable gradient method – Krasovskii's method. State feedback controller design through Pole Assignment – State observers: Full order and Reduced order.

UNIT-V:

Optimal Control: Introduction to optimal control - Formulation of optimal control problems – calculus of variations – fundamental concepts, functional, variation of functional – fundamental theorem of theorem of Calculus of variations – boundary conditions – constrained minimization – formulation using Hamiltonian method – Linear Quadratic regulator.

TEXT BOOKS:

1. Modern Control System Theory by M. Gopal – new age international -1984
2. Control System Engineering, Nagrath and Gopal - New Age International – Fourth Edition

REFERENCES:

1. Optimal control by Kirck , Dover Publications
2. Advanced Control Theory A. NagoorKani, RBA Publications, 1999
3. Modern Control Engineering by Ogata.K – Prentice Hall - 1997

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

**EHV AC TRANSMISSION
(Professional Elective - I)**

Prerequisite: Power Systems -II

Course objectives:

- To identify the different aspects of Extra High Voltage A.C and D.C Transmission design and Analysis
- To understand the importance of modern developments of E.H.V and U.H.V transmission systems.
- To demonstrate EHV ac transmission system components, protection and insulation level for over voltages.

Course Outcomes: Upon the completion of the subject, the student will be able to

- List the necessity of EHV AC transmission, choice of voltage for transmission, line losses and power handling capability.
- Estimate the Statistical procedures for line designs, scientific and engineering principles in power systems.
- Construct commercial transmission system.

UNIT- I:

E.H.V.A.C. Transmission line trends and preliminary aspect standard transmission voltages – Estimation at line and ground parameters-Bundle conductor systems-Inductance and Capacitance of E.H.V. lines – positive, negative and zero sequence impedance – Line Parameters for Modes of Propagation.

UNIT- II:

Electrostatic field and voltage gradients – calculations of electrostatic field of AC lines – effect of high electrostatic field on biological organisms and human beings - surface voltage gradients and maximum gradients of actual transmission lines – voltage gradients on sub conductor.

UNIT- III:

Electrostatic induction in unenergized lines – measurement of field and voltage gradients for three phase single and double circuit lines – un energized lines. Power Frequency Voltage control and over-voltages in EHV lines: No load voltage – charging currents at power frequency-voltage control – shunt and series compensation – static VAR compensation.

UNIT - IV:

Corona in E.H.V. lines – Corona loss formulae- attenuation of traveling waves due to Corona – Audio noise due to Corona, its generation, characteristic and limits. Measurements of audio noise radio interference due to Corona - properties of radio noise – frequency spectrum of RI fields – Measurements of RI and RIV.

UNIT- V:

Design of EHV lines based on steady state and transient limits - EHV cables and their characteristics.

TEXT BOOKS:

1. R. D. Begamudre, "EHVAC Transmission Engineering", New Age International (p) Ltd. 3rd Edition.
2. K. R. Padiyar, "HVDC Power Transmission Systems" New Age International (p) Ltd. 2nd revised Edition, 2012.

REFERENCES:

1. S. Rao "EHVAC and HVDC Transmission Engg. Practice" Khanna publishers.
2. Arrillaga. J "High Voltage Direct Current Transmission" 2nd Edition (London) peter Peregrines, IEE, 1998.
3. Padiyar. K.R, "FACTS Controllers in Power Transmission and Distribution" New Age Int. Publishers, 2007.
4. Hingorani H G and Gyugyi. L "Understanding FACTS-Concepts and Technology of Flexible AC Transmission Systems" New York, IEEE Press, 2000.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

**HIGH VOLTAGE ENGINEERING
(Professional Elective - I)**

Prerequisite: Power Systems and Electrical & Electronics Instrumentation

Course Objectives:

- To distinguish the Gaseous, liquid and solid dielectric behavior under High Voltage.
- To understand the generation methods of High A.C, DC & Impulse Voltages required for various application.
- To apply the measuring techniques of High A.C., D.C & Impulse voltages and currents.
- To identify the testing techniques for High Voltage Equipment.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Know conduction and breakdown will occur in gases, liquids and solids dielectrics, and different applications of the insulating materials in electrical power apparatus.
- Explain the insulation testing of various components in power systems for different types of voltages, namely power frequency A.C, high frequency, switching or lightning impulses, for which generation of high voltages in laboratories is essential
- Interpret the necessity to measure the voltages and currents accurately, ensuring perfect safety to the personnel and equipment.
- Detect the necessary condition for all the electrical equipment which are capable of withstanding the over voltages which met in service like natural causes lightning or system originated ones switching or power frequency transient voltages.

UNIT- I:

Introduction To High Voltage Engineering: Electric Field Stresses, Gas / Vacuum as Insulator, Liquid Dielectrics, Solids and Composites, Estimation and Control of Electric Stress, Numerical methods for electric field computation, Surge voltages, their distribution and control, Applications of insulating materials in transformers, rotating machines, circuit breakers, cable power capacitors and bushings.

UNIT- II:

Break Down In Dielectric Materials: Gases as insulating media, collision process, Ionization process, Townsend's criteria of breakdown in gases, Paschen's law. Liquid as Insulator, pure and commercial liquids, breakdown in pure and commercial liquids. Intrinsic breakdown, electromechanical breakdown, thermal breakdown, breakdown of solid dielectrics in practice, Breakdown in composite dielectrics, solid dielectrics used in practice.

UNIT-III:

Generation & Measurement of High Voltages & Currents : Generation of High Direct Current Voltages, Generation of High alternating voltages, Generation of Impulse Voltages, Generation of Impulse currents, Tripping and control of impulse generators. Measurement of High Direct Current voltages, Measurement of High Voltages alternating and impulse, Measurement of High Currents-direct, alternating and Impulse, Oscilloscope for impulse voltage and current measurements.

UNIT-IV:

Over Voltages & Insulation Co-Ordination: Natural causes for over voltages – Lightning phenomenon, Overvoltage due to switching surges, system faults and other abnormal conditions, Principles of Insulation Coordination on High voltage and Extra High Voltage power systems.

UNIT- V:

Testing of Materials & Electrical Apparatus: Measurement of D.C Resistivity, Measurement of Dielectric Constant and loss factor, Partial discharge measurements. Testing of Insulators and bushings, Testing of Isolators and circuit breakers, testing of cables, Testing of Transformers, Testing of Surge Arresters, and Radio Interference measurements.

TEXT BOOKS:

1. High Voltage Engineering by M.S. Naidu and V. Kamaraju – TMH Publications, 3rd Edition
2. High Voltage Engineering: Fundamentals by E. Kuffel, W.S. Zaengl, J.Kuffel by Elsevier, 2nd Edition.

REFERENCES:

1. High Voltage Engineering by C.L. Wadhwa, New Age International (P) Limited, 1997.
2. High Voltage Insulation Engineering by Ravindra Arora, Wolfgang Mosch, New Age International (P) Limited, 1995.
3. High Voltage Engineering, Theory and Practice by Mazen Abdel Salam, Hussein Anis, Ahdan El-Morshedy, Roshdy Radwan , Marcel Dekker

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (EPE/EPS /PSC&A)

**ADVANCED DIGITAL SIGNAL PROCESSING
(Professional Elective - I)**

Prerequisite: Digital Signal Processing

Course Learning Objectives

- To Comprehend characteristics of discrete time signals and systems
- To analyze and process signals using various transform techniques
- To identify various factors involved in design of digital filters
- To illustrate the effects of finite word length implementation.

Course Outcomes

- Analyze and process signals in the discrete domain
- Design filters to suit specific requirements for specific applications
- Perform statistical analysis and inferences on various types of signals
- Design multi rate signal processing of signals through systems.
- Analyze binary fixed point and floating-point representation of numbers and arithmetic operations

UNIT-I:

Digital Filter Structures: Block diagram representation – Equivalent Structures – FIR and IIR digital filter Structures All pass Filters-tunable IIR Digital Sine-cosine generator- Computational complexity of digital filter structures.

UNIT-II:

Digital Filter Design: Preliminary considerations- Bilinear transformation method of IIR filter design – design of Low pass high-pass – Band-pass, and Band stop- IIR digital filters – Spectral transformations of IIR filters – FIR filter design –based on Windowed Fourier series – design of FIR digital filters with least – mean square-error – constrained Least –square design of FIR digital filters.

UNIT-III:

DSP Algorithm Implémentation : Computation of the discrete Fourier transform- Number representation – Arithmetic operations – handling of overflow – Tunable digital filters – function approximation.

UNIT-IV:

Analysis Of Finite Word Length Effects: The Quantization process and errors-Quantization of fixed –point and floating –point Numbers – Analysis of coefficient Quantization effects – Analysis of Arithmetic Round-off errors- Dynamic range scaling – signal –to- noise in Low –order IIR filters- Low – Sensitivity Digital filter – Reduction of Product round-off errors feedback – Limit cycles in IIR digital filter – Round – off errors in FFT Algorithms.

UNIT-V:

Power Spectrum Estimation: Estimation of spectra from Finite Duration Observations signals- Non-parametric methods for power spectrum Estimation- parametric method for power spectrum Estimation- Estimation of spectral form-Finite duration observation of signals- Non-parametric methods for power spectrum estimation – Walsh methods – Blackman and torchy method.

TEXT BOOKS:

1. Digital Signal Processing principles –algorithms and Applications- john G. Proakis –PHI – 3rd edition 2002.
2. Digital Time Signal Processing: Alan V. Oppenheim, Ronald W ,Shafer – PHI 1996 1st Edition reprint
3. Advanced Digital Signal Processing – Theory and Applications – Glenn Zelniker, Fred J. Taylor.

REFERENCE BOOKS

1. Digital Signal Processing – S Salivahanan. A Vallavaraj C. Gnanapriya –TMH – 2nd reprint 2001.
2. Digital Signal Processing – Sanjit K. Mitra – TMH second edition.
3. Theory and Applications of Digital Signal Processing – Lourens R Rebinarand Bernold.
4. Digital Filter Analysis and Design Auntoniam - TMH
5. Digital Signal Processing – J.S. Chitode – First Edition, 2008, Technical Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

**POWER QUALITY
(Professional Elective - II)**

Prerequisite: Power Systems and Power Electronics

Course Objectives

- To know different terms of power quality.
- To Illustrate of voltage power quality issue - short and long interruption
- To construct study of characterization of voltage sag magnitude and three phase unbalanced voltage sag.
- To know the behavior of power electronics loads; induction motors, synchronous motor etc by the power quality issues
- To prepare mitigation of power quality issues by the VSI converters.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Know the severity of power quality problems in distribution system;
- Understand the concept of voltage sag transformation from up-stream (higher voltages) to down-stream (lower voltage)
- compute the concept of improving the power quality to sensitive load by various mitigating custom power devices

Unit-I:

Introduction : Introduction of the Power Quality (PQ) problem, Terms used in PQ: Voltage, Sag, Swell, Surges, Harmonics, over voltages, spikes, Voltage fluctuations, Transients, Interruption, overview of power quality phenomenon, Remedies to improve power quality, power quality monitoring.

Unit-II:

Long & Short Interruptions: Interruptions – Definition – Difference between failures, outage, Interruptions – causes of Long Interruptions – Origin of Interruptions – Limits for the Interruption frequency – Limits for the interruption duration – costs of Interruption – Overview of Reliability evaluation to power quality, comparison of observations and reliability evaluation.

Short interruptions: definition, origin of short interruptions, basic principle, fuse saving, voltage magnitude events due to re-closing, voltage during the interruption, monitoring of short interruptions, difference between medium and low voltage systems. Multiple events, single phase tripping – voltage and current during fault period, voltage and current at post fault period, stochastic prediction of short interruptions.

Unit III:

1 & 3-Phase Voltage SAG Characterization: Voltage sag – definition, causes of voltage sag, voltage sag magnitude, and monitoring, theoretical calculation of voltage sag magnitude, voltage sag calculation in non-radial systems, meshed systems, and voltage sag duration. Three phase faults, phase angle jumps, magnitude and phase angle jumps for three phase unbalanced sags, load influence on voltage sags.

Unit-IV:

Power Quality Considerations in Industrial Power Systems: Voltage sag – equipment behavior of Power electronic loads, induction motors, synchronous motors, computers, consumer electronics,

adjustable speed AC drives, and its operation. Mitigation of AC Drives, adjustable speed DC drives and its operation, mitigation methods of DC drives.

Unit-V:

Mitigation of Interruptions & Voltage Sags: Overview of mitigation methods – from fault to trip, reducing the number of faults, reducing the fault clearing time changing the power system, installing mitigation equipment, improving equipment immunity, different events, and mitigation methods. System equipment interface – voltage source converter, series voltage controller, shunt controller, combined shunt and series controller.

Power Quality and EMC Standards: Introduction to standardization, IEC Electromagnetic compatibility standards, European voltage characteristics standards, PQ surveys.

TEXTBOOKS:

1. Math H J Bollen "Understanding Power Quality Problems", IEEE Press.
2. R. C. Dugan, M.F. Mc Granaghan and H. W. Beaty, "Electric Power Systems Quality." New York: McGraw-Hill. 1996

REFERENCES:

1. G. T. Heydt, 'Electric Power Quality', 2nd Edition. (West Lafayette, IN, Stars in a Circle Publications, 1994).
2. Power Quality VAR Compensation in Power Systems, R. Sastry Vedam Mulukutla S. Sarma, CRC Press.
3. A Ghosh, G. Ledwich, Power Quality Enhancement Using Custom Power Devices. Kluwer Academic, 2002

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

MICROCONTROLLERS AND APPLICATIONS

(Professional Elective - II)

Prerequisite: Microprocessors and Interfacing Devices

Course Objectives:

- To relate the basic architecture and addressing modes of a microcontroller.
- To summarize the principles of top down design to microcontroller software development
- To demonstrate assembly language programs for the advanced Microcontroller , assembly language code for high-level language structures such as IF-THEN-ELSE and DO-WHILE
- To analyze a typical I/O interface and to discuss timing issues
- To identify different types of memory used in microcontroller systems

Course Outcomes: Upon the completion of the subject, the student will be able to

- Distinguish Types of computers & microcontrollers,
- Generalize 8-Bit, 16- Bit & 32 Bit advanced Microcontrollers.
- Construct Real time Applications of Microcontrollers.
- Demonstrate RTOS for Microcontrollers.
- Translate Hardware applications using Microcontrollers.

UNIT-I:

Overview of Architecture & Microcontroller Resources: Architecture of a microcontroller – Microcontroller resources – Resources in advanced and next generation microcontrollers – 8051 microcontroller – Internal and External memories – Counters and Timers – Synchronous serial-cum asynchronous serial communication - Interrupts.

UNIT-II:

8051- Microcontrollers Instruction Set : Basic assembly language programming – Data transfer instructions – Data and Bit-manipulation instructions – Arithmetic instructions – Instructions for Logical operations on the test among the Registers, Internal RAM, and SFRs – Program flow control instructions – Interrupt control flow.

UNIT-III:

Real Time Control: Interrupts: Interrupt handling structure of an MCU – Interrupt Latency and Interrupt deadline – Multiple sources of the interrupts – Non-maskable interrupt sources – Enabling or disabling of the sources – Polling to determine the interrupt source and assignment of the priorities among them – Interrupt structure in Intel 8051.

Timers: Programmable Timers in the MCU's – Free running counter and real time control – Interrupt interval and density constraints.

UNIT-IV:

Systems Design: Digital and Analog Interfacing Methods: Switch, Keypad and Keyboard interfacing – LED and Array of LEDs – Keyboard-cum-Display controller (8279) – Alphanumeric Devices – Display Systems and its interfaces – Printer interfaces – Programmable instruments interface using IEEE 488 Bus – Interfacing with the Flash Memory – Interfaces – Interfacing to High Power Devices – Analog input interfacing – Analog output interfacing – Optical motor shaft encoders – Industrial control – Industrial process control system – Prototype MCU based Measuring instruments – Robotics and Embedded control – Digital Signal Processing and digital filters.

UNIT-V:

Real Time Operating System for Microcontrollers: Real Time operating system – RTOS of Keil (RTX51) – Use of RTOS in Design – Software development tools for Microcontrollers.

16-Bit Microcontrollers: Hardware – Memory map in Intel 80196 family MCU system – IO ports – Programmable Timers and High-speed outputs and input captures – Interrupts – instructions. ARM 32 Bit MCUs: Introduction to 16/32 Bit processors – ARM architecture and organization – ARM / Thumb programming model – ARM / Thumb instruction set – Development tools.

TEXT BOOKS:

1. Raj Kamal," Microcontrollers Architecture, Programming, Interfacing and System Design"– Pearson Education, 2005.
2. Mazidi and Mazidi, "The 8051 Microcontroller and Embedded Systems" – PHI, 2000.

REFERENCES:

1. A. V. Deshmuk, "Microcontrollers (Theory & Applications)" – WTMH, 2005.
2. John B. Peatman, "Design with PIC Microcontrollers" – Pearson Education, 2005.
3. Microcontroller Programming, Julio Sanchez, Maria P. Canton, CRC Press.
4. The 8051 Microcontroller, Ayala, Cengage Learning.
5. Microprocessors and Microcontrollers, Architecture, Programming and System Design, Krishna Kant, PHI Learning PVT. Ltd.
6. Microprocessors, Nilesh B. Bahadure, PHI Learning PVT. Ltd.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC&A)

**DISTRIBUTION AUTOMATION
(Professional Elective - II)**

Prerequisite: Electrical Distribution Systems

Course objectives:

- To list the distribution systems for load modeling
- To understand the design & working of substations.
- To compute system protection
- To give a comprehensive idea on communication systems.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Find the transfer of electrical data in distribution system through Digital Communication.
- Predict load forecasting and reliability in economic point of view
- Apply Distribution Automation objectives and SCADA
- To have a knowledge on management of different electrical parameters.

Unit-I:

Distribution Automation and The Utility System: Introduction to Distribution Automation (DA), control system interfaces, control and data requirements, centralized (Vs) decentralized control, DA System (DAS), DA Hardware, DAS software.

Unit-II:

Distribution Automation Functions: DA capabilities, Automation system computer facilities, management processes, Information management, system reliability management, system efficiency management, voltage management, Load management.

Unit-III:

Communication Systems for DA: DA communication requirements, Communication reliability, Cost effectiveness, Data rate Requirements, Two way capability, Ability to communicate during outages and faults, Ease of operation and maintenance, Conforming to the architecture of data flow

Communication systems used in DA : Distribution line carrier (Power line carrier), Ripple control, Zero crossing technique, telephone, cable TV, Radio, AM broadcast, FM SCA, VHF Radio, UHF Radio, Microwave satellite. Fiber optics, Hybrid Communication systems, Communication systems used in field tests.

Unit-IV:

Technical Benefits: DA benefit categories, Capital deferred savings, Operation and Maintenance savings, Interruption related savings, Customer related savings, Operational savings, improved operation, Function benefits, Potential benefits for functions, and function shared benefits, Guidelines for formulation of estimating equations Parameters required, economic impact areas, Resources for determining benefits impact on distribution system, integration of benefits into economic evaluation.

Unit-V:

Economic Evaluation Methods: Development and evaluation of alternate plans, Select study area, Select study period, Project load growth, Develop Alternatives, Calculate operating and maintenance costs, Evaluate alternatives. Economic comparison of alternate plans, Classification of expenses and capital expenditures, Comparison of revenue requirements of alternative plans, Book Life and

Continuing plant analysis, Year by year revenue requirement analysis, short term analysis, end of study adjustment, Break even analysis, Sensitivity analysis computational aids.

TEXT BOOKS:

1. Control and Automation of Electrical Distribution Systems, James. Northcote – Green Robert Wilson, CRC Press.
2. Electric Power Distribution Automation, Dr. M. K. Khedkar, Dr. G.M.Dhole, University Science press.
3. Power Distribution Automation, Biswarup Das-IET Power and Energy Series 75.

REFERENCES:

1. IEEE Tutorial Course “Distribution Automation”
2. IEEE Working Group on “Distribution Automation”

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PSC & A)

POWER & ENERGY SYSTEMS LAB – I

Prerequisites: Power System Analysis, Power System Reliability, Voltage Stability

Course Objectives:

- Develop Programs for Power System Analysis.
- Design models for Power Systems and Power Electronics.
- Develop Programs of Power System Reliability and Power Electronics.

Course outcomes: Upon the completion of the lab, the student will be able to Understand/Simulate/Analyze

- Power System Analysis using Software.
- Models of Power Systems and Power Electronics.
- Programs of Power System Reliability and Power Electronics.

List of Experiments

1. Develop Program for Y_{BUS} formation.
2. Develop Program for G-S Load Flow Analysis.
3. Develop Program for N-R Load Flow Analysis.
4. Develop Program for FDLF Load Flow Analysis.
5. Develop Program for Short Circuit Analysis.
6. Develop Program for Transient Stability Analysis for Single Machine connected to Infinite Bus by Point by Point Method.
7. Develop Program for Generation System Reliability Analysis.
8. Develop Program for Distribution System Reliability Analysis.
9. Develop Simulation of RLC Circuit
10. Develop Simulation of Single Phase Full Converter with RLE Load
11. Develop Program model for Closed Loop Speed Control of Separately Excited D.C Motor.
12. Develop Program model for Sinusoidal Pulse Width Modulation.

Note: From the above list minimum 10 experiments are to be conducted using suitable software.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH (POWER ELECTRONICS/ POWER AND INDUSTRIAL DRIVES/
POWER ELECTRONICS AND ELECTRIC DRIVES)**

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Machine Modeling and Analysis	25	75	4	0	0	4
PC-2	Modern Control Theory	25	75	4	0	0	4
PC-3	Power Electronic Devices and Converters	25	75	4	0	0	4
PE-1	1. Special Machines 2. High Frequency Magnetic Components 3. Programmable Logic Controllers and Applications	25	75	3	0	0	3
PE-2	1. Electric Traction systems 2. Advanced Digital Signal Processing 3. Digital Control Systems	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Power Converters Simulation Lab	25	75	0	0	3	2
Seminar I	Seminar - I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Power Electronic Applications to Renewable Energy	25	75	4	0	0	4
PC-5	Embedded Systems for Power Electronic Applications	25	75	4	0	0	4
PC-6	Power Electronic Control of Drives	25	75	4	0	0	4
PE-3	1. HVDC & FACTS 2. Switched Mode Power Supplies (SMPS) 3. AI Techniques in Electrical Engineering	25	75	3	0	0	3
PE4	1. Dynamics of Electrical Machines 2. Hybrid Electric Vehicles 3. Smart Grid Technologies	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Power Converters and Drives Lab	25	75	0	0	3	2
Seminar II	Seminar -II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

MACHINE MODELLING AND ANALYSIS
(Professional core - I)

Prerequisites: Electrical Machines (DC and AC), control theory

Course Objectives:

- To comprehend the basic two-pole machine.
- To identify the methods and assumptions in modeling of machines.
- To write voltage and torque equations for different machines.
- To recognize the different frames for modeling of different AC machines.
- To express the voltage and torque equations in State space form

Course Outcomes: Upon the completion of the course the student will be able to

- Write the voltage equation and torque equations for different machines like dc machine, induction motor and Synchronous machines.
- Model different machines using phase and Active transformations.
- Identify the different reference frames for modeling of machines.

Unit-I:

Basic Two-pole DC machine - primitive 2-axis machine – Voltage and Current relationship – Torque equation.

Unit-II:

Mathematical model of separately excited DC motor and DC Series motor in state variable form – Transfer function of the motor - Numerical problems.

Mathematical model of D.C. shunt motor D.C. Compound motor in state variable form – Transfer function of the motor - Numerical Problems

Unit-III:

Liner transformation – Phase transformation (a, b, c to α, β, o) – Active transformation (α, β, o to d, q). Circuit model of a 3 phase Induction motor – Linear transformation - Phase Transformation – Transformation to a Reference frame – Two axis models for induction motor. dq model based DOL starting of Induction Motors.

Unit-IV:

Voltage and current Equations in stator reference frame – equation in Rotor reference frame – equations in a synchronously rotating frame – Torque equation - Equations I state – space form.

Unit-V:

Circuits model of a 3ph Synchronous motor – Two axis representation of Syn. Motor.

Voltage and current Equations in state – space variable form – Torque equation.

dq model based short circuit fault analysis- emphasis on voltage, frequency and recovery time.

TEXT BOOKS:

1. Analysis of electric machinery and Drive systems- Paul C. Krause , Oleg Waszynezuk, Scott D. Sudhoff, third edition, IEEE press
2. Generalized Machine theory P.S. Bimbhra, Khanna Publishers, 2002

REFERENCES:

1. Thyristor control of Electric Drives - Vedam Subramanyam, Tata McGraw-Hill Education, 1988
2. Power System Stability and Control – Prabha Kundur, EPRI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

**MODERN CONTROL THEORY
(Professional core - II)**

Prerequisites: Linear control systems

Course Objectives:

- To explain the concepts of basic and modern control system for the real time analysis and design of control systems.
- To explain and apply concepts of state variables analysis.
- To study and analyze non linear systems.
- To analyze the concept of stability of nonlinear systems and categorization.
- To apply the comprehensive knowledge of optimal theory for Control Systems.

Course Outcomes: Upon the completion of the course the student will be able to

- Understand the concepts of state variable analysis
- Apply the knowledge of basic and modern control system for the real time analysis and design of control systems.
- Analyze the concept of stability of nonlinear systems and optimal control

UNIT-I:

Mathematical Preliminaries: Fields, Vectors and Vector Spaces – Linear combinations and Bases – Linear Transformations and Matrices – Scalar Product and Norms – Eigen-values, Eigen Vectors and a Canonical form representation of Linear operators – The concept of state – State Equations for Dynamic systems – Time invariance and Linearity – Non-uniqueness of state model – State diagrams for Continuous-Time State models.

UNIT-II:

State Variable Analysis: Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and its properties. General concept of controllability – General concept of Observability – Controllability tests for Continuous-Time Invariant Systems – Observability tests for Continuous-Time Invariant Systems – Controllability and Observability of State Model in Jordan Canonical form – Controllability and Observability Canonical forms of State model.

UNIT-III:

Non Linear Systems: Introduction – Non Linear Systems - Types of Non-Linearities – Saturation – Dead-Zone - Backlash – Jump Phenomenon etc;– Singular Points – Introduction to Linearization of nonlinear systems, Properties of Non-Linear systems – Describing function–describing function analysis of nonlinear systems – Stability analysis of Non-Linear systems through describing functions. Introduction to phase-plane analysis, Method of Isoclines for Constructing Trajectories, singular points, phase-plane analysis of nonlinear control systems.

UNIT-IV:

Stability Analysis: Stability in the sense of Lyapunov, Lyapunov's stability, and Lyapunov's instability theorems - Stability Analysis of the Linear continuous time invariant systems by Lyapunov second method – Generation of Lyapunov functions – Variable gradient method – Krasovskii's method.

State feedback controller design through Pole Assignment – State observers: Full order and Reduced order.

UNIT-V:

Optimal Control: Introduction to optimal control - Formulation of optimal control problems – calculus of variations – fundamental concepts, functional, variation of functional – fundamental theorem of theorem of Calculus of variations – boundary conditions – constrained minimization – formulation using Hamiltonian method – Linear Quadratic regulator.

TEXT BOOKS:

1. Modern Control System Theory by M.Gopal – New Age International -1984
2. Control System Engineering, Nagrath and Gopal - New Age International – Fourth Edition

REFERENCES:

1. Optimal control by Kirck , Dover Publications
2. Advanced Control Theory A. Nagoor Kani, RBA Publications, 1999
3. Modern Control Engineering by Ogata. K – Prentice Hall - 1997

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

POWER ELECTRONIC DEVICES AND CONVERTERS
(Professional core - III)

Course Objectives:

- To understand the characteristics and principle of operation of modern power semi conductor devices.
- To analyze and design switched mode regulator for various industrial applications.
- To analyze different power converters and know their applications

Course Outcomes: Upon the completion of the course the student will be able

- To choose appropriate device for a particular converter topology.
- To analyze and design various power converters and controllers

UNIT-I:

Modern Power Semiconductor Devices: Modern power semiconductor devices – MOS turn Off Thyristor (MTO) – Emitter Turn off Thyristor (ETO) – Integrated Gate-Commutated thyristor (IGCTs) – MOS-controlled Thyristors (MCTs) – Insulated Gate Bipolar Transistor (IGBT) – MOSFET – comparison of their features.

Unit-II:

D.C. to D.C. Converters: Analysis of step – down and step-up dc to dc converters with resistive and Resistive –inductive loads – Switched mode regulators – Analysis of Buck Regulators – Boost regulators – buck and boost regulators – Cuk regulators – Condition for Continuous inductor current and capacitor voltage – comparison of regulators – Multi-output boost converters – Advantages - Applications.

Unit-III:

PWM Techniques: single PWM – Multiple PWM – sinusoidal PWM – modified PWM – phase displacement Control – Advanced modulation techniques for improved performance – Trapezoidal, staircase, stepped, harmonic injection and delta modulations – Advantage – application. Third Harmonic PWM – 60 degree PWM – space vector modulation – Comparison of PWM techniques – harmonic reductions.

UNIT-IV:

Multilevel Inverters: Two level voltage source inverter - Multilevel concept – Classification of multilevel inverters – Diode clamped multilevel inverter – principle of operation – main features – improved diode Clamped inverter – principle of operation – Flying capacitors multilevel inverter – principle of operation – main features. Cascaded multilevel inverter – principle of operation – main features – Multilevel inverter applications – reactive power compensation – back to back intertie system – adjustable drives – Switching device currents – de link capacitor voltage balancing – features of Multilevel inverters – comparisons of multilevel converters.

UNIT-V:

Resonant Pulse Inverters: Resonant pulse inverters – series resonant inverters – series resonant inverters with unidirectional switches – series resonant inverters with bidirectional Switches – analysis of half bridge resonant inverter - evaluation of currents and Voltages of a simple resonant inverter – analysis of half bridge and full bridge resonant inverter with bidirectional switches – Frequency response of series resonant inverters – for series loaded inverter – for parallel loaded inverter – For

series and parallel loaded inverters – parallel resonant inverters – Voltage control of resonant inverters.

Resonant converters: Resonant converters – Zero current switching resonant converters – L type ZCS resonant converter – M type ZCS resonant converter – zero voltage switching resonant converters – comparison between ZCS and ZVS resonant Converters – Two quadrant ZVS resonant converters – resonant de-link Inverters – evaluation of L and C for a zero current switching inverter.

TEXT BOOKS:

1. Power Electronics – Mohammed H. Rashid – Pearson Education Third Edition – First Indian reprint 2004.
2. Power Electronics – Ned Mohan, Tore M. Undeland and William P. Robbins – John Wiley and Sons – Second Edition.

REFERENCE BOOKS:

1. Power Electronics – Daniel W. Hart, McGraw Hill Publications.
2. Power Electronics Devices, Circuits and Industrial applications, V. R. Moorthi, Oxford University Press
3. Power Electronics, Dr. P. S. Bimbhra, Khanna Publishers.
4. Elements of Power Electronics, Philip T. Krein, Oxford University Press.
5. Power Electronics, M. S. Jamil Asghar, PHI Private Limited.
6. Principles of Power Electronics, John G. Kassakian, Martin F. Schlect, Geroge C. Verghese, Pearson Education.
7. Fundamentals of Power Electronics, Robert W. Erickson, Dragan and Maksimovic, Springer.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

**SPECIAL MACHINES
(Professional Elective – I)**

Course objectives:

- To understand the working and construction of special machines
- To know the use of special machines in different feed-back systems
- To understand the use of digital controllers for different machines

Course Outcomes: Upon the completion of this subject, the student will be able

- To understand the operation of different special machines
- To select different special machines as part of control system components
- To use special machines as transducers for converting physical signals into electrical signals
- To design digital controllers for different machines

UNIT-I:

Stepper Motors: Introduction-synchronous inductor (or hybrid stepper motor), Hybrid stepping motor, construction, principles of operation, energization with two phase at a time- essential conditions for the satisfactory operation of a 2-phase hybrid step motor - very slow - speed synchronous motor for servo control-different configurations for switching the phase windings-control circuits for stepping motors-an open-loop controller for a 2-phase stepping motor.

UNIT-II:

Variable Reluctance Stepping Motors: Variable reluctance (VR) Stepping motors, single-stack VR step motors, Multiple stack VR motors-Open-loop control of 3-phase VR step motor-closed-Loop control of step motor, discriminator (or rotor position sensor) transilator, major loop-characteristics of step motor in open-loop drive – comparison between open-loop position control with step motor and a position control servo using a conventional (dc or ac) servo motor- Suitability and areas of application of stepping motors-5- phase hybrid stepping motor - single phase - stepping motor, the construction, operating principle torque developed in the motor.

Switched Reluctance Motor: Introduction – improvements in the design of conventional reluctance motors- Some distinctive differences between SR and conventional reluctance motors-principle of operation of SRM- Some design aspects of stator and rotor pole arcs, design of stator and rotor and pole arcs in SR motor-determination of $L(\theta)$ - θ profile - power converter for SR motor-A numerical example –Rotor sensing mechanism and logic control, drive and power circuits, position sensing of rotor with Hall problems-derivation of torque expression, general linear case.

UNIT-III:

Permanent Magnet Materials and PM DC Machines: Introduction, Hysteresis loops and recoil line-stator frames (pole and yoke - part) of conventional PM dc Motors, Equivalent circuit of PM Generator and Motor-Development of Electronically commutated dc motor from conventional dc motor.

Brushless DC Motor: Types of construction – principle of operation of BLDM- sensing and switching logic scheme, sensing logic controller, lockout pulses –drive and power circuits, Base drive circuits, power converter circuit-Theoretical analysis and performance prediction, modeling and magnet circuit d-q analysis of BLDM -transient analysis formulation in terms of flux linkages as state variables-Approximate solution for current and torque under steady state –Theory of BLDM as variable speed synchronous motor (assuming sinusoidal flux distribution)- Methods or reducing Torque Pulsations, 180 degrees pole arc and 120 degree current sheet.

UNIT-IV:

Linear Induction Motor: Development of a double sided LIM from rotary type IM- A schematic of LIM drive for electric traction development of one sided LIM with back iron-field analysis of a DSLIM fundamental assumptions.

UNIT-V:

Permanent Magnet Axial Flux (Pmaf) Machines: Construction, Armature windings – Toroidal Stator and Trapezoidal Stator Windings, Torque and EMF equations, Phasor diagram and output equation.

TEXT BOOKS:

1. Special electrical machines, K. Venkataratnam, - University press.
2. Special electrical machines, E. G. Janardanan, - PHI.
3. R. K. Rajput, "Electrical machines"-5th edition.
4. V. V. Athani, "Stepper motor: Fundamentals, Applications and Design"- New age International pub.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

**HIGH FREQUENCY MAGNETIC COMPONENTS
(Professional Elective – I)**

Course objectives:

- To understand the fundamentals of magnetic devices
- To know the skin and proximity effects in windings
- To design high frequency transformers
- To analyze and design the various components of converters

Course Outcomes: Upon the completion of this subject, the student will be able

- To understand the operation of magnetic devices
- To appreciate the skin and proximity effects in various windings
- To analyze and design the components in power electronic converters
- To design transformers of High frequency used in converters

Unit-I:

Fundamentals of Magnetic Devices: Introduction, Magnetic Relationships, Magnetic Circuits, Magnetic Laws, Eddy Currents, Core Saturation, Volt-Second Balance, Inductance, Inductance Factor, Magnetic Energy, Self-Resonant Frequency, Classification of Power Losses in Magnetic Components, Non-inductive Coils.

Magnetic Cores: Introduction, Properties of Core Materials, Magnetic Dipoles, Magnetic Domains, Curie Temperature, Magnetization, Magnetic Materials, Hysteresis, Core Permeability, Core Geometries, Iron Alloy Cores, Amorphous Alloy Cores, Nickel–Iron and Cobalt–Iron Cores, Ferrite Cores, Powder Cores, Nano-crystalline Cores, Superconductors, Hysteresis Core Loss, Eddy-Current Core Loss, Total Core Loss, Complex Permeability.

Unit-II:

Skin Effect & Proximity Effect: Introduction, Magnet Wire, Wire Insulation, Skin Depth, Ratio of AC-to-DC Winding Resistance, Skin Effect in Long Single Round Conductor, Current Density in Single Round Conductor, Impedance of Round Conductor, Magnetic Field Intensity for Round Wire, Other Methods of Determining the Round Wire Inductance, Power Density in Round Conductor, Skin Effect on Single Rectangular Plate. Proximity and Skin Effects in Two Parallel Plates, Anti-proximity and Skin Effects in Two Parallel Plates, Proximity Effect in Multiple-Layer Inductor, Appendix: Derivation of Proximity Power Loss.

Winding Resistance at High Frequencies: Introduction, Winding Resistance, Square and Round Conductors, Winding Resistance of Rectangular Conductor, Winding Resistance of Square Wire, Winding Resistance of Round Wire, Leakage Inductance, Solution for Round Conductor Winding in Cylindrical Coordinates, Litz Wire, Winding Power Loss for Inductor Current with Harmonics, Effective Winding Resistance for Non-sinusoidal Inductor Current, Thermal Model of Inductors.

Unit-III:

Transformers: Introduction, Neumann's Formula for Mutual Inductance, Mutual Inductance, Energy Stored in Coupled Inductors, Magnetizing Inductance, Leakage Inductance, Measurement of Transformer Inductances, Stray Capacitance, High-Frequency Transformer Model, Non-interleaved Windings, Interleaved Windings, AC Current Transformers, Winding Power Losses with Harmonics, Thermal Model of Transformers.

Design of Transformers: Introduction, Area Product Method, Optimum Flux Density, Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM,

Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM.

Unit-IV:

Integrated Inductors: Introduction, Resistance of Rectangular Trace, Inductance of Straight Rectangular Trace, Construction of Integrated Inductors, Meander Inductors, Inductance of Straight Round Conductor, Inductance of Circular Round Wire Loop, Inductance of Two-Parallel Wire Loop, Inductance of Rectangle of Round Wire, Inductance of Polygon Round Wire Loop, Bond-wire Inductors, Single-Turn Planar Inductor, Inductance of Planar Square Loop, Planar Spiral Inductors, Multi-metal Spiral Inductors, Planar Transformers, MEMS Inductors, Inductance of Coaxial Cable, Inductance of Two-Wire Transmission Line, Eddy Currents in Integrated Inductors, Model of RF Integrated Inductors, PCB Inductors.

Design of Inductors: Introduction, Restrictions on Inductors, Window Utilization Factor, Temperature Rise of Inductors, Mean Turn Length of Inductors, Area Product Method, AC Inductor Design, Inductor Design for Buck Converter in CCM, Inductor Design for Buck Converter in DCM method.

Unit-V:

Self-Capacitance: Introduction, High-Frequency Inductor Model, Self-Capacitance Components, Capacitance of Parallel-Plate Capacitor, Self-Capacitance of Foil Winding Inductors, Capacitance of Two Parallel Round Conductors, Capacitance of Round Conductor and Conducting Plane, Self-Capacitance of Single-Layer Inductors, Self-Capacitance of Multi-layer Inductors, Capacitance of Coaxial Cable.

TEXT BOOK:

1. Design of Magnetic Components for Switched Mode Power Converters, Umanand L., Bhat, S.R., ISBN: 978-81-224-0339-8, Wiley Eastern Publication, 1992.

REFERENCES:

1. High-Frequency Magnetic Components, Marian K. Kazimierczuk, ISBN: 978-0-470-71453-9 John Wiley & Sons, Inc.
2. G. C. Chryssis, High frequency switching power supplies, McGraw Hill, 1989 (2nd Ed.)
3. Eric Lowdon, Practical Transformer Design Handbook, Howard W. Sams & Co., Inc., 1980
4. "Thompson --- Electrodynamic Magnetic Suspension.pdf"
5. Witulski --- "Introduction to modeling of transformers and coupled inductors" Beattie --- "Inductance 101.pdf"
6. P. L. Dowell, "Effects of eddy currents in transformer windings.pdf"
7. Dixon--- "Eddy current losses in transformer windings.pdf"
8. J J Ding, J S Buckkeridge, "Design Considerations For A Sustainable Hybrid Energy System" IPENZ Transactions, 2000, Vol. 27, No. 1/EMCh.
9. Texas Instruments --- "Windings.pdf"
10. Texas Instruments --- "Magnetic core characteristics.pdf"
11. Ferroxcube --- "3f3 ferrite datasheet.pdf"
12. Ferroxcube --- "Ferrite selection guide.pdf"
13. Magnetics, Inc., Ferrite Cores (www.mag-inc.com).

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

**PROGRAMMABLE LOGIC CONTROLLERS AND APPLICATIONS
(Professional Elective – I)**

Course Objectives:

- To understand the generic architecture and constituent components of a Programmable Logic Controller.
- To develop a software program using modern engineering tools and technique for PLC.
- To apply knowledge gained about PLCs to identify few real life industrial applications

Course Outcomes: Upon the completion of the course the student will be able to

- Develop and explain the working of PLC with the help of a block diagram.
- Execute, debug and test the programs developed for digital and analog operations.
- Reproduce block diagram representation on industrial applications using PLC.

Unit-I:

PLC Basics PLC system, I/O modules and interfacing CPU processor programming equipment programming formats, construction of PLC ladder diagrams, devices connected to I/O modules.

Unit-II:

PLC Programming input instructions, outputs, operational procedures, programming examples using contacts and coils. Drill-press operation.

Digital logic gates programming in the Boolean algebra system, conversion examples Ladder diagrams for process control Ladder diagrams and sequence listings, ladder diagram construction, and flow chart for spray process system.

Unit-III:

PLC Registers: Characteristics of Registers module addressing holding registers input registers, output registers. PLC Functions Timer functions and industrial applications counters counter function industrial applications, Architecture functions, Number comparison functions, number conversion functions.

Unit-IV:

Data handling functions: SKIP, Master control Relay Jump Move FIFO, FAL, ONS, CLR and Sweep functions and their applications. Bit Pattern and changing a bit shift register, sequence functions and applications, controlling of two axes and three axis Robots with PLC, Matrix functions.

Unit-V:

Analog PLC operation: Analog modules and systems Analog signal processing multi bit data processing , analog output application examples, PID principles position indicator with PID control, PID modules, PID tuning, PID functions

REFERENCE BOOKS:

1. Programmable Logic Controllers – Principle and Applications by John W Webb and Ronald A Reiss Fifth edition, PHI
2. Programmable Logic Controllers – Programming Method and Applications by JR Hackworth and F.D Hackworth – Jr- Pearson, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

**ELECTRIC TRACTION SYSTEMS
(Professional Elective - II)**

Course Objectives:

- To understand various systems of track electrification, power supply system and mechanics of electric train.
- To identify a suitable drive for electric traction.

Course Outcomes: Upon the completion of the course the student will be able to

- Understand Traction systems and its mechanics
- Identify the power supply equipment for traction systems
- Analyze various types of motors used in traction and differentiate AC and DC traction drives

UNIT – I

Traction Systems : Electric drives - Advantages & disadvantages - System of track electrification - d.c., 1-Phase low frequency, 3-Phase low frequency and composite systems, Problems of 1-phase traction system - Current unbalance, Voltage unbalance, Production of harmonics, Induction effects, Booster transformer - Rail connected booster transformer. Comparison between ac. and d.c. systems.

UNIT – II

Traction mechanics: Types of services, Speed - time curves - Construction of quadrilateral and trapezoidal speed time curves, Average & schedule speeds. Tractive effort - Speed characteristic, Power of traction motor, specific energy consumption - Factors affecting specific energy consumption, Coefficient of adhesion, slip - Factors affecting slip, magnetically suspended trains.

UNIT – III

Power supply arrangements : High voltage supply, Constituents of supply system - Substations, Feeding post, Feeding & sectioning arrangements, Remote control center, Design considerations of substations, Over head equipment - principle of design of OHE, Polygonal OHE - Different types of constructions, Basic sag & tension calculations, Dropper design, Current collection gear for OHE.

UNIT – IV

Traction motors : Desirable characteristics, D.C. series motors, A.C. series motors, 3-Phase induction motors, linear induction motors, D.C. motor series & parallel control - Shunt bridge transition – Drum controller, Contact type bridge transition control, Energy saving, Types of braking in a.c. and d.c. drives, Conditions for regenerative braking, Stability of motors under regenerative braking.

UNIT – V

Semi conductor converter controlled drives: Advantages of A.C. Traction - Control of d.c. motors - single and two stage converters, Control of ac. motors - CSI fed squirrel cage induction motor, PWM VSI induction motor drive, D.C. traction — Chopper controlled d.c. motors, composite braking, Diesel electric traction — D.C. generator fed d.c. series motor, Alternator fed d.c. series motor, Alternator fed squirrel cage induction motor, Locomotive and axle codes.

TEXT BOOKS:

1. Partab.H - Modern Electric Traction, Dhanpat Rai & Sons – 1998.
2. Dubey. G.K. - Fundamentals of Electrical Drives, Narosa Publishing House - 2001.

3. C. L. Wadhwa — Generation, Distribution and Utilization of Electrical Energy, New Age International - 2006.
4. J.B. Gupta - Utilization of Electrical Power and Electric Traction, S. K. Kataria & Sons publications, 9th edition 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

**ADVANCED DIGITAL SIGNAL PROCESSING
(Professional Elective - II)**

Prerequisite: Digital Signal Processing

Course Objectives:

- To have an overview of signals and systems and DFT & FFT Transforms.
- To study the design of IIR & FIR filters.
- To study the applications of DSP techniques in processors.

Course Outcomes: Upon the completion of the course the student will be able to

- Understand types of digital signals and Transforms and its application to signals and systems.
- Design IIR & FIR filters.
- Estimate power spectrum using various methods

UNIT-I:

Digital Filter Structures: Block diagram representation – Equivalent Structures – FIR and IIR digital filter Structures All pass Filters-tunable IIR Digital Sine-cosine generator- Computational complexity of digital filter structures.

UNIT-II:

Digital Filter Design: Preliminary considerations- Bilinear transformation method of IIR filter design – design of Low pass high-pass – Band-pass, and Band stop- IIR digital filters – Spectral transformations of IIR filters – FIR filter design –based on Windowed Fourier series – design of FIR digital filters with least – mean square-error – constrained Least –square design of FIR digital filters.

UNIT-III:

DSP Algorithm Implementation: Computation of the discrete Fourier transform- Number representation – Arithmetic operations – handling of overflow – Tunable digital filters – function approximation.

UNIT-IV:

Analysis Of Finite Word Length Effects: The Quantization process and errors-Quantization of fixed –point and floating –point Numbers – Analysis of coefficient Quantization effects – Analysis of Arithmetic Round-off errors- Dynamic range scaling – signal –to- noise in Low –order IIR filters- Low – Sensitivity Digital filter – Reduction of Product round-off errors feedback – Limit cycles in IIR digital filter – Round – off errors in FFT Algorithms.

UNIT-V:

Power Spectrum Estimation: Estimation of spectra from Finite Duration Observations signals- Non-parametric methods for power spectrum Estimation- parametric method for power spectrum Estimation- Estimation of spectral form-Finite duration observation of signals- Non-parametric methods for power spectrum estimation – Walsh methods – Blackman and torchy method.

TEXT BOOKS:

1. Digital Signal Processing principles –algorithms and Applications- john G. Proakis –PHI – 3rd edition 2002.
2. Digital Time Signal Processing: Alan V. Oppenheim, Ronald W ,Shafer – PHI 1996 1st Edition reprint

3. Advanced Digital Signal Processing – Theory and Applications – Glenn Zelniker, Fred J. Taylor.

REFERENCE BOOK:

1. Digital Signal Processing – S. Salivahanan . A Vallavaraj C. Gnanapriya –TMH – 2nd reprint 2001.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

**DIGITAL CONTROL SYSTEMS
(Professional Elective - II)**

Prerequisites: Linear control systems, Z-Transforms

Course Objectives:

- To explain basic and digital control system for the real time analysis and design of control systems.
- To apply the knowledge state variable analysis in the design of discrete systems.
- To explain the concept of stability analysis and design of discrete time systems.

Course Outcomes: Upon the completion of the course the student will be able to

- Understand the concepts of Digital control systems.
- Analyze and design discrete systems in state variable analysis.
- Relate the concepts of stability analysis and design discrete time systems.

UNIT – I:

Introduction: Block Diagram of typical control system- advantages of sampling in control systems – examples of discrete data and digital systems – data conversion and quantization – sample and hold devices – D/A and A/D conversion – sampling theorem – reconstruction of sampled signals –ZOH.

Z-transform: Definition and evaluation of Z-transforms – mapping between s-plane and z-plane – inverse z-plane transform – theorems of the Z-transforms –limitations of z-transforms –pulse transfer function –pulse transfer function of ZOH –relation between $G(s)$ and $G(z)$ – signal flow graph method applied to digital systems.

UNIT- II:

State Space Analysis: State space modeling of digital systems with sample and hold – state transition equation of digital time in variant systems – solution of time in variant discrete state equations by the Z-Transformation – transfer function from the state model – Eigen values – Eigen vector and diagonalisation of the A-matrix – Jordan canonical form. Computation of state transition matrix-Transformation to phase to variable canonical form-The state diagram – decomposition of digital system – Response of sample data system between sampling instants using state approach.

Stability: Definition of stability – stability tests – The second method of Liapunov.

UNIT- III:

Time Domain Analysis: Comparison of time response of continuous data and digital control systems-correlation between time response and root locus in the s-plane and z-plane – effect of pole-zero configuration in the z-plane upon the maximum overshoot and peak time of transient response – Root loci for digital control systems – steady state error analysis of digital control systems – Nyquits plot – Bode plot-G.M and P.M.

UNIT- IV:

Design: The digital control design with digital controller with bilinear transformation – Digital PID controller-Design with deadbeat response-Pole placement through state feedback-Design of full order state observer-Discrete Euler Lagrange Equation – Discrete maximum principle.

UNIT-V:

Digital State Observer: Design of - Full order and reduced order observers. Design by max. Principle: Discrete Euler language equation-discrete maximum principle.

TEXT BOOKS:

1. Discrete-Time Control systems - K. Ogata, Pearson Education/PHI, 2nd Edition.
2. Digital Control and State Variable Methods by M. Gopal, TMH.

REFERENCE BOOKS:

1. Digital Control Systems, Kuo, Oxford University Press, 2nd Edition, 2003.
2. Digital Control Engineering, M. Gopal

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

POWER CONVERTERS SIMULATION LAB

Course Objectives:

- Students must be able to write the programs for the given problem / system using suitable software
- Students must be able to model the given problem / system using suitable software

Course Outcomes: Upon the completion of this course, the student will be able to

- Acquire knowledge about potential softwares used in electrical engineering.
- Choose and simulate any problem related to Power Electronics and allied fields using appropriate soft wares
- Validate the obtained results and maintain the record

List of Experiments:

1. Modelling and simulation of separately excited DC motor and to study the dynamic behaviour of the machine for change in load torque
2. Modelling and simulation of separately excited Induction machine and to study the dynamic behaviour of the machine for change in load torque
3. Modelling and simulation of three phase synchronous machine and to study the dynamic behaviour of the machine for change in load torque
4. Simulation & analysis of Boost converters with RL load.
5. Simulation & analysis of Boost converters with RL load.
6. Simulation & analysis of Buck-Boost converters with RL load
7. Single-Phase Inverter using PWM Controller with RL Load.
8. Simulation & analysis of three phase PWM inverter fed Induction Motor.
9. Simulation & analysis of Multi Level inverter fed Induction Motor.
10. Mathematical Modeling of discrete time Systems
11. State Space Model For Classical Transfer Function Using MATLAB Verification
12. Obtain PID controller parameters for DC Motor Speed Control.
13. Dynamic behavior of a Induction motor using transfer function approach.
14. Dynamic behavior of a Induction motor using State Space Model approach.

Note: Any ten experiments can be conducted.

Note: Use the suitable software for simulation.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. SOFTWARE ENGINEERING

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Algorithms	25	75	4	0	0	4
PC-2	Software Development Methodologies	25	75	4	0	0	4
PC-3	Software Requirements and Estimation	25	75	4	0	0	4
PE-1	1. Cloud Computing 2. Database Internals 3. Cryptography and Network Security 4. Internet Technologies and Services	25	75	3	0	0	3
PE-2	1. Big Data Analytics 2. Web Mining 3. Machine Learning 4. Software Metrics and Quality Assurance	25	75	3	0	0	3
OE-1	*Open Elective – 1	25	75	3	0	0	3
Laboratory I	Software Development Methodologies Lab	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Software Architecture and Design Patterns	25	75	4	0	0	4
PC-5	Software Process and Project Management	25	75	4	0	0	4
PC-6	Software Quality Assurance and Testing	25	75	4	0	0	4
PE-3	1. Scripting Languages 2. Information Retrieval Systems 3. Semantic Web and Social Networks 4. E-Commerce	25	75	3	0	0	3
PE-4	1. Software Security Engineering 2. Cyber Security 3. Information Security and Audit 4. Systems Engineering	25	75	3	0	0	3
OE-2	*Open Elective – 2	25	75	3	0	0	3
Laboratory II	Software Testing Lab	25	75	0	0	3	2
Seminar II	Seminar -II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

ADVANCED ALGORITHMS (PC -1)

Course Objectives:

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.
- Significance of algorithms in the computer field
- Various aspects of algorithm development
- Qualities of a good solution

Unit - I : Introduction - Role of algorithms in computing, Analyzing algorithms, Designing Algorithms, Growth of Functions, Divide and Conquer- The maximum-subarray problem, Strassen's algorithms for matrix multiplication, The substitution method for solving recurrences, The recurrence-tree method for solving recurrence, The master method for solving recursions, Probabilistic analysis and random analysis.

Unit - II: Review of Data Structures- Elementary Data Structures, Hash Tables, Binary Search Trees, Red-Black Trees.

Unit - III: Dynamic Programming - Matrix-chain multiplication, Elements of dynamic programming, Longest common subsequence, Greedy Algorithms - Elements of the greedy strategy, Huffman codes, Amortized Analysis - Aggregate analysis, The accounting method, The potential method, Dynamic tables.

Unit - IV: Graph Algorithms - Elementary Graph Algorithms, Minimal spanning trees, Single-Source Shortest Paths, Maximum flow.

Unit - V: NP-Complete & Approximate Algorithms-Polynomial time, Polynomial-time verification, NP-completeness and reducibility, NP-complete & approximation problems - Clique problem, Vertex-cover problem, formula satisfiability, 3 CNF Satisfiability, The vertex-cover problem, The traveling-salesman problem, The subset-sum problem.

TEXT BOOKS:

1. "Introduction to Algorithms", Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Clifford Stein, Third Edition, PHI Publication.
2. "Data Structures and Algorithms in C++", M.T. Goodrich, R. Tamassia and D.Mount, Wiley India.

REFERENCES:

1. Fundamentals of Computer Algorithms, Ellis Horowitz, Sartaj Sahni, Sanguthevar Rajasekaran, Second Edition, Galgotia Publication
2. Data structures with C++, J. Hubbard, Schaum's outlines, TMH.
3. Data structures and Algorithm Analysis in C++, 3rd edition, M. A. Weiss, Pearson.
4. Classic Data Structures, D. Samanta, 2nd edition, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

SOFTWARE DEVELOPMENT METHODOLOGIES (PC - 2)

Course Objectives: Your studies will enable you to develop:

- A broad and critical understanding of all the processes for engineering high quality software and the principles, concepts and techniques associated with software development
- An ability to analyze and evaluate problems and draw on the theoretical and technical knowledge to develop solutions and systems
- A range of skills focused on the analysis of requirements, design and implementation of reliable and maintainable software, with strong emphasis on engineering principles applied over the whole development lifecycle
- An awareness of current research in software development, the analytical skills and research techniques for their critical and independent evaluation and their application to new problems.

UNIT - I

Introduction to Software Engineering: The evolving role of software, Changing Nature of Software, legacy software, Software myths.

A Generic view of process: Software engineering - A layered technology, a process framework, The Capability Maturity Model Integration (CMMI), Process patterns, process assessment, personal and team process models.

Process models: The waterfall model, Incremental process models, Evolutionary process models, specialized process models, The Unified process.

UNIT - II

Software Requirements: Functional and non-functional requirements, User requirements, System requirements, Interface specification, the software requirements document.

Requirements engineering process: Feasibility studies, Requirements elicitation and analysis, Requirements validation, Requirements management.

System models: Context Models, Behavioural models, Data models, Object models, structured methods.

UNIT - III

Design Engineering: Design process and Design quality, Design concepts, the design model, pattern based software design.

Creating an architectural design: software architecture, Data design, Architectural styles and patterns, Architectural Design, assessing alternative architectural designs, mapping data flow into software architecture.

Software Design Approaches, Structured Analysis, Structured Design.

UNIT - IV

Object Oriented Concepts and Principles, Object Oriented Analysis, Object Oriented Design,

Modelling component-level design: Designing class-based components, conducting component-level design, object constraint language, designing conventional components.

UNIT - V

User Interface Design, Performing User interface design: Golden rules, User interface analysis and design, interface analysis, interface design steps, Design evaluation. Coding and Documentation.

TEXT BOOKS:

1. Software Engineering: A practitioner's Approach, Roger S Pressman, sixth edition. McGraw

- Hill International Edition, 2005 (Unit 1, 2, 3, 5)
2. Software Engineering by Jibitesh Mishra, Ashok Mohanty. Pearson.(Unit 4, 5)

REFERENCE BOOKS:

1. Software Engineering, A Precise Approach, Pankaj Jalote, Wiley India, 2010.
2. Software Engineering : A Primer, Waman S Jawadekar, Tata McGraw-Hill, 2008
3. Fundamentals of Software Engineering, Rajib Mall, PHI, 2005
4. Software Engineering, Principles and Practices, Deepak Jain, Oxford University Press.
5. Software Engineering1: Abstraction and modeling, Diner Bjorner, Springer International edition, 2006.
6. Software Engineering2: Specification of systems and languages, Diner Bjorner, Springer International edition, 2006.
7. Software Engineering Foundations, Yingxu Wang, Auerbach Publications, 2008.
8. Software Engineering 3: Domains, Requirements and Software Design, D.Bjorner, Springer, International Edition.
9. Software Engineering Principles and Practice, Hans Van Vliet, 3rd edition, Wiley India edition.
10. Introduction to Software Engineering, R.J. Leach, CRC Press.
11. Software Engineering Fundamentals, Ali Behforooz and Frederick J. Hudson, Oxford University Press, RP 2009
12. Software Engineering Handbook, Jessica Keyes, Auerbach, 2003.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

SOFTWARE REQUIREMENTS AND ESTIMATION (PC - 3)

Course Objectives:

- Students will demonstrate knowledge of the distinction between critical and non- critical systems.
- Students will demonstrate the ability to manage a project including planning, scheduling, and risk assessment/management.
- Students will author a software requirements document.
- Students will demonstrate an understanding of the proper contents of a software requirements document.
- Students will author a formal specification for a software system.
- Students will demonstrate an understanding of distributed system architectures and application architectures.
- Students will demonstrate an understanding of the differences between real-time and non-real time systems.
- Students will demonstrate proficiency in rapid software development techniques.
- Students will demonstrate proficiency in software development cost estimation
- Students will author a software testing plan.

UNIT - I

Software Requirements: What and Why

Essential Software requirement, Good practices for requirements engineering, Improving requirements processes, Software requirements and risk management

Software Requirements Engineering

Requirements elicitation, requirements analysis documentation, review, elicitation techniques, analysis models, Software quality attributes, risk reduction through prototyping, setting requirements priorities, verifying requirements quality,

UNIT - II

Software Requirements Management

Requirements management Principles and practices, Requirements attributes, Change Management Process, Requirements Traceability Matrix, Links in requirements chain

Software Requirements Modeling

Use Case Modeling, Analysis Models, Dataflow diagram, state transition diagram, class diagrams, Object analysis, Problem Frames

UNIT - III

Software Estimation: Components of Software Estimations, Estimation methods, Problems associated with estimation, Key project factors that influence estimation

Size Estimation: Two views of sizing, Function Point Analysis, Mark II FPA, Full Function Points, LOC Estimation, Conversion between size measures,

UNIT - IV

Effort, Schedule and Cost Estimation: What is Productivity? Estimation Factors, Approaches to Effort and Schedule Estimation, COCOMO II, Putnam Estimation Model, Algorithmic models, Cost Estimation

UNIT - V

Tools for Requirements Management and Estimation

Requirements Management Tools: Benefits of using a requirements management tool, commercial requirements management tool, Rational Requisite pro, Caliber – RM, implementing requirements management automation, **Software Estimation Tools:** Desirable features in software estimation tools, IFPUG, USC's COCOMO II, SLIM (Software Life Cycle Management) Tools

TEXT BOOK:

1. Software Requirements and Estimation by Rajesh Naik and Swapna Kishore, Tata Mc Graw Hill.

REFERENCE BOOKS:

1. Software Requirements by Karl E. Weigers, Microsoft Press.
2. Managing Software Requirements, Dean Leffingwell & Don Widrig, Pearson Education, 2003.
3. Mastering the requirements process, second edition, Suzanne Robertson & James Robertson, Pearson Education, 2006.
4. Estimating Software Costs, Second edition, Capers Jones, TMH, 2007.
5. Practical Software Estimation, M.A. Parthasarathy, Pearson Education, 2007.
6. Measuring the software process, William A. Florac & Anita D. Carleton, Pearson Education, 1999.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY

M. Tech - I Year-I Sem (Software Engineering)

CLOUD COMPUTING (Professional Elective- 1)

Course Objectives:

- To learn the new computing model which enables shared resources on demand over the network.
- To learn about the pay-per-use scenarios.
- To learn about the new kind of service models and deployment models.
- To learn about the virtualization technology.
- To learn the python programming or various services and models.
- To develop cloud applications in Python

UNIT-I

Principles of Parallel and Distributed Computing, Introduction to cloud computing, Cloud computing Architecture, cloud concepts and technologies, cloud services and platforms, Cloud models, cloud as a service, cloud solutions, cloud offerings, introduction to Hadoop and Map Reduce.

UNIT –II

Cloud Platforms for Industry, Healthcare and education, Cloud Platforms in the Industry, cloud applications.

Virtualization, cloud virtualization technology, deep dive: cloud virtualization, Migrating in to cloud computing, Virtual Machines Provisioning and Virtual Machine Migration Services, On the Management of Virtual Machines for cloud Infrastructure, Comet cloud, T-Systems,

UNIT-III

Cloud computing Applications: Industry, Health, Education, Scientific Applications, Business and Consumer Applications, Understanding Scientific Applications for Cloud Environments, Impact of Cloud computing on the role of corporate IT.

Enterprise cloud computing Paradigm, Federated cloud computing Architecture, SLA Management in Cloud Computing, Developing the cloud: cloud application Design.

UNIT-IV

Python Basics, Python for cloud, cloud application development in python, Cloud Application Development in Python.

Programming Google App Engine with Python: A first real cloud Application, Managing Data in the cloud, Google app engine Services for Login Authentication, Optimizing UI and Logic, Making the UI Pretty: Templates and CSS, Getting Interactive. Map Reduce Programming Model and Implementations.

UNIT-V

Cloud management, Organizational Readiness and change management in the cloud age, Cloud Security, Data security in the cloud, Legal Issues in the Cloud , Achieving Production Readiness for the cloud Services

TEXT BOOKS:

1. Cloud Computing: Raj Kumar Buyya , James Broberg, andrzej Goscinski, 2013 Wiley
2. Mastering Cloud Computing: Raj Kumar buyya, Christian Vecchiola, selvi-2013.
3. Cloud Computing: Arshdeep Bahga, Vijay Madiseti, 2014, University Press.

4. Cloud computing: Dr Kumar Saurab Wiley India 2011.

REFERENCES:

1. Code in the Cloud: Mark C. Chu-Carroll 2011, SPD. (Second part of IV UNIT)
2. Essentials of cloud computing: K Chandrasekharan, CRC Press.
3. Cloud Computing: John W. Rittinghouse, James Ransome, CRC Press.
4. Cloud Security and Privacy: Mather, Kumara swamy and Latif. 2011. SPD, Oreilly.
5. Virtualization Security: Dave shackleford 2013. SYBEX a wiley Brand.
6. Cloud Computing Bible: Sosinsky 2012. Wiley India.
7. Cloud Computing: Dan C. Marinescu-2013, Morgan Kaufmann.
8. Distributed and Cloud Computing, Kai Hwang, Geoffery C. Fox, Jack J. Dongarra, Elsevier, 2012.
9. Fundamentals of Python Kenneth A. Lambert, B.L. Juneja

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

DATABASE INTERNALS (Professional Elective- 1)

Course Objectives: By the end of the course, student will know:

- History and Structure of databases
- How to design a database
- How to convert the design into the appropriate tables
- Handling Keys appropriately
- Enforcing Integrity Constraints to keep the database consistent
- Normalizing the tables to eliminate redundancies
- Querying relational data
- and processing the queries
- Storage Optimizing Strategies for easy retrieval of data through index
- Triggers, Procedures and Cursors ,Transaction Management
- Distributed databases management system concepts and Implementation

UNIT - I

Database System Applications, Purpose of Database Systems, View of Data – Data Abstraction, Instances and Schemas, Data Models – the ER Model, Relational Model, Other Models – Database Languages – DDL, DML, Database Access from Applications Programs, Transaction Management, Data Storage and Querying, Database Architecture, Database Users and Administrators, ER diagrams,. Relational Model: Introduction to the Relational Model – Integrity Constraints Over Relations, Enforcing Integrity constraints, Querying relational data, Logical data base Design, Introduction to Views –Altering Tables and Views, Relational Algebra, Basic SQL Queries, Nested Queries, Complex Integrity Constraints in SQL, Triggers

UNIT - II

Introduction to Schema Refinement – Problems Caused by redundancy, Decompositions – Problem related to decomposition, Functional Dependencies - Reasoning about FDS, Normal Forms – FIRST, SECOND, THIRD Normal forms – BCNF –Properties of Decompositions- Loss less- join Decomposition, Dependency preserving Decomposition, Schema Refinement in Data base Design – Multi valued Dependencies – FOURTH Normal Form, Join Dependencies, FIFTH Normal form.

UNIT - III

Transaction Management: The ACID Properties, Transactions and Schedules, Concurrent Execution of Transactions – Lock Based Concurrency Control, Deadlocks – Performance of Locking – Transaction Support in SQL.

Concurrency Control: Serializability, and recoverability – Introduction to Lock Management – Lock Conversions, Dealing with Deadlocks, Specialized Locking Techniques – Concurrency Control without Locking.

Crash recovery: Introduction to Crash recovery, Introduction to ARIES, the Log, and Other Recovery related Structures, the Write-Ahead Log Protocol, Check pointing, recovering from a System Crash, Media recovery

UNIT - IV

Overview of Storage and Indexing: Data on External Storage, File Organization and Indexing – Clustered Indexes, Primary and Secondary Indexes, Index data Structures – Hash Based Indexing, Tree based Indexing

Storing data: Disks and Files: -The Memory Hierarchy – Redundant Arrays of Independent Disks.

Tree Structured Indexing: Intuitions for tree Indexes, Indexed Sequential Access Methods (ISAM)

B+ Trees: A Dynamic Index Structure, Search, Insert, Delete.

Hash Based Indexing: Static Hashing, Extendable hashing, Linear Hashing, Extendable Vs Linear Hashing.

UNIT - V

Distributed databases: Introduction to distributed databases, Distributed DBMS architectures, Storing data in a distributed DBMS, Distributed catalog management, Distributed query processing Updating distributed data, Distributed transactions, Distributed concurrency control, Distributed recovery

TEXT BOOKS:

1. Data base Management Systems, Raghu Ramakrishnan, Johannes Gehrke, TMH, 3rd Edition, 2003.
2. Data base System Concepts, A. Silberschatz, H.F. Korth, S. Sudarshan, McGraw hill, VI edition, 2006.
3. Fundamentals of Database Systems 5th edition, Ramez Elmasri, Shamkant B. Navathe, Pearson Education, 2008.

REFERENCE BOOKS:

1. Introduction to Database Systems, C.J. Date, Pearson Education.
2. Database Management System Oracle SQL and PL/SQL, P.K. Das Gupta, PHI.
3. Database System Concepts, Peter Rob, & Carlos Coronel, Cengage Learning, 2008.
4. Database Systems, A Practical approach to Design Implementation and Management Fourth edition, Thomas Connolly, Carolyn Begg, Pearson education.
5. Database-Principles, Programming and Performance, P. O'Neil & E. O'Neil, 2nd ed, ELSEVIER
6. Fundamentals of Relational Database Management Systems, S. Sumathi, S. Esakkirajan, Springer.
7. Introduction to Database Management, M.L. Gillenson and others, Wiley Student Edition.
8. Database Development and Management, Lee Chao, Auerbach publications, Taylor & Francis Group.
9. Distributed Databases Principles & Systems, Stefano Ceri, Giuseppe Pelagatti, TMH.
10. Principles of Distributed Database Systems, M. Tamer Ozsu, Patrick Valduriez, Pearson Education, 2nd Edition.
11. Distributed Database Systems, Chhanda Ray, Pearson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

**CRYPTOGRAPHY AND NETWORK SECURITY
(Professional Elective- 1)**

Course Objectives:

- Understand the basic categories of threats to computers and networks
- Understand various cryptographic algorithms.
- Describe public-key cryptosystem.
- Describe the enhancements made to IPv4 by IPSec
- Understand Intrusions and intrusion detection
- Discuss the fundamental ideas of public-key cryptography.
- Generate and distribute a PGP key pair and use the PGP package to send an encrypted e-mail message.
- Discuss Web security and Firewalls

Course Outcomes:

- Student will be able to understand basic cryptographic algorithms, message and web authentication and security issues.
- Ability to identify information system requirements for both of them such as client and server.
- Ability to understand the current legal issues towards information security

UNIT – I

Security Concepts: Introduction, The need for security, Security approaches, Principles of security, Types of Security attacks, Security services, Security Mechanisms, A model for Network Security

Cryptography Concepts and Techniques: Introduction, plain text and cipher text, substitution techniques, transposition techniques, encryption and decryption, symmetric and asymmetric key cryptography, steganography, key range and key size, possible types of attacks.

UNIT – II

Symmetric key Ciphers: Block Cipher principles, DES, AES, Blowfish, RC5, IDEA, Block cipher operation, Stream ciphers, RC4.

Asymmetric key Ciphers: Principles of public key cryptosystems, RSA algorithm, Elgamal Cryptography, Diffie-Hellman Key Exchange, Knapsack Algorithm.

UNIT – III

Cryptographic Hash Functions: Message Authentication, Secure Hash Algorithm (SHA-512), **Message authentication codes:** Authentication requirements, HMAC, CMAC, Digital signatures, Elgamal Digital Signature Scheme.

Key Management and Distribution: Symmetric Key Distribution Using Symmetric & Asymmetric Encryption, Distribution of Public Keys, Kerberos, X.509 Authentication Service, Public – Key Infrastructure

UNIT – IV

Transport-level Security: Web security considerations, Secure Socket Layer and Transport Layer Security, HTTPS, Secure Shell (SSH)

Wireless Network Security: Wireless Security, Mobile Device Security, IEEE 802.11 Wireless LAN, IEEE 802.11i Wireless LAN Security

UNIT – V

E-Mail Security: Pretty Good Privacy, S/MIME **IP Security:** IP Security overview, IP Security architecture, Authentication Header, Encapsulating security payload, combining security associations, Internet Key Exchange

Case Studies on Cryptography and security: Secure Multiparty Calculation, Virtual Elections, Single sign On, Secure Inter-branch Payment Transactions, Cross site Scripting Vulnerability.

TEXT BOOKS:

1. Cryptography and Network Security - Principles and Practice: William Stallings, Pearson Education, 6th Edition
2. Cryptography and Network Security : Atul Kahate, Mc Graw Hill, 3rd Edition

REFERENCE BOOKS:

1. Cryptography and Network Security: C K Shyamala, N Harini, Dr T R Padmanabhan, Wiley India, 1st Edition.
2. Cryptography and Network Security : Forouzan Mukhopadhyay, Mc Graw Hill, 3rd Edition
3. Information Security, Principles, and Practice: Mark Stamp, Wiley India.
4. Principles of Computer Security: WM. Arthur Conklin, Greg White, TMH
5. Introduction to Network Security: Neal Krawetz, Cengage Learning
6. Network Security and Cryptography: Bernard Menezes, Cengage Learning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

INTERNET TECHNOLOGIES AND SERVICES (Professional Elective- 1)

Course Objective:

The student who has knowledge of programming with java should be able to develop web based solutions using multi-tier architecture. S/he should have good understanding of different technologies on client and server side components as Follows:

- Client Side: HTML5, CSS3, Javascript, Ajax, JQuery and JSON
- Server Side: Servlets, JSP
- Database: MySQL with Hibernate and Connection Pooling
- Framework: Struts with validation framework, Internationalization (I18N)
- SOA: Service Oriented Architecture, Web services fundamentals, Axis framework for WS

UNIT - I

Client Side Technologies:

Overview of HTML - Common tags, XHTML, capabilities of HTML5, Cascading Style sheets, CSS3 enhancements, linking to HTML Pages, Classes in CSS, Introduction to Java Scripts, variables, arrays, methods and string manipulation, BOM/DOM (Browser/Document Object Model), accessing elements by ID, Objects in Java Script, Dynamic HTML with Java Script and with CSS, form validation with Java Script, Handling Timer Events, Simplifying scripting with JQuery, JASON for Information exchange.

UNIT - II

Introduction to Java Servlets:

Introduction to Servlets: Lifecycle of a Servlet, Reading request and initialization parameters, Writing output to response, MIME types in response, Session Tracking: Using Cookies and Sessions, Steps involved in Deploying an application, Database Access with JDBC and Connection Pooling, Introduction to XML, XML Parsing with DOM and SAX Parsers in Java, Ajax - Ajax programming with JSP/Servlets, creating XML Http Object for various browsers, Sending request, Processing response data and displaying it. Introduction to Hibernate

UNIT - III

Introduction to JSP:

JSP Application Development: Types of JSP Constructs (Directives, Declarations, Expressions, Code Snippets), Generating Dynamic Content, Exception Handling, Implicit JSP Objects, Conditional Processing, Sharing Data Between JSP pages, Sharing Session and Application Data, Using user defined classes with jsp:useBean tag, Accessing a Database from a JSP

UNIT - IV

Introduction to Struts Framework:

Introduction to MVC architecture, Anatomy of a simple struts2 application, struts configuration file, Presentation layer with JSP, JSP bean, html and logic tag libraries, Struts Controller class, Using form data in Actions, Page Forwarding, validation frame work, Internationalization

UNIT - V

Service Oriented Architecture and Web Services

Overview of Service Oriented Architecture – SOA concepts, Key Service Characteristics, Technical Benefits of a SOA, Introduction to Web Services– The definition of web services, basic operational model of web services, basic steps of implementing web services.

Core fundamentals of SOAP – SOAP Message Structure, SOAP encoding, SOAP message exchange models, Describing Web Services –Web Services life cycle, anatomy of WSDL
Introduction to Axis– Installing axis web service framework, deploying a java web service on axis.
Web Services Interoperability – Creating java and .Net client applications for an Axis Web Service
(Note: The Reference Platform for the course will be open source products Apache Tomcat Application Server, MySQL database, Hibernate and Axis)

TEXT BOOKS:

1. Web Programming, building internet applications, Chris Bates 3rd edition, WILEY Dreamtech.
2. The complete Reference Java 7th Edition, Herbert Schildt., TMH.
3. Java Server Pages,Hans Bergsten, SPD, O'Reilly.
4. Professional Jakarta Struts - James Goodwill, Richard Hightower, Wrox Publishers.
5. Developing Java Web Services, R. Nagappan, R. Skoczylas, R.P. Sriganesh, Wiley India, rp – 2008.
6. Understanding SOA with Web Services, Eric Newcomer and Greg Lomow, Pearson Edition – 2009
7. Java Web Service Architecture, James McGovern, Sameer Tyagi et al., Elsevier - 2009

REFERENCE BOOKS:

1. Programming the world wide web, 4th edition, R.W. Sebesta, Pearson
2. Core Servlets and Javasever Pages VOLUME 1: Core Technologies, Marty Hall and Larry Brown Pearson.
3. Internet and World Wide Web – How to program, Dietel and Nieto PHI/Pearson.
4. Jakarta Struts Cookbook, Bill Siggelkow, S P D O'Reilly.
5. Professional Java Server Programming, S. Allamaraju & others Apress (dreamtech).
6. Java Server Programming, Ivan Bayross and others, The X Team, SPD
7. Web Warrior Guide to Web Programming - Bai/Ekedaw-Cengage Learning.
8. Beginning Web Programming-Jon Duckett , WROX.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

**BIG DATA ANALYTICS
(Professional Elective- 2)**

Course Objectives:

- To understand about big data
- To learn the analytics of Big Data
- To Understand the Map Reduce fundamentals

Unit - I

Big Data Analytics: What is big data, History of Data Management; Structuring Big Data; Elements of Big Data; Big Data Analytics; Distributed and Parallel Computing for Big Data;

Big Data Analytics: What is Big Data Analytics, What Big Data Analytics Isn't, Why this sudden Hype Around Big Data Analytics, Classification of Analytics, Greatest Challenges that Prevent Business from Capitalizing Big Data; Top Challenges Facing Big Data; Why Big Data Analytics Important; Data Science; Data Scientist; Terminologies used in Big Data Environments; Basically Available Soft State Eventual Consistency (BASE); Open source Analytics Tools;

Unit - II

Understanding Analytics and Big Data: Comparing Reporting and Analysis, Types of Analytics; Points to Consider during Analysis; Developing an Analytic Team; Understanding Text Analytics;

Analytical Approach and Tools to Analyze Data: Analytical Approaches; History of Analytical Tools; Introducing Popular Analytical Tools; Comparing Various Analytical Tools.

Unit - III

Understanding MapReduce Fundamentals and HBase : The MapReduce Framework; Techniques to Optimize MapReduce Jobs; Uses of MapReduce; Role of HBase in Big Data Processing; Storing Data in Hadoop: Introduction of HDFS, Architecture, HDFS Files, File system types, commands, org.apache.hadoop.io package, HDFS High Availability; Introducing HBase, Architecture, Storing Big Data with HBase , Interacting with the Hadoop Ecosystem; HBase in Operations- Programming with HBase; Installation, Combining HBase and HDFS;

Unit - IV

Big Data Technology Landscape and Hadoop : NoSQL, Hadoop; RDBMS versus Hadoop; Distributed Computing Challenges; History of Hadoop; Hadoop Overview; Use Case of Hadoop; Hadoop Distributors; HDFS (Hadoop Distributed File System), HDFS Daemons, read,write, Replica Processing of Data with Hadoop; Managing Resources and Applications with Hadoop YARN.

Unit - V

Social Media Analytics and Text Mining: Introducing Social Media; Key elements of Social Media; Text mining; Understanding Text Mining Process; Sentiment Analysis, Performing Social Media Analytics and Opinion Mining on Tweets;

Mobile Analytics: Introducing Mobile Analytics; Define Mobile Analytics; Mobile Analytics and Web Analytics; Types of Results from Mobile Analytics; Types of Applications for Mobile Analytics; Introducing Mobile Analytics Tools;

TEXT BOOKS:

1. BIG DATA and ANALYTICS, Seema Acharya, Subhasinin Chellappan, Wiley publications.

2. BIG DATA, Black Book™, Dream Tech Press, 2015 Edition.
3. BUSINESS ANALYTICS 5e , BY Albright |Winston

REFERENCE BOOKS:

1. Rajiv Sabherwal, Irma Becerra- Fernandez,” Business Intelligence –Practice, Technologies, and Management”, John Wiley 2011.
2. Lariss T. Moss, Shaku Atre, “Business Intelligence Roadmap”, Addison-Wesley It Service.
3. Yuli Vasiliev, “Oracle Business Intelligence: The Condensed Guide to Analysis and Reporting”, SPD Shroff, 2012.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

**WEB MINING
(Professional Elective- 2)**

Course Objectives:

- To describe web mining and understand the need for web mining
- To differentiate between Web mining and data mining
- To understand the different application areas for web mining
- To understand the different methods to introduce structure to web-based data
- To describe Web mining, its objectives, and its benefits
- To understand the methods of Web usage mining

UNIT - I

Introduction to Web Data Mining and Data Mining Foundations, Introduction – World Wide Web (WWW), A Brief History of the Web and the Internet, Web Data Mining-Data Mining, Web Mining. Data Mining Foundations – Association Rules and Sequential Patterns – Basic Concepts of Association Rules, Apriori Algorithm- Frequent Itemset Generation, Association Rule Generation, Data Formats for Association Rule Mining, Mining with multiple minimum supports – Extended Model, Mining Algorithm, Rule Generation, Mining Class Association Rules, Basic Concepts of Sequential Patterns, Mining Sequential Patterns on GSP, Mining Sequential Patterns on PrefixSpan, Generating Rules from Sequential Patterns.

UNIT - II

Supervised and Unsupervised Learning

Supervised Learning - Basic Concepts, Decision Tree Induction – Learning Algorithm, Impurity Function, Handling of Continuous Attributes, Classifier Evaluation, Rule Induction – Sequential Covering, Rule Learning, Classification Based on Associations, Naïve Bayesian Classification , Naïve Bayesian Text Classification - Probabilistic Framework, Naïve Bayesian Model .

Unsupervised Learning – Basic Concepts , K-means Clustering – K-means Algorithm, Representation of Clusters, Hierarchical Clustering – Single link method, Complete link Method, Average link method, Strength and Weakness.

UNIT - III

Information Retrieval and Web Search:

Basic Concepts of Information Retrieval, Information Retrieval Methods - Boolean Model, Vector Space Model and Statistical Language Model, Relevance Feedback, Evaluation Measures, Text and Web Page Preprocessing – Stopword Removal, Stemming, Web Page Preprocessing, Duplicate Detection, Inverted Index and Its Compression – Inverted Index, Search using Inverted Index, Index Construction, Index Compression, Latent Semantic Indexing – Singular Value Decomposition, Query and Retrieval, Web Search, Meta Search, Web Spamming.

UNIT - IV

Link Analysis and Web Crawling:

Link Analysis - Social Network Analysis, Co-Citation and Bibliographic Coupling, Page Rank Algorithm, HITS Algorithm, Community Discovery-Problem Definition, Bipartite Core Communities, Maximum Flow Communities, Email Communities.

Web Crawling – A Basic Crawler Algorithm- Breadth First Crawlers, Preferential Crawlers, Implementation Issues – Fetching, Parsing, Stopword Removal, Link Extraction, Spider Traps, Page Repository, Universal Crawlers, Focused Crawlers, Topical Crawlers, Crawler Ethics and Conflicts.

UNIT - V

Opinion Mining and Web Usage Mining

Opinion Mining - Sentiment Classification – Classification based on Sentiment Phrases, Classification Using Text Classification Methods, Feature based Opinion Mining and Summarization – Problem Definition, Object feature extraction, Feature Extraction from Pros and Cons of Format1, Feature Extraction from Reviews of Format 2 and 3, Comparative Sentence and Relation Mining, Opinion Search and Opinion Spam.

Web Usage Mining - Data Collection and Preprocessing- Sources and Types of Data, Key Elements of Web usage Data Preprocessing, Data Modeling for Web Usage Mining, Discovery and Analysis of Web usage Patterns -Session and Visitor Analysis, Cluster Analysis and Visitor Segmentation, Association and Correlation Analysis, Analysis of Sequential and Navigation Patterns.

TEXT BOOK:

1. Web Data Mining: Exploring Hyperlinks, Contents, and Usage Data by Bing Liu (Springer Publications)

REFERENCES BOOKS:

1. Data Mining: Concepts and Techniques, Second Edition Jiawei Han, Micheline Kamber (Elsevier Publications)
2. Web Mining:: Applications and Techniques by Anthony Scime
3. Mining the Web: Discovering Knowledge from Hypertext Data by Soumen Chakrabarti

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

**MACHINE LEARNING
(Professional Elective - 2)**

Course Objectives:

- To be able to formulate machine learning problems corresponding to different applications.
- To understand a range of machine learning algorithms along with their strengths and weaknesses.
- To understand the basic theory underlying machine learning.
- To be able to apply machine learning algorithms to solve problems of moderate complexity.
- To be able to read current research papers and understands the issues raised by current research.

UNIT - I

Introduction - Well-posed learning problems, Designing a learning system, Perspectives and issues in machine learning

Concept learning and the general to specific ordering – Introduction, A concept learning task, Concept learning as search, Find-S: finding a maximally specific hypothesis, Version spaces and the candidate elimination algorithm, Remarks on version spaces and candidate elimination, Inductive bias

UNIT - II

Decision Tree learning – Introduction, Decision tree representation, Appropriate problems for decision tree learning, The basic decision tree learning algorithm, Hypothesis space search in decision tree learning, Inductive bias in decision tree learning, Issues in decision tree learning

Artificial Neural Networks – Introduction, Neural network representation, Appropriate problems for neural network learning, Perceptions, Multilayer networks and the back propagation algorithm, Remarks on the back propagation algorithm, An illustrative example face recognition

Advanced topics in artificial neural networks

Evaluation Hypotheses – Motivation, Estimation hypothesis accuracy, Basics of sampling theory, A general approach for deriving confidence intervals, Difference in error of two hypotheses, Comparing learning algorithms

UNIT - III

Bayesian learning – Introduction, Bayes theorem, Bayes theorem and concept learning, Maximum likelihood and least squared error hypotheses, Maximum likelihood hypotheses for predicting probabilities, Minimum description length principle, Bayes optimal classifier, Gibbs algorithm, Naïve Bayes classifier, An example learning to classify text, Bayesian belief networks The EM algorithm

Computational learning theory – Introduction, Probability learning an approximately correct hypothesis, Sample complexity for Finite Hypothesis Space, Sample Complexity for infinite Hypothesis Spaces, The mistake bound model of learning - **Instance-Based Learning**- Introduction, k -Nearest Neighbour Learning, Locally Weighted Regression, Radial Basis Functions, Case-Based Reasoning, Remarks on Lazy and Eager Learning

Genetic Algorithms – Motivation, Genetic Algorithms, An illustrative Example, Hypothesis Space Search, Genetic Programming, Models of Evolution and Learning, Parallelizing Genetic Algorithms

UNIT - IV

Learning Sets of Rules – Introduction, Sequential Covering Algorithms, Learning Rule Sets: Summary, Learning First Order Rules, Learning Sets of First Order Rules: FOIL, Induction as Inverted Deduction, Inverting Resolution

Analytical Learning - Introduction, Learning with Perfect Domain Theories: Prolog-EBG Remarks on Explanation-Based Learning, Explanation-Based Learning of Search Control Knowledge

UNIT - V

Combining Inductive and Analytical Learning – Motivation, Inductive-Analytical Approaches to Learning, Using Prior Knowledge to Initialize the Hypothesis, Using Prior Knowledge to Alter the Search Objective, Using Prior Knowledge to Augment Search Operators,

Reinforcement Learning – Introduction, The Learning Task, Q Learning, Non-Deterministic, Rewards and Actions, Temporal Difference Learning, Generalizing from Examples, Relationship to Dynamic Programming

TEXT BOOKS:

1. Machine Learning – Tom M. Mitchell, - MGH
2. Machine Learning: An Algorithmic Perspective, Stephen Marsland, Taylor & Francis (CRC)

REFERENCE BOOKS:

1. Machine Learning Methods in the Environmental Sciences, Neural Networks, William W Hsieh, Cambridge Univ. Press.
2. Richard o. Duda, Peter E. Hart and David G. Stork, pattern classification, John Wiley & Sons Inc., 2001
3. Chris Bishop, Neural Networks for Pattern Recognition, Oxford University Press, 1995.
4. Machine Learning by Peter Flach , Cambridge.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

**SOFTWARE METRICS AND QUALITY ASSURANCE
(Professional Elective- 2)**

Course Objectives:

- To gain basic knowledge about metrics, measurement theory and related terminologies
- To learn measure the quality level of internal and external attributes of the software product
- To introduce the basics of software reliability and to illustrate how to perform planning, executing and testing for software reliability
- To explore various metrics and models of software reliability
- To compare various models of software reliability based on its application

Course Outcomes: Upon completion of the course, students shall be able to

- Identify and apply various software metrics, which determines the quality level of software
- Identify and evaluate the quality level of internal and external attributes of the software product
- Compare and Pick out the right reliability model for evaluating the software
- Evaluate the reliability of any given software product
- Design new metrics and reliability models for evaluating the quality level of the software based on the requirement.

UNIT-I

What Is Software Quality: Quality: Popular Views, Quality Professional Views, Software Quality, Total Quality Management, and Summary. Fundamentals Of Measurement Theory: Definition, Operational Definition, And Measurement, Level Of Measurement, Some Basic Measures, Reliability And Validity, Measurement Errors, Be Careful With Correlation, Criteria For Causality, Summary. Software Quality Metrics Overview: Product Quality Metrics, In Process Quality Metrics, Metrics for Software Maintenance, Examples for Metrics Programs, Collecting software Engineering Data.

UNIT-II

Applying The Seven Basic Quality Tools In Software Development: Ishikawa's Seven Basic Tools, Checklist, Pareo Diagram, Histogram, Run Charts, Scatter Diagram, Control Chart, Cause, and Effect Diagram. The Rayleigh Model: Reliability Models, the Rayleigh Model Basic Assumptions, Implementation, Reliability and Predictive Validity.

UNIT-III

Complexity Metrics and Models: Lines of Code, Halstead's Software Science, Cyclomatic Complexity Syntactic Metrics, An Example of Module Design Metrics in Practice .Metric And Lessons Learned for Object Oriented Projects: Object Oriented Concepts And Constructs, Design And Complexity Metrics, Productivity Metrics, Quality And Quality Management Metrics, Lessons Learned For object oriented Projects.

UNIT-IV

Availability Metrics: Definition and Measurement of System Availability, Reliability Availability and Defect Rate, Collecting Customer Outage Data For Quality Improvement, In Process Metrics For Outage And Availability. Conducting Software Project Assessment: Audit Ad Assessment, Software Process Maturity Assessment And Software Project Assessment, Software Process Assessment A Proponed Software Project Assessment Method.

UNIT-V

Dos And Don'ts Of Software Process Improvement :Measuring Process Maturity, Measuring Process Capability, Staged Versus Continuous Debating Religion, Measuring Levels Is Not Enough, Establishing The Alignment Principle ,Take Time Getting Faster, Keep it Simple Or Face Decomplexification, Measuring The Value Of Process Improvement ,Measuring Process Compliance , Celebrate The Journey Not Just The Destination. Using Function Point Metrics to Measure Software Process Improvement: Software Process Improvement Sequences, Process Improvement Economies, Measuring Process Improvement at Activity Levels.

TEXT BOOK:

1. Norman E-Fentor and Share Lawrence Pflieger." Software Metrics". International Thomson Computer Press, 1997.
2. Stephen H Khan: Metrics and Models in Software Quality Engineering, Pearson 2nd edition 2013.

REFERENCES:

1. S.A. Kelkar, "Software quality and Testing, PHI Learning, Pvt., Ltd., New Delhi 2012.
2. Watts S Humphrey, "Managing the Software Process", Pearson Education Inc, 2008.
3. Mary Beth Chrissis, Mike Konrad and Sandy Shrum, "CMMI", Pearson Education (Singapore) Pvt. Ltd., 2003
4. Philip B Crosby, " Quality is Free: The Art of Making Quality Certain ", Mass Market, 1992.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Semester (Software Engineering)

SOFTWARE DEVELOPMENT METHODOLOGIES LAB

Course Objectives:

- To understand the software engineering methodologies involved in the phases for project development.
- To gain knowledge about open source tools used for implementing software engineering methods.
- To exercise developing product-startups implementing software engineering methods.

Open source Tools: StarUML / UMLGraph / Topcased

Prepare the following documents and develop the software project startup, prototype model, using software engineering methodology for at least two real time scenarios or for the sample experiments.

1. Problem Analysis and Project Planning -Thorough study of the problem – Identify Project scope, Objectives and Infrastructure.
2. Software Requirement Analysis – Describe the individual Phases/modules of the project and Identify deliverables. Identify functional and non-functional requirements.
3. Data Modeling – Use work products – data dictionary.
4. Software Designing - Develop use case diagrams and activity diagrams, build and test class diagrams, sequence diagrams and add interface to class diagrams.
5. Prototype model – Develop the prototype of the product.

The SRS and prototype model should be submitted for end semester examination.

List of Sample Experiments:

1. Student Enrolment System.

A University has contracted you to develop their new student records system.

The normal tasks that the system performs are as follows:

Enrol a student at the university: A student provides his or her personal details (name, address, sex, date of birth), along with the code of the course (e.g. Bachelor of Computer Science) in which he or she wishes to enrol. A student record is created, and a unique student ID number is assigned to the student. The system automatically enrolls the student in any core first-year subjects for the course.

Enrol a student in a subject: A student provides his or her student ID number and the subject code of the subject in which he or she wish to enrol. The system checks that the subject requested by the student is allowed for the course in which the student is enrolled. If not, the enrolment request is rejected. The system checks what subjects (if any) are specified as prerequisites for the subject in which the student wishes to enrol. If the student has passed all the prerequisite subjects, he or she is enrolled in the desired subject. Otherwise, the enrolment request is rejected.

Record a mark for a student: A staff member accesses the system by giving a subject code and a password for that subject. If the password is correct, the system displays the list of students enrolled in the subject to the staff member. The staff member can then specify a mark for any student on the list.

Create a new subject: An administrator accesses the system using a password. The administrator then chooses a subject code for the new subject. The system checks that this code is not already in use in the system, and if not, creates a new subject record. The administrator then gives the subject name, the course to which it belongs, the year of the course in which it may first be taken, a flag indicating whether or not it is a core subject and the codes of any prerequisite subjects.

Print a transcript of a student's results: An administrator accesses the system using a password. The administrator then gives the student ID number of the student for whom the transcript is to be

generated. The system contacts the finance system to check whether or not the student has paid all fees. If fees have been paid, the system creates a transcript showing all the subjects in which the student has been enrolled in each year, and the mark for that subject. The header of the transcript shows the student's personal details and the course in which he or she is enrolled.

Assign a staff member to a subject: An administrator accesses the system using a password. The administrator then gives the subject code for the subject to which the staff member is to be assigned and the staff ID number of the staff member.

2. Online Bookshop.

A major book retailer is planning to develop a computer system to handle their new online bookshop: Booky.com. You have been chosen to do the analysis and design.

The following requirements have been identified:

Customers can search for books on the Booky.com website, either by author name, or words in the title. A list of all matching books is returned to the customer. A customer does not need to be logged-in in order to search. The system records all the customers of the Booky.com who have ever logged in. A customer may be an individual customer or a business customer. · Each customer has a username and password. Business customers may have several usernames and passwords, corresponding to different divisions within the business.

When a customer has selected a book to buy at the Booky.com website.

The system prompts for the customer's username and password. The customer enters these details. The system verifies the customer's identity and retrieves the customer's name and address, then prompts for credit card details. The customer enters these details. The system checks the credit card details. The system shows the customer the book and delivery price. The customer confirms the transaction.

The system records all books available at Booky.com. For each book, the author, title and ISBN number are recorded. The number of each book in stock is also stored, along with the number on order by customers and the number on order from publishers. Books may be temporarily unavailable. All books are stored in the Booky.com warehouse. The warehouse can be contacted via a secure internet connection.

For each customer, a permanent record of books bought by that customer is maintained. Likewise, for each book, a record of customers who have bought that book is kept.

A customer order consists of one or more order lines, each corresponding to a particular book. A customer may choose to defer the shipment of an order until all the order lines have been filled.

When the warehouse fills all or part of customer order, an email is sent to the customer informing them of what has been shipped.

If a book ordered by a customer turns out to be unavailable, the corresponding order line is flagged and an email is sent to the customer informing them of the problem. At this stage the customer can cancel this order line.

When a book corresponding to a previously-unavailable order line becomes available, an email is sent to the customer and a copy of the book is held for seven days, after which it is returned to normal stock if the customer has not confirmed the order.

The shop keeps track of which publishers produce particular book titles. Some books may be available from more than one publisher.

Although Booky.com will initially sell only books, it is envisaged that in future it will offer further products, such as CDs. The list of possible future products has not yet been finalized.

3. Course management system (CMS)

A course management system (CMS) is a collection of software tools providing an online environment for course interactions. A CMS typically includes a variety of online tools and environments, such as:

- An area for faculty posting of class materials such as course syllabus and handouts
- An area for student posting of papers and other assignments

- A gradebook where faculty can record grades and each student can view his or her grades
- An integrated email tool allowing participants to send announcement email messages to the entire class or to a subset of the entire class
- A chat tool allowing synchronous communication among class participants
- A threaded discussion board allowing asynchronous communication among participants

In addition, a CMS is typically integrated with other databases in the university so that students enrolled in a particular course are automatically registered in the CMS as participants in that course.

The Course Management System (CMS) is a web application for department personnel, Academic Senate, and Registrar staff to view, enter, and manage course information formerly submitted via paper.

Departments can use CMS to create new course proposals, submit changes for existing courses, and track the progress of proposals as they move through the stages of online approval.

4. **Easy Leave**

This project is aimed at developing a web based Leave Management Tool, which is of importance to either an organization or a college.

The **Easy Leave** is an Intranet based application that can be accessed throughout the organization or a specified group/Dept. This system can be used to automate the workflow of leave applications and their approvals. The periodic crediting of leave is also automated. There are features like notifications, cancellation of leave, automatic approval of leave, report generators etc in this Tool.

Functional components of the project:

There are registered people in the system. Some are approvers. An approver can also be a requestor. In an organization, the hierarchy could be Engineers/Managers/Business Managers/Managing Director etc. In a college, it could be Lecturer/Professor/Head of the Department/Dean/Principal etc.

Following is a list of functionalities of the system:

1. A person should be able to
 - o login to the system through the first page of the application
 - o change the password after logging into the system
 - o see his/her eligibility details (like how many days of leave he/she is eligible for etc)
 - o query the leave balance
 - o see his/her leave history since the time he/she joined the company/college
 - o apply for leave, specifying the from and to dates, reason for taking leave, address for communication while on leave and his/her superior's email id
 - o see his/her current leave applications and the leave applications that are submitted to him/her for approval or cancellation
 - o approve/reject the leave applications that are submitted to him/her
 - o withdraw his/her leave application (which has not been approved yet)
 - o Cancel his/her leave (which has been already approved). This will need to be approved by his/her Superior
 - o get help about the leave system on how to use the different features of the system
2. As soon as a leave application /cancellation request /withdrawal /approval /rejection /password-change is made by the person, an automatic email should be sent to the person and his superior giving details about the action
3. The number of days of leave (as per the assumed leave policy) should be automatically credited to everybody and a notification regarding the same be sent to them automatically

An automatic leave-approval facility for leave applications which are older than 2 weeks should be there. Notification about the automatic leave approval should be sent to the person as well as his superior

5. E-Bidding

Auctions are among the latest economic institutions in place. They have been used since antiquity to sell a wide variety of goods, and their basic form has remained unchanged. In this dissertation, we explore the efficiency of common auctions when values are interdependent- the value to a particular bidder may depend on information available only to others-and asymmetric. In this setting, it is well known that sealed-bid auctions do not achieve efficient allocations in general since they do not allow the information held by different bidders to be shared.

Typically, in an auction, say of the kind used to sell art, the auctioneer sets a relatively low initial price. This price is then increased until only one bidder is willing to buy the object, and the exact manner in which this is done varies. In my model a bidder who drops out at some price can "reenter" at a higher price.

With the invention of E-commerce technologies over the Internet the opportunity to bid from the comfort of one's own home has seen a change like never seen before. Within the span of a few short years, what may have began as an experimental idea has grown to an immensely popular hobby, and in some cases, a means of livelihood, the Auction Patrol gathers tremendous response every day, all day. With the point and click of the mouse, one may bid on an item they may need or just want, and in moments they find that either they are the top bidder or someone else wants it more, and you're outbid! The excitement of an auction all from the comfort of home is a completely different experience.

Society cannot seem to escape the criminal element in the physical world, and so it is the same with Auction Patrols. This is one area where in a question can be raised as to how safe Auction Patrols.

Proposed system

1. To generate the quick reports
2. To make accuracy and efficient calculations
3. To provide proper information briefly
4. To provide data security
5. To provide huge maintenance of records
6. Flexibility of transactions can be completed in time

6. Electronic Cash counter

This project is mainly developed for the Account Division of a Banking sector to provide better interface of the entire banking transactions. This system is aimed to give a better out look to the user interfaces and to implement all the banking transactions like:

- Supply of Account Information
- New Account Creations
- Deposits
- Withdraws
- Cheque book issues
- Stop payments
- Transfer of accounts
- Report Generations.

Proposed System:

The development of the new system contains the following activities, which try to automate the entire process keeping in view of the database integration approach.

- User friendliness is provided in the application with various controls.
- The system makes the overall project management much easier and flexible.
- Readily upload the latest updates, allows user to download the alerts by clicking the URL.
- There is no risk of data mismanagement at any level while the project development is under process.

It provides high level of security with different level of authentication

7. Enterprise Security Services

Verification and Validation is a part of S/W Quality Assurance. Verification refers to the set of activities that ensure correctly implements a specific function. Validation refers to a different set of activities that ensure that the software that has been built is traceable to customer requirements.

Verification: "Are we building the product right"

Validation: "Are we building the right product"

The project entitled Independent Project Metrics is an effort, to develop a tool to manage the Verification and Validation process.

The specific purpose of the Independent Verification and Validation Process of Project Metrics Tool is to bring out the various Verification and validation tasks to be performed. The scope of the Project Metrics is to cover the developed for system.

The goals of the V&V effort is to ensure that the software and the documents are developed are of high quality as expected from any mission critical software. This project generates the plan for Verification and validation process. This project maintain the document names, source code module names, version number, released date, receiving date size of document and source code modules of receiving projects for Verification and validation.

Using this application we assign the tasks/activities to different persons and also calculate the expected efforts and actual efforts. The V&V co-coordinator does this work.

Proposed System:

The general description gives an "executive overview" and is very client-oriented. It expounds on the functional and data requirements of the application. It also lists the limitations, assumptions and dependencies of the application. It also touches on the performance and quality requirements of the application and provides a solid definition of the interface

The computerization of this system would avoid the wrong interpretation and bad calculation of data .The system help the user to see any documents, source code, tasks, activities, team information with details at the click of a button. The record data is maintained and backed up such a way that data is not loss. The speed of the system could also increased

8. ERP

ERP is a powerful human resource tool for maintaining employee and company information. More than a data storage program, ERP helps you manage your employees. ERP offers a wide variety of reports that give you exactly the information you need. View payroll information by department, or find everyone who is receiving company

Module Description:

1. Payroll
2. Employee
3. Employee payslip
4. Selection process
5. Reports
6. Mailing System
7. Training
8. Add Company Information

PROPOSED SYSTEM

The proposed system is designed to eliminate all the drawbacks of the existing system. The system is part of a large HRMS Application and shall be responsible for maintaining information about employees,

- positions,
- company benefits,
- departments,
- new recruit checklists,

- employee achievements,
- warnings,
- evaluation reports,
- education & training,
- administration,
- Work changes and several ad hoc reports.

The major advantage of the proposed system is,

- It's online, so that information is available anytime.
- High integrity and security.
- Ability to incorporate newly available data.
- It is user friendly
- Speed and accuracy is increased
- Fully automated.
- Security is associated with user authentication
- Duplication of information is curbed

8. Examination Branch System

The project "**Examination Branch System**" is to reduce the overhead involved in the process of maintains the data and the transaction in the Examination branch. Examination branch is an intranet application for an organization. It is software which is used to perform all the examination activities like adding employees, search employees, delete employees, and assign examination duties etc.

The basic framework of the project can be developed in .NET. Making use of this application the administrator can perform their activities through it.

Proposed System: can be extend to assign duties to faculty, can implement edit, update operations, and develop a user friendly type.

9. Exam Experts

The system would be providing a number of services, automating the processes that are being done manually. The services include communication services such as mailing facility, chat service, electronic file transfer etc and office automation packages such as leave letter processing, admission management, teaching evaluation, counseling automation etc.

The aim of the project is to design a comprehensive web enabled application for management of the Examination Process. Examination system is categorized into various sections. Among those sections, this system concentrates on the work being done in section (E-X).

The section (E-X) deals with the confidential work, i.e., Coding-Decoding of answer scripts, Processing of results, Computerization of certificates etc. This is an automated section and it plays a pivotal role in the Examination Process starting from the Application Processing to the final announcement of results

This project is aimed to solve many of the problems that are in the existing system and also provide a hassle free system that is efficient and easy to use. This project concentrates mainly on Application Processing, Marks Processing and Results Processing with an easy to use interface. The system also provides a means to generate and print various types of reports.

This project can include an **Application Processing System**:

This phase involves the storing of the application information and generating the required reports.

- Entry of Application forms according to center ,course order and batch
- Generating Application Id for further transactions
- Capturing of photographs of students for hall ticket processing
- Reports involving the information about students who are appearing for supplementary exams
- Generating nominal roles

Reports describing the college, course, subjects and the students appearing

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech in CAD/CAM

Effective from Academic Year 2017- 18 admitted batch

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced CAD	25	75	4	0	0	4
PC-2	Computer Aided Manufacturing	25	75	4	0	0	4
PC-3	Advanced FEM	25	75	4	0	0	4
PE-1	1. Mechanical Behavior of Materials 2. Stress Analysis and Vibration 3. Additive Manufacturing Technologies	25	75	3	0	0	3
PE-2	1. Automation in Manufacturing 2. Computer Aided Process Planning 3. Performance Modeling and Analysis of Manufacturing Systems	25	75	3	0	0	3
OE-1	*Open Elective - I	25	75	3	0	0	3
Laboratory I	Advanced CAD/CAM Laboratory	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Design for Manufacturing and Assembly	25	75	4	0	0	4
PC-5	Flexible Manufacturing Systems	25	75	4	0	0	4
PC-6	Industrial Robotics	25	75	4	0	0	4
PE-3	1. Intelligent Manufacturing Systems 2. Special Manufacturing Process 3. Optimization Techniques and Applications	25	75	3	0	0	3
PE4	1. Advanced Mechatronics 2. MEMS and Micro Systems : Design and Manufacture 3. Fuzzy Logic and Neural Networks	25	75	3	0	0	3
OE-2	*Open Elective - II	25	75	3	0	0	3
Laboratory II	Manufacturing simulation & Precision Engineering lab	25	75	0	0	3	2
Seminar II	Seminar-II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I year II Sem. (CAD/CAM)

DESIGN FOR MANUFACTURING AND ASSEMBLY (PC – 4)

UNIT - I

Introduction: Design philosophy steps in Design process - General Design rules for manufacturability - basic principles of design Ling for economical production - creativity in design. Materials: Selection of Materials for design Developments in Material technology - criteria for material selection - Material selection interrelationship with process selection process selection charts.

UNIT - II

Machining Process: Overview of various machining processes - general design rules for machining - Dimensional tolerance and surface roughness - Design for machining - Ease - Redesigning of components for machining ease with suitable examples. General design recommendations for machined parts. **Metal Casting:** Appraisal of various casting processes, selection of casting process, - general design considerations for casting - casting tolerances - use of solidification simulation in casting design - product design rules for sand casting.

UNIT - III

Metal Joining: Appraisal of various welding processes, Factors in design of weldments - general design guidelines - pre and post treatment of welds - effects of thermal stresses in weld joints - design of brazed joints. Forging - Design factors for Forging - Closed dies forging design - parting lines of die5 drop forging die design - general design recommendations. Extrusion & Sheet Metal Work: Design guidelines for extruded sections - design principles for Punching, Blanking, Bending, Deep Drawing - Keeler Goodman Forming Line Diagram - Component Design for Blanking.

UNIT- IV

Assemble Advantages: Development of the assemble process, choice of assemble method, assemble advantages social effects of automation.

Automatic Assembly Transfer Systems: Continuous transfer, intermittent transfer, indexing mechanisms, and operator - paced free – transfer machine.

UNIT - V

Design of Manual Assembly: Design for assembly fits in the design process, general design guidelines for manual assembly, development of the systematic DFA methodology, assembly efficiency, classification system for manual handling, classification system for manual insertion and fastening, effect of part symmetry on handling time, effect of part thickness and size on handling time, effect of weight on handling time, parts requiring two hands for manipulation, effects of combinations of factors, effect of symmetry effect of chamfer design on insertion operations, estimation of insertion time.

REFERENCES:

1. Assembly Automation and Product Design/ Geoffrey Boothroyd/ Marcel Dekker Inc., NY, 1992.
2. Engineering Design - Material & Processing Approach/ George E. Deiter/McGraw Hill Intl. 2nd Ed. 2000.
3. Hand Book of Product Design/ Geoffrey Boothroyd/ Marcel and Dekken, N.Y. 1990.
4. Computer Aided Assembly London/ A Delbainbre/.
5. Product Design for Manufacturing and Assembly/ Geoffrey Boothroyd, Peter Dewhurst & Winston Anstony Knight/CRC Press/2010

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech I year II Sem. (CAD/CAM)

FLEXIBLE MANUFACTURING SYSTEMS (PC - 5)

UNIT - I:

Introduction to flexible manufacturing systems. Planning and scheduling and control of FMS. Knowledge based scheduling. The Development of Manufacturing systems. Pallets, Fixtures and Machines, work handling system layouts.

UNIT - II:

Hierarchy of computer control. Supervisory computer. System Management, Tool Management, Simulation and Analysis in the Design of FMS.

UNIT - III:

Software for simulation and database of FMS. Specification and selection, trends, application of simulation software. Simulation Modeling for FMS.

UNIT - IV:

Manufacturing data systems data flow, CAD/CAM considerations. Planning FMS database, just in time characteristics, Pull method, quality small lot sizes, work station loads, close supplier ties, flexible workforce — line flow strategy. Simulation for FMS Design.

UNIT - V:

Preventive maintenance. Karban system, implementation issues. Economic justification of FMS; Artificial Intelligence in the Design of FMS.

REFERENCES:

1. Joseph Talavage, Roger G. Hannam “ Flexible Manufacturing systems in Practice (Applications, Design and simulation)” CRC Press
2. Hand Book of Flexible Manufacturing Systems/ Jha N K/ Academic Press.
3. Production System I3eyond Large Scale Production/ Talichi Ohno/ Toyota Productivity Press India Pvt. Lid.
4. Flexible Manufacturing Systems/ H K Shivanand/New Age International/2006
5. S.R.Deb “Robotics Technology and Flexible Automation”McGraw-Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech – I year II Sem. (CAD/CAM)

INDUSTRIAL ROBOTICS (PC – 6)

UNIT - I

Introduction: Automation and Robotics, Robot anatomy, robot configuration, motions joint notation work volume, robot drive system, control system and dynamic performance, precision of movement.
Control System and Components: basic concept and modais controllers control system analysis, robot activation and feedback components. Positions sensors, velocity sensors, actuators sensors, power transmission system.

UNIT - II

Motion Analysis and Control: Manipulator kinematics, position representation forward transformation, homogeneous transformation, manipulator path control, robot dynamics, configuration of robot controller.

UNIT - III

End Effectors: Grippers-types, operation, mechanism, force analysis, tools as end effectors consideration in gripper selection and design. **SENSORS:** Desirable features, tactile, proximity and range sensors, uses sensors in robotics.

Machine Vision: Functions, Sensing and Digitizing-imaging, Devices, Lighting techniques, Analog to digital single conversion, image storage, Image processing and Analysis-image data reduction, Segmentation feature extraction. Object recognition, training the vision system, Robotics application.

UNIT - IV

Robot Programming: Lead through programming, Robot programming as a path in space, Motion interpolation, WAIT, SINONAL AND DELAY commands, Branching capabilities and Limitations.
ROBOT LANGUAGES: Textual robot Languages, Generation, Robot language structures, Elements in function.

UNIT - V

Robot Cell DESGIN AND CONTROL: Robot cell layouts-Robot centered cell, In-line robot cell, Considerations in work design, Work and control, Inter locks, Error detect ion, Work wheel controller.
Robot Application: Material transfer, Machine loading/unloading. Processing operation, Assembly and Inspection, Feature Application.

REFERENCES:

1. Robot Dynamics & Control – Mark W. Spong and M. Vidyasagar / John Wiley & Sons (ASIA) Pte Ltd.
2. Robotics and Control / Mittal R K & Nagrath I J / TMH
3. Robotics / Fu K S/ McGraw Hill.
4. Industrial Robotics / Groover M P /Pearson Edu.
5. Introduction to Robotic Mechanics and Control by JJ Craig, Pearson, 3rd edition.
6. Robotic Engineering / Richard D. Klafter, Prentice Hall
7. Robot Analysis and Intelligence / Asada and Slotine / Wiley Inter-Science.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech – I year II Sem. (CAD/CAM)

INTELLIGENT MANUFACTURING SYSTEMS (Professional Elective – 3)

UNIT - I:

Computer Integrated Manufacturing Systems Structure and functional areas of CIM system, - CAD, CAPP, **CAM**, CAQC, ASRS. Advantages of CIM. Manufacturing Communication Systems - MAP/TOP, OSI Model, Data Redundancy, Top- down and Bottom-up Approach, Volume of Information. Intelligent Manufacturing System Components, System Architecture and Data Flow, System Operation.

UNIT - II:

Components of Knowledge Based Systems - Basic Components of Knowledge Based Systems, Knowledge Representation, Comparison of Knowledge Representation Schemes, Interference Engine, Knowledge Acquisition.

UNIT - III:

Machine Learning - Concept of Artificial Intelligence, Conceptual Learning, Artificial Neural Networks - Biological Neuron, Artificial Neuron, Types of Neural Networks, Applications in Manufacturing.

UNIT - IV:

Automated Process Planning - Variant Approach, Generative Approach, Expert Systems for Process Planning, Feature Recognition, Phases of Process planning. Knowledge Based System for Equipment Selection (KBSES) - Manufacturing system design. Equipment Selection Problem, Modeling the Manufacturing Equipment Selection Problem, Problem Solving approach in KBSES, Structure of the KBSES.

UNIT - V:

Group Technology: Models and Algorithms Visual Method, Coding Method, Cluster Analysis Method, Matrix Formation - Similarity Coefficient Method, Sorting-based Algorithms, Bond Energy Algorithm, Cost Based method, Cluster Identification Method, Extended CI Method. Knowledge Based Group Technology - Group Technology in Automated Manufacturing System. Structure of Knowledge based system for group technology (KBSCIT) — Data Base, Knowledge Base, Clustering Algorithm.

REFERENCES:

1. Intelligent Manufacturing Systems/ Andrew Kusiak/Prentice Hall.
2. Artificial Neural Networks/ Yagna Narayana/PHI/2006
3. Automation, Production Systems and CIM / Groover M.P./PHI/2007
4. Neural networks: A comprehensive foundation/ Simon Haykin/ PHI.
5. Artificial neural networks/ B. Vegnanarayana/PHI
6. Neural networks in Computer intelligence/ Li Min Fu/ TMH/2003
7. Neural networks/ James A Freeman David M S kapura/ Pearson education/2004
8. Introduction to Artificial Neural Systems/Jacek M. Zurada/JAICO Publishing House Ed. 2006.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech – I year II Sem. (CAD/CAM)

SPECIAL MANUFACTURING PROCESS (Professional Elective – 3)

UNIT- I

Surface Treatment: Scope, Cleaners, Methods of cleaning, Surface coating types, and ceramic and organic methods of coating, economics of coating. Electro forming, Chemical vapor deposition, thermal spraying, Ion implantation, diffusion coating, Diamond coating and cladding.

UNIT- II

Processing of Ceramics: Applications, characteristics, classification .Processing of particulate ceramics, Powder preparations, consolidation, Drying, sintering, Hot compaction, Area of application, finishing of ceramics. Processing of Composites: Composite Layers, Particulate and fiber reinforced composites, Elastomers, Reinforced plastics, MMC, CMC, Polymer matrix composites.

UNIT- III

Fabrication of Microelectronic Devices:

Crystal growth and wafer preparation, Film Deposition oxidation, lithography, bonding and packaging, reliability and yield, Printed Circuit boards, computer aided design in micro electronics, surface mount technology, Integrated circuit economics.

UNIT - IV

E-Manufacturing: Nano manufacturing techniques and micromachining, High Speed Machining and hot machining

UNIT -V

Rapid Prototyping: Working Principles, Methods, Stereo Lithography, Laser Sintering, Fused Deposition Method, Applications and Limitations, Rapid tooling, Techniques of rapid manufacturing

REFERENCES:

1. Manufacturing Engineering and Technology / Kalpakijian / Adisson Wesley, 1995.
2. Process and Materials of Manufacturing / R. A. Lindburg / 1th edition, PHI 1990.
3. Microelectronic packaging handbook / Rao. R. Thummala and Eugene, J. Rymaszewski / Van Nostrand Renihold,
4. MEMS & Micro Systems Design and manufacture / Tai — Run Hsu / TMGH
5. Advanced Machining Processes / V.K.Jain / Allied Publications.
6. Introduction to Manufacturing Processes / John A Schey / Mc Graw Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech – I year II Sem. (CAD/CAM)

OPTIMIZATION TECHNIQUES AND APPLICATIONS (Professional Elective – 3)

UNIT - I

Single Variable Non-Linear Unconstrained Optimization: One dimensional Optimization methods:- Uni-modal function, elimination methods, ,, Fibonacci method, golden section method, interpolation methods – quadratic & cubic interpolation methods.

UNIT - II

Multi variable non-linear unconstrained optimization: Direct search method – Univariant method - pattern search methods – Powell's- Hook -Jeeves, Rosenbrock search methods- gradient methods, gradient of function, steepest decent method, Fletcher Reeves method, variable metric method.

UNIT- III

Linear Programming: Formulation – Sensitivity analysis. Change in the constraints, cost coefficients, coefficients of the constraints, addition and deletion of variable, constraints.

Simulation – Introduction – Types- steps – application – inventory – queuing systems

UNIT - IV

Integer Programming: Introduction – formulation – Gomory cutting plane algorithm – Zero or one algorithm, branch and bound method

Stochastic programming:

Basic concepts of probability theory, random variables- distributions-mean, variance, correlation, co variance, joint probability distribution- stochastic linear, dynamic programming.

UNIT- V

Geometric Programming: Polynomials – arithmetic - geometric inequality – unconstrained G.P- constrained G.P (<= TYPE ONLY)

Non-traditional optimization Techniques: Genetic Algorithms-Steps-Solving simple problems- Comparisons of similarities and dissimilarities between traditional and non-traditional techniques-Particle Swarm Optimization (PSO)- Steps(Just understanding)-Simulated Annealing-Steps-Simple problems.

REFERENCES:

1. Optimization theory & Applications / S. S. Rao / New Age International.
2. Engineering Optimization-Kalyan Deb/ PHI
3. Introductory to operation Research / Kasan & Kumar / Springer
4. Optimization Techniques theory and practice / M. C. Joshi, K. M. Moudgalya/ Narosa Publications
5. Operation Research / H. A. Taha /TMH
6. Optimization in operations research / R. L Rardin
7. Optimization Techniques /Benugundu & Chandraputla / Pearson Asia

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech – I year II Sem. (CAD/CAM)

ADVANCED MECHATRONICS (Professional Elective – 4)

UNIT - I

Mechatronics systems, elements, levels of mechatronics system, Mechatronics design process, system, measurement systems, control systems, microprocessor-based controllers, advantages and disadvantages of mechatronics systems. Sensors and transducers, types, displacement, position, proximity, velocity, motion, force, acceleration, torque, fluid pressure, liquid flow, liquid level, temperature and light sensors.

UNIT - II

Solid state electronic devices, PN junction diode, BJT, FET, DIA and TRIAC. Analog signal conditioning, amplifiers, filtering. Introduction to MEMS & typical applications.

UNIT - III

Hydraulic and pneumatic actuating systems, Fluid systems, Hydraulic and pneumatic systems, components, control valves, electro-pneumatic, hydro-pneumatic, electro-hydraulic servo systems: Mechanical actuating systems and electrical actuating systems.

UNIT - IV

Digital electronics and systems, digital logic control, micro processors and micro controllers, programming, process controllers, programmable logic controllers, PLCs versus computers, application of PLCs for control.

UNIT - V

System and interfacing and data acquisition, DAQS, SCADA, A to D and D to A conversions; Dynamic models and analogies, System response. Design of mechatronics systems & future trends.

REFERENCES:

1. MECHATRONICS Integrated Mechanical Electronics Systems/K P Ramachandran & GK Vijaya Raghavan/WILEY India Edition/2008
2. Mechatronics Electronics Control Systems in Mechanical and Electrical Engineering by W Bolton, Pearson Education Press, 3rd edition, 2005.
3. Mechatronics Source Book by Newton C Braga, Thomson Publications, Chennai.
4. Mechatronics – N. Shanmugam / Anuradha Agencies Publishers.
5. Mechatronics System Design / Devdas shetty/Richard/Thomson.
6. Mechatronics/M. D. Singh/J. G. Joshi/PHI.
7. Mechatronics – Electronic Control Systems in Mechanical and Electrical Engg. 4th Edition, Pearson, 2012 W. Bolton
8. Mechatronics – Principles and Application Godfrey C. Onwubolu, Wlsevier, 2006 Indian print

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech – I year II Sem. (CAD/CAM)

MEMS AND MICRO SYSTEMS: DESIGN AND MANUFACTURE (Professional Elective – 4)

UNIT - I:

Overview and Working Principles of MEMS and Microsystems

MEMS & Microsystems, Evolution of Micro fabrication, Microsystems & Microelectronics, Microsystems & Miniaturization, Applications of MEMS in Industries, Micro sensors, Micro actuation, MEMS with Micro actuators Micro accelerometers, Micro fluidics.

UNIT - II:

Engineering Science for Microsystems Design and Fabrication:

Atomic structure of Matter, Ions and Ionization, Molecular Theory of Matter and Intermolecular Force, Doping of Semiconductors, The diffusion Process, Plasma Physics, Electrochemistry, Quantum Physics

UNIT - III:

Engineering Mechanics for Microsystems Design:

Static Bending of thin Plates, Mechanical Vibration, Thermo mechanics Fracture Mechanics, Thin-Film Mechanics, Overview of Finite Element Stress Analysis

UNIT - IV:

Thermo Fluid Engineering & Microsystems Design:

Overview of Basics of Fluid Mechanics in Macro and Meso scales, Basic equations in Continuum Fluid dynamics, Laminar Fluid Flow in Circular Conduits, Computational Fluid Dynamics, Incompressible Fluid Flow in Micro conduits, Fluid Flow in Sub micrometer and Nano scale, Overview of Heat conduction in Solids, Heat Conduction in Multilayered Thin films and in solids in sub micrometer scale, Design Considerations, Process Design Mechanical Design, Mechanical Design using FEM, Design of a Silicon Die for a Micro pressure Sensor.

UNIT - V:

Materials for MEMS & Microsystems and Their Fabrication:

Substrates and Wafers, Active substrate materials, Silicon as a substrate material, Silicon Compounds, Silicon Piezoresistors, Gallium Arsenide, Quartz, Piezoelectric Crystals and Polymers, Photolithography, Ion implantation, Diffusion and oxidation, chemical and physical vapor deposition, Etching, Bulk micro manufacturing, Surface Micromachining, The LIGA Process

REFERENCES:

1. MEMS & Microsystems: Design & Manufacture/ Tai-Ran Hsu/Tata Mc-Graw Hill., ed./2002
2. An Introduction to Microelectromechanical Systems Engineering/ Maluf, M./ Artech House, Boston, 2000
3. Micro robots and Micromechanical Systems/ Trimmer, W.S.N/ Sensors & Actuators, vol19, no.1989.
4. Applied Partial Differential Equations/ Trim, D.W/ PWS-Kent Publishing/ Boston 1990.
5. Fundamentals of Microfabrication. Madou, M/ CRC Press, Boca Raton, 1997.
6. The Finite Element Method in Thermomechanics/ Hsu, T.R / Alien & Unwin, London.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

FUZZY LOGIC AND NEURAL NETWORKS (Professional Elective – 4)

UNIT- I

Fuzzy Set Theory and Fuzzy Logic Control:

Basic concepts of fuzzy sets- Operations on fuzzy sets- Fuzzy relation equations- Fuzzy logic control- Fuzzification –Defuzzification- Knowledge base- Decision making logic- Membership functions – Rule base.

UNIT- II

Adaptive Fuzzy Systems:

Performance index- Modification of rule base- Modification of membership functions- Simultaneous modification of rule base and membership functions- Genetic algorithms-Adaptive fuzzy system- Neuro fuzzy systems.

UNIT- III

Artificial Neural Networks:

Introduction- History of neural networks- multilayer perceptions- Back propagation algorithm and its Variants- Different types of learning, examples.

UNIT- IV

Mapping and Recurrent Net works:

Counter propagation –Self organization Map- Cognitron and Neocognitron- Hopfield Net- Kohonen Nets- Grossberg Nets- Art-I, Art-II reinforcement learning

UNIT- V

Case Studies:

Application of fuzzy logic and neural networks to Measurement- Control- Adaptive Neural Controllers – Signal Processing and Image Processing

TEXT BOOK:

1. Vallum B. R And Hayagriva V.R C++, Neural networks and Fuzzy logic, BPB Publications, New Delhi, 1996

REFERENCE BOOKS:

1. Fuzzy logic & Neural Networks/ Chennakesava R. Alavala/ New Age International, 2008
2. Neural Networks for control, Millon W. T, Sutton R.S and Werbos P.J, MIT Press 1992
3. Fuzzy sets Fuzzy logic, Klir, G.J and Yuan B.B Prentice Hall of India Pvt. Ltd., New Delhi
4. Neural Networks and Fuzzy systems, Kosko.. Prentice hall of India Pvt. Ltd., New Delhi 1994
5. Introduction to Fuzzy control, Dirankov D. Hellendoorn H, Reinfrank M., Narosa Publications House, New Delhi 1996
6. Introduction to Artificial Neural systems, Zurada J. M Jaico Publishing House, New Delhi 1994

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

MANUFACTURING SIMULATION & PRECISION ENGINEERING LAB

A. MANUFACTURING SIMULATION

The students will be given training on the use and application of the following software to manufacturing problems:

1. Auto MOD Software.
2. PROMODEL
3. SLAM-II
4. CAFIMS
5. Flexsim

They also learn how to write sub routines in C-language and interlinking with the above packages.

Problems for modelling and simulation experiments:

1. AGV planning
2. ASRS simulation and performance evaluation
3. Machines, AGVs and AS/RS integrated problems
4. JIT system
5. Kanban flow
6. Material handling systems
7. M.R.P. Problems
8. Shop floor scheduling etc.

B. PRECISION ENGINEERING

1. Hydraulic and Pneumatic circuits
2. Closed loop control systems
3. Study of the chip formation in turning process
4. Study of operation of tool and cutter grinder, twist drill grinder, Centreless grinder
5. Determination of cutting forces in turning
6. Experiments in unconventional manufacturing processes-AJM and study of USM, EDM, Laser Machining and Plasma spraying
7. Inspection of parts using tool makers microscope, roughness and form tester
8. Study of micro-controllers, programming on various CNC machine tools and also controllers
9. Studies on PLC programming
10. Study and programming of robots
11. Condition monitoring in machining process using acoustic emission.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech in CAD/CAM
Effective from Academic Year 2017- 18 admitted batch

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced CAD	25	75	4	0	0	4
PC-2	Computer Aided Manufacturing	25	75	4	0	0	4
PC-3	Advanced FEM	25	75	4	0	0	4
PE-1	1. Mechanical Behavior of Materials 2. Stress Analysis and Vibration 3. Additive Manufacturing Technologies	25	75	3	0	0	3
PE-2	1. Automation in Manufacturing 2. Computer Aided Process Planning 3. Performance Modeling and Analysis of Manufacturing Systems	25	75	3	0	0	3
OE-1	*Open Elective - I	25	75	3	0	0	3
Laboratory I	Advanced CAD/CAM Laboratory	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Design for Manufacturing And Assembly	25	75	4	0	0	4
PC-5	Flexible Manufacturing Systems	25	75	4	0	0	4
PC-6	Industrial Robotics	25	75	4	0	0	4
PE-3	1. Intelligent Manufacturing Systems 2. Special Manufacturing Process 3. Optimization Techniques and Applications	25	75	3	0	0	3
PE4	1. Advanced Mechatronics 2. MEMS and Micro Systems : Design and Manufacture 3. Fuzzy Logic and Neural Networks	25	75	3	0	0	3
OE-2	*Open Elective - II	25	75	3	0	0	3
Laboratory II	Manufacturing simulation & Precision Engineering lab	25	75	0	0	3	2
Seminar II	Seminar-II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

ADVANCED CAD

(PC-1)

UNIT- I:

CAD Tools: Definition of CAD Tools, Graphics standards, Graphics software: requirements of graphics software, Functional areas of CAD, Efficient use of CAD software.

Basics of Geometric Modelling: Requirement of geometric modeling, Geometric models, Geometric construction methods, Modelling facilities desired.

UNIT- II:

Geometric modelling: Classification of wireframe entities, Curve representation methods, Parametric representation of analytic curves: line, circle, arc, conics, Parametric representation of synthetic curves: Hermite cubic curve, Bezier curve, B-Spline curvewire, NURBS, Curve manipulations.

UNIT- III:

Surface Modeling : Classification of surface entities, Surface representation methods, Parametric representation of analytic surfaces: plane surface, ruled surface, surface of revolution, tabulated cylinder, Parametric representation of synthetic curves: Hermite cubic surface, Bezier surface, B-Spline surface, Blending surface, Surface manipulations.

UNIT- IV:

Solid Modelling: Geometry and topology, Boundary representation, The Euler-Poincare formula, Euler operators, Constructive solid geometry: CSG primitives, Boolean operators, CSG expressions, Interior, Exterior, closure, Sweeping: linear and non-linear, Solid manipulations.

UNIT- V:

Transformations: 2-D and 3-D transformations: translation, scaling, rotation, reflection, concatenation, homogeneous coordinates, Perspective projection, orthographic projection, isometric projection, Hidden surface removal, shading, rendering.

Evaluation Criteria: Evaluation criteria of CAD software, Data exchange formats: GKS, IGES, PHIGS, CGM, STEP

Dimensioning and tolerances: Linear, angular, angular dimensions, maximum material condition (MMC), Least material condition (LMC), Regardless of feature size (RFS).

REFERENCES:

1. CAD/CAM Concepts and Applications/ Alavala/ PHI.
2. Mastering CAD/CAM / Ibrahim Zeid / McGraw Hill International.
3. CAD/CAM Principles and Applications/ P.N. Rao/TMH/3rd Edition
4. CAD/CAM /Groover M.P./ Pearson education
5. CAD / CAM / CIM, Radhakrishnan and Subramanian/ New Age
6. Principles of Computer Aided Design and Manufacturing/ Farid Amirouche/ Pearson
7. Computer Numerical Control Concepts and programming/ Warren S Seames/ Thomson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

COMPUTER AIDED MANUFACTURING (PC-2)

UNIT - I

Computer-Aided Programming: General information, APT programming, Examples Apt programming problems (2D machining only). NC programming on CAD/CAM systems, the design and implementation of post processors .Introduction to CAD/CAM software, Automatic Tool Path generation.

UNIT - II

Tooling for CNC Machines: Interchangeable tooling system, preset and qualified tools, coolant fed tooling system, modular fixturing, quick change tooling system, automatic head changers. DNC Systems and Adaptive Control: Introduction, type of DNC systems, advantages and disadvantages of DNC, adaptive control with optimization, Adaptive control with constraints, Adaptive control of machining processes like turning, grinding.

UNIT - III

Post Processors for CNC:

Introduction to Post Processors: The necessity of a Post Processor, the general structure of a Post Processor, the functions of a Post Processor, DAPP — based- Post Processor: Communication channels and major variables in the DAPP — based Post Processor, the creation of a DAPP — Based Post Processor.

UNIT - IV

Micro Controllers: Introduction, Hardware components, I/O pins, ports, external memory:, counters, timers and serial data I/O interrupts. Selection of Micro Controllers Embedded Controllers, Applications, and Programming of Micro Controllers. Programming Logic Controllers (PLC' s): Introduction, Hardware components of PLC, System, basic structure, principle of operations, Programming mnemonics timers, Internal relays and counters, Applications of PLC's in CNC Machines.

UNIT - V

Computer Aided Process Planning: Hybrid CAAP System, Computer Aided Inspection and quality control, Coordinate Measuring Machine, Limitations of CMM, Computer Aided Testing, Optical Inspection Methods, Artificial Intelligence and expert system: Artificial Neural Networks, Artificial Intelligence in CAD, Experts systems and its structures.

REFERENCES:

1. P.N. Rao, N. K. Tewari, T K Kundra “ Computer Aided Manufacturing” McGraw Hill
2. CAD/CAM Principles and Applications, P.N. Rao, TMH
3. Computer Control of Manufacturing Systems / Yoram Koren / McGraw Hill. 1983.
4. CAD / CAM / CIM, Radha krishnan and Subramanian, New Age
5. Computer Numerical Control Concepts and programming, Warren S Seames, Thomson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

**ADVANCED FINITE ELEMENT METHODS
(PC-3)**

UNIT-I:

Introduction to FEM, basic concepts, historical back ground, applications of FEM, general description, comparison of FEM with other methods, variational approach, Glerkin's Methods. Co-ordinates, basic element shapes, interpolation function, Virtual energy principle, Rayleigh – Ritz method, properties of stiffness matrix, treatment of boundary conditions, solution of system of equations, shape functions and characteristics, Basic equations of elasticity, strain- displacement relations.

UNIT-II:

1-D Structural Problems: Axial bar element – stiffness matrix, load vector, temperature effects, Quadratic shape functions and problems.

Analysis of Trusses: Plane Trusses and Space Truss elements and problems

Analysis of BECAD/CAM: Hermite shape functions – stiffness matrix – Load vector – Problems.

UNIT-III:

2-D problems: CST, LST, force terms, Stiffness matrix and load vectors, boundary conditions, Isoparametric elements – quadrilateral element, shape functions – Numerical Integration. Finite element modeling of Axi-symmetric solids subjected to Axi-symmetric loading with triangular elements.

3-D Problems: Tetrahedran element – Jacobian matrix – Stiffness matrix.

UNIT-VI:

Scalar Field Problems: 1-D Heat conduction-Slabs – fins - 2-D heat conduction problems – Introduction to Torsional problems.

UNIT-V:

Dynamic considerations, Dynamic equations – consistent mass matrix – Eigen Values, Eigen vector, natural frequencies – mode shapes – modal analysis.

REFERENCES:

1. Finite Element Methods: Basic Concepts and applications, Alavala, PHI.
2. Finite Element Method – Zencowitz / McGraw Hill
3. The Finite Element Methods in Engineering / SS Rao / Pergamon.
4. Introduction to Finite Elements in Engineering, Chandrupatla, Ashok and Belegundu, Prentice – Hall
5. Introduction to Finite element analysis- S.Md. Jalaludeen, Anuradha Publications, print-2012
6. A First Course in the Finite Element Method/Daryl L Logan/ Cengage Learning/5th Edition
7. Finite Element Method – Krishna Murthy / TMH
8. Finite Element Analysis – Bathe / PHI

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I year I Sem. (CAD/CAM)

MECHANICAL BEHAVIOUR OF MATERIALS (PE – I)

UNIT-I:

Introduction to Deformation Behaviour: Concept of stresses and strains, engineering stresses and strains, Different types of loading and temperature encountered in applications, Tensile Test - stress-strain response for metal, ceramic and polymer, elastic region, yield point, plastic deformation, necking and fracture, Bonding and Material Behaviour, theoretical estimates of yield strength in metals and ceramics.

UNIT-II:

Elasticity Theory: The State of Stress and strain, stress and strain tensor, tensor transformation, principal stress and strain, elastic stress-strain relation, anisotropy, elastic behaviour of metals, ceramics and polymers.

Yielding and Plastic Deformation: Hydrostatic and Deviatoric stress, Octahedral stress, yield criteria and yield surface, texture and distortion of yield surface, Limitation of engineering strain at large deformation, true stress and true strain, effective stress, effective strain, flow rules, strain hardening, Ramberg Osgood equation, stress -strain relation in plasticity, plastic deformation of metals and polymers

UNIT-III:

Microscopic view of plastic deformation: crystals and defects, classification of defects, thermodynamics of defects, geometry of dislocations, slip and glide, dislocation generation - Frank Read and grain boundary sources, stress and strain field around dislocations, force on dislocation - self-stress, dislocation interactions, partial dislocations, twinning, dislocation movement and strain rate, deformation behavior of single crystal, critical resolved shear stress (CRSS), deformation of polycrystals - Hall-Petch and other hardening mechanisms, grain size effect - source limited plasticity, Hall-Petch breakdown, dislocations in ceramics and glasses.

UNIT-IV:

Fracture: fracture in ceramics, polymers and metals, different types of fractures in metals, fracture mechanics - Linear fracture mechanics -KIC, elasto-plastic fracture mechanics - JIC, Measurement and ASTM standards, Design based on fracture mechanics, effect of environment, effect of microstructure on KIC and JIC, application of fracture mechanics in the design of metals, ceramics and polymers

UNIT-V:

Deformation under cyclic load - Fatigue: S-N curves, Low and high cycle fatigue, Life cycle prediction, Fatigue in metals, ceramics and polymers

Deformation at High temperature: Time dependent deformation - creep, different stages of creep, creep and stress rupture, creep mechanisms and creep mechanism maps, creep under multi-axial loading, microstructural aspects of creep and design of creep resistant alloys, high temperature deformation of ceramics and polymers.

REFERENCES:

1. G.E. Dieter, "Mechanical Metallurgy", McGraw-Hill, 1986.
2. R.W. Hertzberg, "Deformation and Fracture Mechanics of Engineering Materials", John Wiley and Sons, 1976.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

**STRESS ANALYSIS AND VIBRATION
(P E – I)**

UNIT-I:

Two dimensional elasticity theory in Cartesian coordinates, plane stress problem in polar coordinates
Thick cylinders, Rotating discs - stress concentration.

UNIT- II:

Torsion of non circular prismatic sections, rectangular and axisymmetric, Circular plates, introduction
to shell theory — contact stresses.

UNIT- III:

Single degree freedom, two degree freedom system without and with damping - Free and forced
vibrations. Transient vibrations.

UNIT- IV:

Transient vibrations of single and two degree freedom systems, multi-degree of freedom systems -
applications of matrix methods , continuous systems.

UNIT -V:

Free and forced vibrations of strings bars and be CAD/CAM. Principle of orthogonality - classical and
energy methods.

REFERENCES:

1. Theory of Elasticity/Timoshenko S.P. and Goodier J. N./ Koakusha Publishers
2. Advanced strength of materials / Den Hortog J.P./Torrent
3. Mechanical Vibrations/ Den Ilartog J.P./ Dover Publications
4. Theory of Vibrations with Applications/ Thomson W.T./ CBS Publishing
5. Mechanical Vibrations/ Rao S.S./ Addison Wesley Longman

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I year I Sem. (CAD/CAM)

ADDITIVE MANUFACTURING TECHNOLOGIES (PE – I)

UNIT-I

Introduction: Introduction to Prototyping, Traditional Prototyping Vs Rapid Prototyping (RP), Need for time compression in product development, Usage of RP parts, Generic RP process, Distinction between RP and CNC and other related technologies, Classification of RP, Need for RP software, MIMICS, Magics, SurgiGuide, 3-matic, 3D-Doctor, Simplant, Velocity2, VoXim, SolidView, 3D View, etc., Preparation of CAD models, Problems with STL files, STL file manipulation, RP data formats: SLC, CLI, RPI, LEAF, IGES, HP/GL, CT,STEP.

UNIT-II

RP Processes:

- a) **Photopolymerization RP Processes:**-Stereolithography (SL), SL resin curing process, SL scan patterns, Microstereolithography, Applications of Photopolymerization processes.
- b) **Power Bed Fusion RP Processes:**-Stereolithography (SL), SL resin curing process, SL scan patterns, Microstereolithography. Applications of Photopolymerization Processes.
- c) **Extrusion Based RP Processes:** Fused Deposition Modelling (FDM), Principles, Plotting and path control, Applications of Extrusion-Based Processes
- d) **Printing RP Processes:** 3D printing (3DP), Research achievements in printing deposition, Technical challenges in printing, Printing process modeling, Application of Printing Process
- e) **Sheet Lamination RP Processes:** Laminated Object Manufacturing (LOM), Ultrasonic Consolidation (UC), Gluing, Thermal bonding, LOM and UC applications
- f) **Beam Deposition RP Processes:** Laser Engineered Net Shaping (LENS), Direct Metal Deposition (DMD), Processing-structure-properties, relationships, Benefits and drawbacks.

UNIT-III

Rapid tooling: Conventional Tooling Vs. Rapid Tooling, Classification of Rapid Tooling, Direct and Indirect Tooling Methods, Soft and Hard Tooling methods

UNIT-IV

Reverse engineering: Reverse Engineering (RE) Methodologies and Techniques, Selection of RE systems, RE software, RE hardware, RE in product development

UNIT-V

Errors in RP processes and applications: Pre-processing, processing, post-processing errors, Part building errors in SLA, SLS, etc., Design, Engineering Analysis and planning applications, Rapid Tooling, Reverse Engineering, Medical Applications of RP

REFERENCE BOOKS:

- 1 Chua Chee Kai., Leong Kah Fai., Chu Sing Lim, Rapid Prototyping: Principles and Applications in Manufacturing, World Scientific, 2010.
- 2 Ian Gibson., David W Rosen., Brent Stucker, Additive Manufacturing Technologies: Rapid Prototyping to Direct Digital Manufacturing, Springer, 2010.
- 3 Rafiq Noorani, Rapid Prototyping: Principles and Applications in Manufacturing, John Wiley & Sons, 2006.
- 4 D.T. Pham, S.S. Dimov, Rapid Manufacturing: The Technologies and Applications of Rapid Prototyping and Rapid Tooling, Springer, 2011
- 5 Amit Bandyopadhyay, Additive Manufacturing, CRC Press 2015.
- 6 T.S. Srivatsan, T.S. Sudharshan, CRC Press 2015

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

**AUTOMATION IN MANUFACTURING
(P E – II)**

UNIT – I

Over View of Manufacturing and Automation: Production systems, Automation in production systems, Automation principles and strategies, Manufacturing operations, production facilities. Basic elements of an automated system, levels of automation; Hardware components for automation and process control, programmable logic controllers and personal computers.

UNIT – II:

Material Handling and Identification Technologies: Material handling, equipment, Analysis. Storage systems, performance and location strategies, Automated storage systems, AS/RS, types. Automatic identification methods, Barcode technology, RFID.

UNIT – III:

Manufacturing Systems and Automated Production Lines: Manufacturing systems: components of a manufacturing system, Single station manufacturing cells; Manual Assembly lines, line balancing Algorithms, Mixed model Assembly lines, Alternative Assembly systems. Automated production lines, Applications, Analysis of transfer lines.

UNIT – IV:

Automated Assembly Systems: Fundamentals, Analysis of Assembly systems. Cellular manufacturing, part families, cooling, production flow analysis. Group Technology and flexible Manufacturing systems, Quantitative Analysis.

UNIT – V:

Quality Control and Support Systems: Quality in Design and manufacturing, inspection principles and strategies, Automated inspection, contact Vs non contact, CMM. Manufacturing support systems. Quality function deployment, computer aided process planning, concurrent engineering, shop floor control, just in time and lean production.

REFERENCES:

1. Automation, production systems and computer integrated manufacturing/ Mikell. P Groover/PHI/3rd edition/2012.
2. Automation, Production Systems and CIM/ Mike J P. Grower/PHI
3. CAD/CAM/CIM/ P. Radha Krishnan & S. Subrahmanyam and Raju /New Age International Publishers/2003.
4. System Approach to Computer Integrated Design and Manufacturing/ Singh/John Wiley /96.
5. Computer Aided Manufacturing/Tien-Chien Chang, Richard A. Wysk and Hsu-Pin Wang/ Pearson/ 2009.
6. Manufacturing and Automation Technology / R Thomas Wright and Michael Berkeihiser / Good Heart/Willcox Publishers.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

**COMPUTER AIDED PROCESS PLANNING
(P E – II)**

UNIT-I:

Introduction: The Place of Process Planning in the Manufacturing cycle-Process planning and production Planning-Process planning and Concurrent Engineering, CAPP, Group Technology.

UNIT-II:

Part Design Representation: Design Drafting-Dimensioning-Conventional Tolerance- Geometric Tolerance-CAD-input/output devices-Topology - Geometric transformation-Perspective transformation-Data Structure-Geometric modeling for process planning--GT Coding-The OPITZ system-The MICLASS System.

UNIT-III;

Process Engineering and Process Planning: Experience based planning-Decision table and Decision trees-Process capability analysis-Process planning-Variant process planning-Generative approach-Forward and backward planning, Input format, AI.

UNIT-IV

Computer Aided Process Planning Systems: Logical Design of process planning- Implementation considerations-Manufacturing system components, Production Volume, No. of production families - CAM-I, CAPP, MIPLAN, APPAS, AUTOPLAN and PRO, CPPP.

UNIT-V

An Integrated Process Planning Systems: Totally integrated process planning systems-An Overview-Modulus structure-Data Structure-Operation-Report Generation, Expert process planning

REFERENCE BOOKS:

1. Gideon Halevi and Roland D. Weill, "Principle of process planning- A Logical Approach", Chapman & Hall, 1995
2. Chang T. C. & Richard A.Wysk, "An Introduction to automated process planning systems", Prentice Hall 1985
3. Chang, T.C., "An Expert Process Planning System", Prentice Hall, 1985
4. Nanua Singh, "Systems Approach to Computer Integrated Design and Manufacturing", John Wiley & Sons, 1996
5. Rao P.N., "Computer Aided Manufacturing", Tata McGraw Hill Publishing Co., 2000.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

**PERFORMANCE MODELING AND ANALYSIS OF MANUFACTURING SYSTEMS
(PE – II)**

UNIT I:

Manufacturing Systems & Control: Automated Manufacturing Systems – Modeling – Role of performance modeling – simulation models-Analytical models. Product cycle – Manufacturing automation – Economics of scale and scope – input/output model – plant configurations. Performance measures – Manufacturing lead time – Work in process – Machine utilization – Throughput – Capacity – Flexibility – Performability – Quality Control Systems – Control system architecture – Factory communications – Local area network interconnections – Manufacturing automation protocol – Database management system.

UNIT II:

Manufacturing Processes: Examples of stochastics processes – Poisson process - Discrete time Markov chain models – Definition and notation – Sojourn times in states – Examples of DTMCs in manufacturing – Chapman – Kolmogorov equation – Steady-state analysis. Continuous Time Markov Chain Models – Definitions and notation – Sojourn times in states – examples of CTMCs in manufacturing – Equations for CTMC evolution – Markov model of a transfer line. Birth and Death Processes in Manufacturing – Steady state analysis of BD Processes – Typical BD processes in manufacturing.

UNIT III:

Queuing Model: Notation for queues – Examples of queues in manufacturing systems – Performance measures – Little's result – Steady state analysis of M/M/m queue, queues with general distributions and queues with breakdowns – Analysis of a flexible machine center.

UNIT IV:

Queuing Networks: Examples of QN models in manufacturing – Little's law in queuing networks – Tandem queue – An open queuing network with feedback – An open central server model for FMS – Closed transfer line – Closed server model – Garden Newell networks.

UNIT V:

Petrinets: Classical Petri Nets – Definitions – Transition firing and reachability – Representational power – properties – Manufacturing models.
Stochastic Petri Nets – Exponential timed Petri Nets – Generalized Stochastic Petri Nets – modeling of KANBAN systems – Manufacturing models.

REFERENCES:

1. Performance Modelling of Automated Manufacturing Systems/ Viswanadham, N and Narahari, Y/ Prentice Hall of India, New Delhi, 1994
2. Probability and Statistics with Reliability, Queuing and Computer Science Applications/ Trivedi, K.S./ Prentice Hall, New Jersey, 1982.
3. Fundamentals of Mathematical Statistics/ Gupta S.C. & Kapoor V.K./ 3rd Edition, Delhi, 1988

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I year I Sem. (CAD/CAM)

ADVANCED CAD/CAM LAB

Features and selection of CNC turning and milling centers. Practice in part programming and operation of CNC turning machines, subroutine techniques and use of cycles. Practice in part programming and operating a machining center, tool panning and selection of sequences of operations, tool setting on machine, practice in APT based NC programming. Practice in Robot programming and its languages. Robotic simulation using software. Robo path control, preparation of various reports and route sheets, Simulation of manufacturing system using CAM software, controller operating system commands.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech in COMPUTER SCIENCE AND ENGINEERING
Common to (CS, CSE)

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Algorithms	25	75	4	0	0	4
PC-2	Computer Networking	25	75	4	0	0	4
PC-3	Software Engineering	25	75	4	0	0	4
PE-1	1. Network Security and Cryptography 2. Mobile Application Development 3. Graph Theory 4. Internet of Things	25	75	3	0	0	3
PE-2	1. Game Theory 2. Parallel and Distributed Algorithms 3. Software Architecture and Design Patterns 4. Embedded Systems	25	75	3	0	0	3
OE-1	*Open Elective – 1	25	75	3	0	0	3
Laboratory I	Advanced Algorithms Lab	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Network Programming	25	75	4	0	0	4
PC-5	Distributed Systems and Cloud Computing	25	75	4	0	0	4
PC-6	Theory of Computation	25	75	4	0	0	4
PE-3	1. Data Warehousing and Data Mining 2. Storage Area Networks 3. Semantic Web and Social Networks 4. Cyber Security	25	75	3	0	0	3
PE4	1. Big Data Analytics 2. Soft Computing 3. Software Process and Project Management 4. Machine Learning	25	75	3	0	0	3
OE-2	*Open Elective – 2	25	75	3	0	0	3
Laboratory II	Internet Technologies and Services Lab	25	75	0	0	3	2
Seminar II	Seminar -II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech - CSE/CS – I Year – II Sem

NETWORK PROGRAMMING (PC – 4)

Course Objectives:

- To understand to Linux utilities
- To understand file handling, signals
- To understand IPC, network programming in Java
- To understand processes to communicate with each other across a Computer Network.

UNIT – I

Linux Utilities- File handling utilities, Security by file permissions, Process utilities, Disk utilities, Networking utilities, Filters, Text processing utilities and Backup utilities.

Bourne again shell(bash) - Introduction, pipes and redirection, here documents, running a shell script, the shell as a programming language, shell meta characters, file name substitution, shell variables, command substitution, shell commands, the environment, quoting, test command, control structures, arithmetic in shell, shell script examples.

Review of C programming concepts-arrays, strings (library functions), pointers, function pointers, structures, unions, libraries in C.

UNIT - II

Files- File Concept, File types File System Structure, Inodes, File Attributes, file I/O in C using system calls, kernel support for files, file status information-stat family, file and record locking-lockf and fcntl functions, file permissions- chmod, fchmod, file ownership-chown, lchown , fchown, links-soft links and hard links – symlink, link, unlink.

File and Directory management – Directory contents, Scanning Directories- Directory file APIs.

Process- Process concept, Kernel support for process, process attributes, process control – process creation, replacing a process image, waiting for a process, process termination, zombie process, orphan process.

UNIT - III

Signals- Introduction to signals, Signal generation and handling, Kernel support for signals, Signal function, unreliable signals, reliable signals, kill, raise , alarm, pause, abort, sleep functions.

Interprocess Communication - Introduction to IPC mechanisms, Pipes- creation, IPC between related processes using unnamed pipes, FIFOs-creation, IPC between unrelated processes using FIFOs(Named pipes), differences between unnamed and named pipes, popen and pclose library functions, Introduction to message queues, semaphores and shared memory.

Message Queues- Kernel support for messages, UNIX system V APIs for messages, client/server example.

Semaphores-Kernel support for semaphores, UNIX system V APIs for semaphores.

UNIT – IV

Shared Memory- Kernel support for shared memory, UNIX system V APIs for shared memory, client/server example.

Network IPC - Introduction to Unix Sockets, IPC over a network, Client-Server model ,Address formats(Unix domain and Internet domain), Socket system calls for Connection Oriented - Communication, Socket system calls for Connectionless-Communication, Example-Client/Server Programs- Single Server-Client connection, Multiple simultaneous clients, Socket options – setsockopt, getsockopt, fcntl.

UNIT-V

Network Programming in Java-Network basics, TCP sockets, UDP sockets (datagram sockets), Server programs that can handle one connection at a time and multiple connections (using multithreaded server), Remote Method Invocation (Java RMI)-Basic RMI Process, Implementation details-Client-Server Application.

TEXT BOOKS:

1. Unix System Programming using C++, T.Chan, PHI.(Units II,III,IV)
2. Unix Concepts and Applications, 4th Edition, Sumitabha Das, TMH.(Unit I)
3. An Introduction to Network Programming with Java, Jan Graba, Springer, rp 2010.(Unit V)
4. Unix Network Programming ,W.R. Stevens, PHI.(Units II,III,IV)
5. Java Network Programming,3rd edition, E.R. Harold, SPD, O'Reilly.(Unit V)

REFERENCE BOOKS:

1. Linux System Programming, Robert Love, O'Reilly, SPD.
2. Advanced Programming in the UNIX environment, 2nd Edition, W.R.Stevens, Pearson Education.
3. UNIX for programmers and users, 3rd Edition, Graham Glass, King Ables, Pearson Education.
4. Beginning Linux Programming, 4th Edition, N.Matthew, R.Stones, Wrox, Wiley India Edition.
5. Unix Network Programming The Sockets Networking API, Vol.-I,W.R.Stevens, Bill Fenner, A.M.Rudoff, Pearson Education.
6. Unix Internals, U.Vahalia, Pearson Education.
7. Unix shell Programming, S.G.Kochan and P.Wood, 3rd edition, Pearson Education.
8. C Programming Language, Kernighan and Ritchie, PHI

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech - CSE/CS – I Year – II Sem

DISTRIBUTED SYSTEMS AND CLOUD COMPUTING (PC – 5)

Course Objectives:

- To explain the evolving computer model called cloud computing.
- To introduce the various levels of services that can be achieved by cloud.
- To describe the security aspects in cloud.

Course Outcomes:

- Ability to understand the virtualization and cloud computing concepts.

UNIT- I

Systems Modeling, Clustering and Virtualization: Distributed System Models and Enabling Technologies, Computer Clusters for Scalable Parallel Computing, Virtual Machines and Virtualization of Clusters and Data centers.

UNIT- II

Foundations: Introduction to Cloud Computing, Migrating into a Cloud, Enriching the 'Integration as a Service' Paradigm for the Cloud Era, The Enterprise Cloud Computing Paradigm.

UNIT- III

Infrastructure as a Service (IAAS) & Platform and Software as a Service (PAAS / SAAS): Virtual machines provisioning and Migration services, On the Management of Virtual machines for Cloud Infrastructures, Enhancing Cloud Computing Environments using a cluster as a Service, Secure Distributed Data Storage in Cloud Computing.

Aneka, Comet Cloud, T-Systems', Workflow Engine for Clouds, Understanding Scientific Applications for Cloud Environments.

UNIT- IV

Monitoring, Management and Applications: An Architecture for Federated Cloud Computing, SLA Management in Cloud Computing, Performance Prediction for HPC on Clouds, Best Practices in Architecting Cloud Applications in the AWS cloud, Building Content Delivery networks using Clouds, Resource Cloud Mashups.

UNIT - V

Governance and Case Studies: Organizational Readiness and Change management in the Cloud age, Data Security in the Cloud, Legal Issues in Cloud computing, Achieving Production Readiness for Cloud Services.

TEXT BOOKS:

1. Cloud Computing: Principles and Paradigms by Rajkumar Buyya, James Broberg and Andrzej M. Goscinski, Wiley, 2011.
2. Distributed and Cloud Computing, Kai Hwang, Geoffery C.Fox, Jack J.Dongarra, Elsevier, 2012.

REFERENCE BOOKS:

1. Cloud Computing : A Practical Approach, Anthony T.Velte, Toby J.Velte, Robert Elsenpeter, Tata McGraw Hill, 2011.
2. Enterprise Cloud Computing, Gautam Shroff, Cambridge University Press, 2010.

3. Cloud Computing: Implementation, Management and Security, John W. Rittinghouse, James F. Ransome, CRC Press, rp2012.
4. Cloud Application Architectures: Building Applications and Infrastructure in the Cloud, George Reese, O'Reilly, SPD, rp2011.
5. Cloud Security and Privacy: An Enterprise Perspective on Risks and Compliance, Tim Mather, Subra Kumaraswamy, Shahed Latif, O'Reilly, SPD, rp2011.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech - CSE/CS – I Year – II Sem

THEORY OF COMPUTATION (PC – 6)

Course Outcomes

- Able to understand the concept of abstract machines and their power to recognize the languages.
- Able to employ finite state machines for modeling and solving computing problems.
- Able to design context free grammars for formal languages.
- Able to distinguish between decidability and undecidability.
- Able to gain proficiency with mathematical tools and formal methods.

UNIT - I:

Regular Languages –Finite Automata, Formal definition of finite automaton, Examples of finite automata, Formal definition of computation, Designing finite automata, The regular operations, Non determinism, formal definition of nondeterministic finite automaton, equivalence of NFAs and DFAs, closure under the regular operations, Regular Expressions, formal definition of a regular expression, equivalence with finite automata, Nonregular languages, The pumping lemma for regular languages.

UNIT - II :

Context-Free languages, Context-free grammars, formal definition of a Context-free grammar, Examples of context-free grammars, Designing context-free grammars, Ambiguity, Chomsky normal form, Pushdown Automata, Examples of pushdown Automata, Equivalence with context-free grammars, Non-context-free languages, The pumping lemma for context-free languages.

UNIT - III :

The Church-Turing Thesis - Turing machines, Formal definition of turing machine, Examples of turing machines, Variants of turing machines, Multitape turing machines, Nondeterministic turing machine, Enumerators, Equivalence with other models, The definition of algorithm, Hilbert's problem, Terminology of describing turing machines.

UNIT - IV:

Decidability – Decidable languages, Decidable problems concerning regular languages, Decidable problems concerning context-free languages, The halting problem, The diagonalization method, The halting method is undecidable, A turing –unrecognizable language, **Reducibility** – Undecidable problems for language theory, Reductions via computations histories, A simple undecidable problem, Mapping reducibility, computable functions, Formal definition of mapping reducibility.

UNIT - V:

Time Complexity – Measuring complexity, Big – O and small-o notation, Analyzing algorithms, Complexity relationships among models, The class P, Polynomial time, examples of problems in P, The class NP, Examples of problems in NP, The P versus NP question, NP-Completeness, polynomial time reducibility, Definition of NP-Completeness, The Cook-Levin Theorem, Additional NP-Complete problems, The vertex cover problem, The Hamiltonian path problem, The subset sum problem.

TEXTBOOKS:

1. Introduction to the theory of computation, Micheal Sipser, Third Edition, Cengage Learning.

REFERENCES:

1. Introduction to Languages and The Theory of Computation, John C Martin, TMH.

2. Introduction to Computer Theory, Daniel I.A. Cohen, John Wiley.
3. A Text book on Automata Theory, P. K. Srimani, Nasir S. F. B, Cambridge University Press.
4. Introduction to Formal languages Automata Theory and Computation Kamala Krithivasan, Rama R, Pearson.
5. Theory of Computer Science – Automata languages and computation, Mishra and Chandrashekar, 2nd edition, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
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DATA WAREHOUSING AND DATA MINING (PE – III)

Course Objectives:

- To develop the abilities of critical analysis to data mining systems and applications.
- To implement practical and theoretical understanding of the technologies for data mining
- To understand the strengths and limitations of various data mining models;

UNIT- I

Data mining Overview and Advanced Pattern Mining: Data mining tasks – mining frequent patterns, associations and correlations, classification and regression for predictive analysis, cluster analysis, outlier analysis; advanced pattern mining in multilevel, multidimensional space – mining multilevel associations, mining multidimensional associations, mining quantitative association rules, mining rare patterns and negative patterns.

UNIT- II

Advance Classification: Classification by back propagation, support vector machines, classification using frequent patterns, other classification methods – genetic algorithms, roughset approach, fuzzy set approach;

UNIT- III

Advance Clustering: Density - based methods – DBSCAN, OPTICS, DENCLUE; Grid-Based methods – STING, CLIQUE; Exception – maximization algorithm; clustering High- Dimensional Data; Clustering Graph and Network Data.

UNIT- IV

Web and Text Mining: Introduction, web mining, web content mining, web structure mining, web usage mining, Text mining – unstructured text, episode rule discovery for texts, hierarchy of categories, text clustering.

UNIT- V

Temporal and Spatial Data Mining: Introduction; Temporal Data Mining – Temporal Association Rules, Sequence Mining, GSP algorithm, SPADE, SPIRIT Episode Discovery, Time Series Analysis, Spatial Mining – Spatial Mining Tasks, Spatial Clustering. Data Mining Applications.

TEXT BOOKS:

1. Data Mining Concepts and Techniques, Jiawei Han, Micheline Kamber, Jian Pei, Morgan Kaufmann.
2. Data Mining Techniques – Arun K. Pujari, Universities Press.

REFERENCE BOOKS:

1. Introduction to Data Mining – Pang-Ning Tan, Vipin Kumar, Michael Steinbach, Pearson.
2. Data Mining Principles & Applications – T.V. Suresh Kumar, B. Eswar Reddy, Jagadish S. Kalimani, Elsevier.

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STORAGE AREA NETWORKS (PE - III)

Course Objectives:

- To understand Storage Area Networks characteristics and components.
- To become familiar with the SAN vendors and their products
- To learn Fibre Channel protocols and how SAN components use them to communicate with each other
- To become familiar with Cisco MDS 9000 Multilayer Directors and Fabric Switches Thoroughly learn Cisco SAN-OS features.
- To understand the use of all SAN-OS commands. Practice variations of SANOS features

UNIT - I:

Introduction to Storage Technology Review data creation and the amount of data being created and understand the value of data to a business, challenges in data storage and data management, Solutions available for data storage, Core elements of a data center infrastructure, role of each element in supporting business activities

UNIT - II:

Storage Systems Architecture Hardware and software components of the host environment, Key protocols and concepts used by each component ,Physical and logical components of a connectivity environment ,Major physical components of a disk drive and their function, logical constructs of a physical disk, access characteristics, and performance Implications, Concept of RAID and its components , Different RAID levels and their suitability for different application environments: RAID 0, RAID 1, RAID 3, RAID 4, RAID 5, RAID 0+1, RAID 1+0, RAID 6, Compare and contrast integrated and modular storage systems , High-level architecture and working of an intelligent storage system

UNIT - III:

Introduction to Networked Storage Evolution of networked storage, Architecture, components, and topologies of FC-SAN, NAS, and IP-SAN, Benefits of the different networked storage options, understand the need for long-term archiving solutions and describe how CAS fulfills the need, understand the appropriateness of the different networked storage options for different application environments

UNIT - IV:

Information Availability & Monitoring & Managing Datacenter List reasons for planned/unplanned outages and the impact of downtime, Impact of downtime, Differentiate between business continuity (BC) and disaster recovery (DR) ,RTO and RPO, Identify single points of failure in a storage infrastructure and list solutions to mitigate these failures , Architecture of backup/recovery and the different backup/recovery topologies , replication technologies and their role in ensuring information availability and business continuity, Remote replication technologies and their role in providing disaster recovery and business continuity capabilities
Identify key areas to monitor in a data center, Industry standards for data center monitoring and management, Key metrics to monitor for different components in a storage infrastructure, Key management tasks in a data center

UNIT - V:

Securing Storage and Storage Virtualization Information security, Critical security attributes for information systems, Storage security domains, List and analyzes the common threats in each

domain, Virtualization technologies, block-level and file-level virtualization technologies and processes

Case Studies

The technologies described in the course are reinforced with EMC examples of actual solutions. Realistic case studies enable the participant to design the most appropriate solution for given sets of criteria.

TEXT BOOK:

1. EMC Corporation, Information Storage and Management, Wiley.

REFERENCE BOOKS:

1. Robert Spalding, "Storage Networks: The Complete Reference", Tata McGraw Hill, Osborne, 2003.
2. Marc Farley, "Building Storage Networks", Tata McGraw Hill ,Osborne, 2001.
3. Meeta Gupta, Storage Area Network Fundamentals, Pearson Education Limited, 2002.

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SEMANTIC WEB AND SOCIAL NETWORKS (PE – III)

Course Objectives:

- To learn Web Intelligence
- To learn Knowledge Representation for the Semantic Web
- To learn Ontology Engineering
- To learn Semantic Web Applications, Services and Technology
- To learn Social Network Analysis and semantic web

UNIT – I:

Web Intelligence: Thinking and Intelligent Web Applications, The Information Age ,The World Wide Web, Limitations of Today's Web, The Next Generation Web, Machine Intelligence, Artificial Intelligence, Ontology, Inference engines, Software Agents, Berners-Lee www, Semantic Road Map, Logic on the semantic Web.

UNIT - II:

Knowledge Representation for the Semantic Web: Ontologies and their role in the semantic web, Ontologies Languages for the Semantic Web –Resource Description Framework(RDF) / RDF Schema, Ontology Web Language(OWL), UML, XML/XML Schema.

UNIT- III:

Ontology Engineering: Ontology Engineering, Constructing Ontology, Ontology Development Tools, Ontology Methods, Ontology Sharing and Merging, Ontology Libraries and Ontology Mapping, Logic, Rule and Inference Engines.

UNIT- IV:

Semantic Web Applications, Services and Technology: Semantic Web applications and services, Semantic Search, e-learning, Semantic Bioinformatics, Knowledge Base ,XML Based Web Services, Creating an OWL-S Ontology for Web Services, Semantic Search Technology, Web Search Agents and Semantic Methods,

UNIT - V: .Social Network Analysis and semantic web

What is social Networks analysis, development of the social networks analysis, Electronic Sources for Network Analysis – Electronic Discussion networks, Blogs and Online Communities, Web Based Networks. Building Semantic Web Applications with social network features.

TEXT BOOKS:

1. Thinking on the Web - Berners Lee, Godel and Turing, Wiley inter science, 2008.
2. Social Networks and the Semantic Web, Peter Mika, Springer, 2007.

REFERENCE BOOKS:

1. Semantic Web Technologies, Trends and Research in Ontology Based Systems, J. Davies, R. Studer, P. Warren, John Wiley & Sons.
2. Semantic Web and Semantic Web Services -Liyang Lu Chapman and Hall/CRC Publishers,(Taylor & Francis Group)
3. Information Sharing on the semantic Web - Heiner Stuckenschmidt; Frank Van Harmelen, Springer Publications.
4. Programming the Semantic Web, T. Segaran, C.Evans, J. Taylor, O'Reilly, SPD.

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CYBER SECURITY (PE - III)

Course Objectives:

- To learn about cyber crimes and how they are planned
- To learn the vulnerabilities of mobile and wireless devices
- To learn about the crimes in mobile and wireless devices

UNIT - I

Introduction to Cybercrime: Introduction, Cybercrime and Information security, who are cybercriminals, Classifications of Cybercrimes, Cybercrime: The legal Perspectives and Indian Perspective, Cybercrime and the Indian ITA 2000, A Global Perspective on Cybercrimes.

Cyber offenses: How criminals Plan Them: Introduction, How Criminals plan the Attacks, Social Engineering, Cyber stalking, Cyber cafe and Cybercrimes, Botnets: The Fuel for Cybercrime, Attack Vector, Cloud Computing.

UNIT- II

Cybercrime: Mobile and Wireless Devices: Introduction, Proliferation of Mobile and Wireless Devices, Trends in Mobility, Credit card Frauds in Mobile and Wireless Computing Era, Security Challenges Posed by Mobile Devices, Registry Settings for Mobile Devices, Authentication service Security, Attacks on Mobile/Cell Phones, Mobile Devices: Security Implications for Organizations, Organizational Measures for Handling Mobile, Organizational Security Policies and Measures in Mobile Computing Era, Laptops.

UNIT - III

Cybercrimes and Cyber security: the Legal Perspectives

Introduction

Cyber Crime and Legal Landscape around the world, Why Do We Need Cyber laws: The Indian Context, The Indian IT Act, Challenges to Indian Law and Cybercrime Scenario In India, Digital signatures and the Indian IT Act, Amendments to the Indian IT Act, Cybercrime and Punishment Cyber law, Technology and Students: Indian Scenario.

UNIT - IV

Understanding Computer Forensics

Introduction, Historical background of Cyber forensics, Digital Forensics Science, The Need for Computer Forensics, Cyber Forensics and Digital evidence, Forensics Analysis of Email, Digital Forensics Lifecycle, Chain of Custody concept, Network Forensics, Approaching a computer, Forensics Investigation, Challenges in Computer Forensics, Special Tools and Techniques Forensics Auditing

UNIT - V

Cyber Security: Organizational Implications

Introduction, Cost of Cybercrimes and IPR issues, Web threats for Organizations, Security and Privacy Implications, Social media marketing: Security Risks and Perils for Organizations, Social Computing and the associated challenges for Organizations.

TEXT BOOK:

1. **Cyber Security: Understanding Cyber Crimes, Computer Forensics and Legal Perspectives**, Nina Godbole and Sunil Belapure, Wiley INDIA.
2. **Introduction to Cyber Security**, Chwan-Hwa(john) Wu, J. David Irwin. CRC Press T&F Group

REFERENCE BOOK:

1. **Cyber Security Essentials**, James Graham, Richard Howard and Ryan Otson, CRC Press.

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BIG DATA ANALYTICS (PE – IV)

Course Objectives:

- To understand about big data
- To learn the analytics of Big Data
- To Understand the MapReduce fundamentals

UNIT - I

Big Data Analytics: What is big data, History of Data Management ; Structuring Big Data ; Elements of Big Data ; Big Data Analytics; Distributed and Parallel Computing for Big Data;

Big Data Analytics: What is Big Data Analytics, What Big Data Analytics Isn't, Why this sudden Hype Around Big Data Analytics, Classification of Analytics, Greatest Challenges that Prevent Business from Capitalizing Big Data; Top Challenges Facing Big Data; Why Big Data Analytics Important; Data Science; Data Scientist; Terminologies used in Big Data Environments; Basically Available Soft State Eventual Consistency (BASE); Open source Analytics Tools;

UNIT - II

Understanding Analytics and Big Data: Comparing Reporting and Analysis, Types of Analytics; Points to Consider during Analysis; Developing an Analytic Team; Understanding Text Analytics; Analytical Approach and Tools to Analyze Data: Analytical Approaches; History of Analytical Tools; Introducing Popular Analytical Tools; Comparing Various Analytical Tools.

UNIT - III

Understanding MapReduce Fundamentals and HBase : The MapReduce Framework; Techniques to Optimize MapReduce Jobs; Uses of MapReduce; Role of HBase in Big Data Processing; Storing Data in Hadoop : Introduction of HDFS, Architecture, HDFS Files, File system types, commands, org.apache.hadoop.io package, HDFS High Availability; Introducing HBase, Architecture, Storing Big Data with HBase , Interacting with the Hadoop Ecosystem; HBase in Operations-Programming with HBase; Installation, Combining HBase and HDFS;

UNIT - IV

Big Data Technology Landscape and Hadoop : NoSQL, Hadoop; RDBMS versus Hadoop; Distributed Computing Challenges; History of Hadoop; Hadoop Overview; Use Case of Hadoop; Hadoop Distributors; HDFS (Hadoop Distributed File System), HDFS Daemons, read,write, Replica Processing of Data with Hadoop; Managing Resources and Applications with Hadoop YARN.

UNIT - V

Social Media Analytics and Text Mining: Introducing Social Media; Key elements of Social Media; Text mining; Understanding Text Mining Process; Sentiment Analysis, Performing Social Media Analytics and Opinion Mining on Tweets;

Mobile Analytics: Introducing Mobile Analytics; Define Mobile Analytics; Mobile Analytics and Web Analytics; Types of Results from Mobile Analytics; Types of Applications for Mobile Analytics; Introducing Mobile Analytics Tools;

TEXT BOOKS:

1. BIG DATA and ANALYTICS, Seema Acharya, Subhasinin Chellappan, Wiley publications.
2. BIG DATA, Black Book™, DreamTech Press, 2015 Edition.
3. BUSINESS ANALYTICS 5e, BY Albright |Winston

REFERENCE BOOKS:

1. Rajiv Sabherwal, Irma Becerra- Fernandez, " Business Intelligence –Practice, Technologies and Management", John Wiley 2011.
2. Lariss T. Moss, ShakuAtre, " Business Intelligence Roadmap", Addison-Wesley It Service.
3. Yuli Vasiliev, " Oracle Business Intelligence : The Condensed Guide to Analysis and Reporting", SPD Shroff, 2012.

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SOFT COMPUTING (PE – IV)

Course Objectives:

To give students knowledge of soft computing theories fundamentals, i.e. Fundamentals of artificial and neural networks, fuzzy sets and fuzzy logic and genetic algorithms.

UNIT- I

AI Problems and Search: AI problems, Techniques, Problem Spaces and Search, Heuristic Search Techniques- Generate and Test, Hill Climbing, Best First Search Problem reduction, Constraint Satisfaction and Means End Analysis. Approaches to Knowledge Representation- Using Predicate Logic and Rules.

UNIT- II

Artificial Neural Networks: Introduction, Basic models of ANN, important terminologies, Supervised Learning Networks, Perceptron Networks, Adaptive Linear Neuron, Back propagation Network. Associative Memory Networks. Training Algorithms for pattern association, BAM and Hopfield Networks.

UNIT- III

Unsupervised Learning Network- Introduction, Fixed Weight Competitive Nets, Maxnet, Hamming Network, Kohonen Self-Organizing Feature Maps, Learning Vector Quantization, Counter Propagation Networks, Adaptive Resonance Theory Networks. Special Networks-Introduction to various networks.

UNIT- IV

Introduction to Classical Sets (crisp Sets)and Fuzzy Sets- operations and Fuzzy sets. Classical Relations -and Fuzzy Relations- Cardinality, Operations, Properties and composition. Tolerance and equivalence relations.

Membership functions- Features, Fuzzification, membership value assignments, Defuzzification.

UNIT- V

Fuzzy Arithmetic and Fuzzy Measures, Fuzzy Rule Base and Approximate Reasoning Fuzzy Decision making

Fuzzy Logic Control Systems, Genetic Algorithm- Introduction and basic operators and terminology. Applications: Optimization of TSP, Internet Search Technique.

TEXT BOOKS:

1. Principles of Soft Computing- S N Sivanandam, S N Deepa, Wiley India, 2007
2. Soft Computing and Intelligent System Design -Fakhreddine O Karray, Clarence D Silva,. Pearson Edition, 2004.

REFERENCE BOOKS:

1. Artificial Intelligence and Soft Computing- Behavioural and Cognitive Modeling of the Human Brain- Amit Konar, CRC press, Taylor and Francis Group.
2. Artificial Intelligence – Elaine Rich and Kevin Knight, TMH, 1991, rp2008.
3. Artificial Intelligence – Patric Henry Winston – Third Edition, Pearson Education.
4. A first course in Fuzzy Logic-Hung T Nguyen and Elbert A Walker, CRC. Press Taylor and Francis Group.

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SOFTWARE PROCESS AND PROJECT MANAGEMENT (PE – IV)

Course Objectives:

- Describe and determine the purpose and importance of project management from the perspectives of planning, tracking and completion of project.
- Compare and differentiate organization structures and project structures.
- Implement a project to manage project schedule, expenses and resources with the application of suitable project management tools.

UNIT- I

Software Process Maturity

Software maturity Framework, Principles of Software Process Change, Software Process Assessment, The Initial Process, The Repeatable Process, The Defined Process, The Managed Process, The Optimizing Process.

Process Reference Models

Capability Maturity Model (CMM), CMMI, PCMM, PSP, TSP.

UNIT - II

Software Project Management Renaissance

Conventional Software Management, Evolution of Software Economics, Improving Software Economics, The old way and the new way.

Life-Cycle Phases and Process artifacts

Engineering and Production stages, inception phase, elaboration phase, construction phase, transition phase, artifact sets, management artifacts, engineering artifacts and pragmatic artifacts, model based software architectures

UNIT - III

Workflows and Checkpoints of process

Software process workflows, Iteration workflows, Major milestones, Minor milestones, Periodic status assessments.

Process Planning

Work breakdown structures, Planning guidelines, cost and schedule estimating process, iteration planning process, Pragmatic planning.

UNIT - IV

Project Organizations

Line-of- business organizations, project organizations, evolution of organizations, process automation.

Project Control and process instrumentation

The seven core metrics, management indicators, quality indicators, life-cycle expectations, Pragmatic software metrics, and metrics automation.

UNIT - V

CCPDS-R Case Study and Future Software Project Management Practices

Modern Project Profiles, Next-Generation software Economics, Modern Process Transitions.

TEXT BOOKS:

1. Managing the Software Process, *Watts S. Humphrey*, Pearson Education.
2. Software Project Management, *Walker Royce*, Pearson Education.

REFERENCE BOOKS:

1. Effective Project Management: Traditional, Agile, Extreme, Robert Wysocki, Sixth edition, Wiley India, rp2011.
2. An Introduction to the Team Software Process, Watts S. Humphrey, Pearson Education, 2000
3. Process Improvement essentials, James R. Persse, O'Reilly, 2006
4. Software Project Management, Bob Hughes & Mike Cotterell, fourth edition, TMH, 2006
5. Applied Software Project Management, Andrew Stellman & Jennifer Greene, O'Reilly, 2006.
6. Head First PMP, Jennifer Greene & Andrew Stellman, O'Reilly, 2007
7. Software Engineering Project Managent, Richard H. Thayer & Edward Yourdon, 2nd edition, Wiley India, 2004.
8. The Art of Project Management, Scott Berkun, SPD, O'Reilly, 2011.
9. Applied Software Project Management, Andrew Stellman & Jennifer Greene, SPD, O'Reilly, rp2011.
10. Agile Project Management, Jim Highsmith, Pearson education, 2004.

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MACHINE LEARNING (PE – IV)

Course Objectives:

- To be able to formulate machine learning problems corresponding to different applications.
- To understand a range of machine learning algorithms along with their strengths and weaknesses.
- To understand the basic theory underlying machine learning.
- To be able to apply machine learning algorithms to solve problems of moderate complexity.
- To be able to read current research papers and understands the issues raised by current research.

UNIT - I

Introduction - Well-posed learning problems, Designing a learning system, Perspectives and issues in machine learning

Concept learning and the general to specific ordering – Introduction, A concept learning task, Concept learning as search, Find-S: finding a maximally specific hypothesis, Version spaces and the candidate elimination algorithm, Remarks on version spaces and candidate elimination, Inductive bias

UNIT - II

Decision Tree learning – Introduction, Decision tree representation, Appropriate problems for decision tree learning, The basic decision tree learning algorithm, Hypothesis space search in decision tree learning, Inductive bias in decision tree learning, Issues in decision tree learning

Artificial Neural Networks – Introduction, Neural network representation, Appropriate problems for neural network learning, Perceptions, Multilayer networks and the back propagation algorithm, Remarks on the back propagation algorithm, An illustrative example face recognition

Advanced topics in artificial neural networks

Evaluation Hypotheses – Motivation, Estimation hypothesis accuracy, Basics of sampling theory, A general approach for deriving confidence intervals, Difference in error of two hypotheses, Comparing learning algorithms

UNIT - III

Bayesian learning – Introduction, Bayes theorem, Bayes theorem and concept learning, Maximum likelihood and least squared error hypotheses, Maximum likelihood hypotheses for predicting probabilities, Minimum description length principle, Bayes optimal classifier, Gibbs algorithm, Naïve Bayes classifier, An example learning to classify text, Bayesian belief networks The EM algorithm

Computational learning theory – Introduction, Probability learning an approximately correct hypothesis, Sample complexity for Finite Hypothesis Space, Sample Complexity for infinite Hypothesis Spaces, The mistake bound model of learning - **Instance-Based Learning**- Introduction, k -Nearest Neighbour Learning, Locally Weighted Regression, Radial Basis Functions, Case-Based Reasoning, Remarks on Lazy and Eager Learning

Genetic Algorithms – Motivation, Genetic Algorithms, An illustrative Example, Hypothesis Space Search, Genetic Programming, Models of Evolution and Learning, Parallelizing Genetic Algorithms

UNIT - IV

Learning Sets of Rules – Introduction, Sequential Covering Algorithms, Learning Rule Sets: Summary, Learning First Order Rules, Learning Sets of First Order Rules: FOIL, Induction as Inverted Deduction, Inverting Resolution

Analytical Learning - Introduction, Learning with Perfect Domain Theories: Prolog-EBG Remarks on Explanation-Based Learning, Explanation-Based Learning of Search Control Knowledge

UNIT - V

Combining Inductive and Analytical Learning – Motivation, Inductive-Analytical Approaches to Learning, Using Prior Knowledge to Initialize the Hypothesis, Using Prior Knowledge to Alter the Search Objective, Using Prior Knowledge to Augment Search Operators,

Reinforcement Learning – Introduction, The Learning Task, Q Learning, Non-Deterministic, Rewards and Actions, Temporal Difference Learning, Generalizing from Examples, Relationship to Dynamic Programming

TEXT BOOKS:

1. Machine Learning – Tom M. Mitchell, - MGH
2. Machine Learning: An Algorithmic Perspective, Stephen Marsland, Taylor & Francis (CRC)

REFERENCE BOOKS:

1. Machine Learning Methods in the Environmental Sciences, Neural Networks, William W Hsieh, Cambridge Univ Press.
2. Richard o. Duda, Peter E. Hart and David G. Stork, pattern classification, John Wiley & Sons Inc., 2001
3. Chris Bishop, Neural Networks for Pattern Recognition, Oxford University Press, 1995.
4. Machine Learning by Peter Flach , Cambridge.

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INTERNET TECHNOLOGIES AND SERVICES LAB

Course Objectives:

- Write syntactically correct HTTP messages and describe the semantics of common HTTP methods and header fields
- Discuss differences between URIs, URNs, and URLs, and demonstrate a detailed understanding of http-scheme URLs, both relative and absolute
- Describe the actions, including those related to the cache, performed by a browser in the process of visiting a Web address
- Install a web server and perform basic administrative procedures, such as tuning communication parameters, denying access to certain domains, and interpreting an access log
- Write a valid standards-conformant HTML document involving a variety of element types, including hyperlinks, images, lists, tables, and forms
- Use CSS to implement a variety of presentation effects in HTML and XML documents, including explicit positioning of elements
- Demonstrate techniques for improving the accessibility of an HTML document

List of Sample Problems:

i) Internet Technologies

1. Develop static pages (using Only HTML) of an online Book store. The pages should resemble: www.amazon.com the website should consist the following pages.
Home page, Registration and user Login
User Profile Page, Books catalog
Shopping Cart, Payment By credit card
Order Conformation
2. Validate the Registration, user login, user profile and payment by credit card pages using JavaScript.
3. Create and save an XML document at the server, which contains 10 users information. Write a program, which takes User Id as an input and returns the user details by taking the user information from the XML document.
4. Install TOMCAT web server. Convert the static web pages of assignments 2 into dynamic web pages using Servlets and cookies. Hint: Users information (user id, password, credit card number) would be stored in web.xml. Each user should have a separate Shopping Cart.
5. Redo the previous task using JSP by converting the static web pages of assignments 2 into dynamic web pages. Create a database with user information and books information. The books catalogue should be dynamically loaded from the database. Follow the MVC architecture while doing the website.
6. Implement the “Hello World!” program using JSP Struts Framework.

ii) Additional Assignment Problems

Write an HTML page including any required Javascript that takes a number from one text field in the range of 0 to 999 and shows it in another text field in words. If the number is out of range, it should show “out of range” and if it is not a number, it should show “not a number” message in the result box.

Write a java swing application that takes a text file name as input and counts the characters, words

and lines in the file. Words are separated with white space characters and lines are separated with new line character.

Write a simple calculator servlet that takes two numbers and an operator (+, -, /, * and %) from an HTML page and returns the result page with the operation performed on the operands. It should check in a database if the same expression is already computed and if so, just return the value from database. Use MySQL or PostgreSQL.

Write an HTML page that contains a list of 5 countries. When the user selects a country, its capital should be printed next to the list. Add CSS to customize the properties of the font of the capital (color, bold and font size).

Write a servlet that takes name and age from an HTML page. If the age is less than 18, it should send a page with "Hello <name>, you are not authorized to visit this site" message, where <name> should be replaced with the entered name. Otherwise it should send "Welcome <name> to this site" message.

Write a calculator program in HTML that performs basic arithmetic operations (+, -, /, * and %). Use CSS to change the foreground and background color of the values, buttons and result display area separately. Validate the input strings using JavaScript regular expressions. Handle any special cases like division with zero reasonably. The screen may look similar to the following:

Value 1	Operator	Value 2		Result
	+		=	

Write a Java program that creates a calculator GUI, as shown in figure. Extra components may be added for convenience:

Color Scheme

Black on White

0

^

+

▼

0

^

▼

Result

0

The Color Scheme may be Black on White or Blue on Yellow (selectable) and accordingly all components colors must be changed. The values can be either entered or increased or decreased by a step of 10. The operators are +, -, / and * (selectable). Once any change

takes place, the result must be automatically computed by the program.

Write a Java Application that will read an XML file that contains personal information (Name, Mobile Number, age and place). It reads the information using SAX parser. After reading the information, it shows two input Text Fields in a window, one for tag name and the other for value. Once these two values are given, it should list all the records in the XML file that match the value of the given field in a text area (result box). For example, if the two text boxes are entered with "name" and "ABCD" then it should show all the records for which name is "ABCD"? An Illustration is given below that takes a mobile number and lists all the records that have the same mobile number.

Field	mobile	
Value	9449449449	OK
Result	abc, 22, Hyd def, 23, Delhi xxx, 44, Chennai	

Consider the following web application for implementation:

The user is first served a login page which takes user's name and password. After submitting the details the server checks these values against the data from a database and takes the following decisions.

If name and password matches, serves a welcome page with user's full name.

If name matches and password doesn't match, then serves "password mismatch" page

If name is not found in the database, serves a registration page, where users full name, present user name (used to login) and password are collected. Implement this application in:

1. Pure JSP
2. Pure Servlets
3. Struts Framework

Implement a simple arithmetic calculator with +, -, /, *, % and = operations using Struts Framework

The number of times the calculator is used should be displayed at the bottom (use session variable).

iii)Internet Technologies and Services Lab - Additional Problems

Create a web Service in Java that takes two city names from the user and returns the distance between these two from data available from a table in MySql.

Write a java and a C# client which use the above service

Write a Java program that takes a file as input and encrypts it using DES encryption. The program should check if the file exists and its size is not zero.

Write a Java program that generates a key pair and encrypts a given file using RSA algorithm.

Write a Java program that finds digest value of a given string.

Consider the following xml file for encryption

```
<?xml version="1.0"> <transaction> <from>12345</from> <to>54321</to> <amount>10000</amount>
<secretcode>abc123</secretcode> <checksum></checksum> </transaction>
```

Replace <from> and <to> values with the RSA encrypted values represented with base64 encoding assuming that the public key is available in a file in local directory "pubkey.dat". Encrypt <secretcode> with AES algorithm with a password 'secret'. The checksum of all the field values concatenated with a delimiter character '+' will be inserted in the checksum and the xml file is written to encrypted.xml file.

Assume that a file 'config.xml', which has the following information:

```
<users>
<user> <name>abc</name> <pwd>pwd123</pwd> <role>admin</role> <md5>xxx</md5> </user>
<user> <name>def</name> <pwd>pwd123</pwd> <role>guest</role> <md5>xxx</md5> </user>
</users>
```

Replace name and role with DES encrypted values and pwd with RSA encrypted values (represent the values with base64 encoding). The public key is available in "public.key" file in current directory. Replace xxx with respective MD5 values of all the fields for each user. Write the resulting file back to config.xml.

Write an HTML page that gives 3 multiple choice (a,b,c and d) questions from a set of 5 preloaded questions randomly. After each question is answered change the color of the question to either green or blue using CSS. Finally on clicking OK button that is provided, the score should be displayed as a pop-up window. Use Java Script for dynamic content.

Write an HTML page that has 3 countries on the left side ("USA", "UK" and "INDIA") and on the right side of each country, there is a pull-down menu that contains the following entries: ("Select Answer", "New Delhi", "Washington" and "London"). The user will match the Countries with their respective capitals by selecting an item from the menu. The user chooses all the three answers (whether right or wrong). Then colors of the countries should be changed either to green or to red depending on the answer. Use CSS for changing color.
Write an HTML Page that can be used for registering the candidates for an entrance test. The fields are: name, age, qualifying examination (diploma or 10+2), stream in qualifying examination. If qualifying examination is "diploma", the stream can be "Electrical", "Mechanical" or "Civil". If the qualifying examination is 10+2, the stream can be "MPC" or "BPC". Validate the name to accept only characters and spaces.
Write an HTML page that has two selection menus. The first menu contains the states ("AP", "TN" and "KN") and depending on the selection the second menu should show the following items: "Hyderabad", "Vijayawada", "Kurnool" for AP, "Chennai", "Salem", "Madurai" for TN and "Bangalore", "Bellary", "Mysore" for KN.
Write an HTML page that has phone buttons 0 to 9 and a text box that shows the dialed number. If 00 is pressed at the beginning, it should be replaced with a + symbol in the text box. If the number is not a valid international number (+ followed by country code and 10 digit phone number) the color of the display should be red and it should turn to green when the number is valid. Consider only "+91, +1 and +44 as valid country codes. Use CSS for defining colors.
Write an HTML page that has a text box for phone number or Name. If a number is entered in the box the name should be displayed next to the number. If 00 is pressed at the beginning, it should be replaced with a + symbol in the text box. If a name is entered in the text box, it should show the number next to the name. If the corresponding value is not found, show it in red and show it in green otherwise. Use CSS for colors. Store at least 5 names and numbers in the script for testing.
A library consists of 10 titles and each title has a given number of books initially. A student can take or return a book by entering his/her HTNo as user ID and a given password. If there are at least two books, the book is issued and the balance is modified accordingly. (a) Use RDBMS and implement it with JSP. (b) Use XML File for data and Implement it with JSP (c) Use RDBMS and implement it with Servlets (d) Use XML File for data and Implement it with Servlets
A Bus Reservation System contains the details of a bus seat plan for 40 seats in 2x2 per row arrangement, where the seats are numbered from 1 to 40 from first row to last row. The customer can visit the website and can reserve a ticket of his choice if available by entering his details (Name, Address, Gender and Age). The customer can cancel the ticket by entering the seat number and his name as entered for reservation. (a) Use RDBMS and implement it with JSP. (b) Use XML File for data and Implement it with JSP (c) Use RDBMS and implement it with Servlets (d) Use XML File for data and Implement it with Servlets.
Implement a simple messaging system with the following details: When a student logs in with his/her HTNO and a given password, they should get all the messages posted to him/her giving the ID of sender and the actual message. Each message may be separated with a ruler. There should be a provision for the user to send a message to any number of users by giving the IDs separated with commas in the "To" text box. (a) Use RDBMS and implement it with JSP. (b) Use XML File for data and Implement it with JSP (c) Use RDBMS and implement it with Servlets (d) Use XML File for data and Implement it with Servlets.
There is an image of 600x100 size which can be logically divided into 12 button areas with labels (0-9, +, =). Write a javascript calculator program that uses this image as input virtual keyboard and three text areas for two input numbers and result of sum of these numbers. Add a CSS that can be used to change the colors of text and background of text areas and the page. The input numbers can be up to 4 digits each.
Develop a web application that takes user name and password as input and compares them with those available in an xml user database. If they match, it should display the welcome page that contains the user's full

name and last used date and time retrieved from a client cookie. On logout it stores new time to the cookie and displays a goodbye page. If authentication fails, it should store the attempt number to the client cookie and displays an error page. Add necessary CSS that takes care of the font, color of foreground and background.

A web application has the following specifications:

The first page (Login page) should have a login screen where the user gives the login name and password. Both fields must be validated on client side for a minimum length of 4 characters, name should be lower case a-z characters only and password should contain at least one digit. On submitting these values, the server should validate them with a MySQL database and if failed, show the login page along with a message saying "Login Name or Password Mismatch" in Red color below the main heading and above the form. If successful, show a welcome page with the user's full name (taken from database) and a link to Logout. On logout, a good bye page is displayed with the total time of usage (Logout time – login time). Specify the Schema details of table and web.xml file contents.

Implement it using (a) JSP Pages (b) Servlets (c) Struts

Design a struts based web portal for an international conference with following specifications:

The welcome page should give the details of the conference and a link to login. If login fails, direct them back for re-login and also provide a link for registration. On successful registration/login, the user will be directed to a page where s/he can see the status (accepted/rejected) of their already submitted papers followed by a form for submitting a doc file to the conference. Provide a logout button on all pages including the home page, once the user logs in. Implement validation framework to check that the user name is in the form of CCDDCC and password is in the form of (CCSDDD) (C for character, S for special character (one of @, #, \$, %, ^, & and !)) and D for digit)., Database should be accessed through Connection Pool for MySql for user information. Provide scope for internationalization in future. Assume any missing information and mention it first.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech in COMPUTER SCIENCE AND ENGINEERING
Common to (CS, CSE)

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Algorithms	25	75	4	0	0	4
PC-2	Computer Networking	25	75	4	0	0	4
PC-3	Software Engineering	25	75	4	0	0	4
PE-1	1. Network Security and Cryptography 2. Mobile Application Development 3. Graph Theory 4. Internet of Things	25	75	3	0	0	3
PE-2	1. Game Theory 2. Parallel and Distributed Algorithms 3. Software Architecture and Design Patterns 4. Embedded Systems	25	75	3	0	0	3
OE-1	*Open Elective – 1	25	75	3	0	0	3
Laboratory I	Advanced Algorithms Lab	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Network Programming	25	75	4	0	0	4
PC-5	Distributed Systems and Cloud Computing	25	75	4	0	0	4
PC-6	Theory of Computation	25	75	4	0	0	4
PE-3	1. Data Warehousing and Data Mining 2. Storage Area Networks 3. Semantic Web and Social Networks 4. Cyber Security	25	75	3	0	0	3
PE4	1. Big Data Analytics 2. Soft Computing 3. Software Process and Project Management 4. Machine Learning	25	75	3	0	0	3
OE-2	*Open Elective – 2	25	75	3	0	0	3
Laboratory II	Internet Technologies and Services Lab	25	75	0	0	3	2
Seminar II	Seminar -II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

ADVANCED ALGORITHMS (PC -1)

Course Objectives:

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.
- Significance of algorithms in the computer field
- Various aspects of algorithm development
- Qualities of a good solution

Unit - I : Introduction - Role of algorithms in computing, Analyzing algorithms, Designing Algorithms, Growth of Functions, Divide and Conquer- The maximum-subarray problem, Strassen's algorithms for matrix multiplication, The substitution method for solving recurrences, The recurrence-tree method for solving recurrence, The master method for solving recursions, Probabilistic analysis and random analysis.

Unit - II: Review of Data Structures- Elementary Data Structures, Hash Tables, Binary Search Trees, Red-Black Trees.

Unit - III: Dynamic Programming - Matrix-chain multiplication, Elements of dynamic programming, Longest common subsequence, Greedy Algorithms - Elements of the greedy strategy, Huffman codes, Amortized Analysis - Aggregate analysis, The accounting method, The potential method, Dynamic tables.

Unit - IV: Graph Algorithms - Elementary Graph Algorithms, Minimal spanning trees, Single-Source Shortest Paths, Maximum flow.

Unit - V: NP-Complete & Approximate Algorithms-Polynomial time, Polynomial-time verification, NP-completeness and reducibility, NP-complete & approximation problems - Clique problem, Vertex-cover problem, formula satisfiability, 3 CNF Satisfiability, The vertex-cover problem, The traveling-salesman problem, The subset-sum problem.

TEXT BOOKS:

1. "Introduction to Algorithms", Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Clifford Stein, Third Edition, PHI Publication.
2. "Data Structures and Algorithms in C++", M.T. Goodrich, R. Tamassia and D.Mount, Wiley India.

REFERENCES:

1. Fundamentals of Computer Algorithms, Ellis Horowitz, Sartaj Sahni, Sanguthevar Rajasekaran, Second Edition, Galgotia Publication
2. Data structures with C++, J. Hubbard, Schaum's outlines, TMH.
3. Data structures and Algorithm Analysis in C++, 3rd edition, M. A. Weiss, Pearson.
4. Classic Data Structures, D. Samanta, 2nd edition, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year - I Sem. (CSE)

COMPUTER NETWORKING (PC -2)

UNIT-1

The internet architecture, Access Networks, The network Core, Peer-to-Peer Networks, Content Distribution Networks, Delay Tolerant Networks, Circuit Switching vs. Packet switching, Packet switching Delays and congestion, Client/Server and Peer-to-Peer Architectures, MAC and LLC, Virtual LAN, Asynchronous Transfer Mode (ATM)

UNIT-2

Network Address Translator, Internet Control Message Protocol, SNMP, CIDR, IPv6, Routing Protocol Basics in advanced networks, Routing Information Protocol (RIP), Interior Gateway Routing Protocol (IGRP), Switching Services, Spanning Tree Protocol (STP), Standard Network Management Protocol.

UNIT-3

TCP and Mobile TCP, TCP Tahoe and TCP Reno, High speed TCP, Coexistence of UDP and TCP flows, HTTP and HTTPS, FTP and SFTP, Domain Name Service, TCP and UDP sockets

UNIT-4

Introduction to traffic Engineering, Requirement Definition for Traffic Engineering, Traffic Sizing, Traffic Characteristics, Delay Analysis, Connectivity and Availability, Introduction to Multimedia Services, Explaining Transmission of Multimedia over the Internet.

Introduction, Wireless Links and Network Characteristics, CDMA, WiFi: 802.11, Wireless LANs, The 802.11 Architecture, The 802.11 MAC Protocol, The IEEE 802.11 Frame, Mobility in the Same IP Subnet, Advanced Features in 802.11, Personal Area Networks: Bluetooth and Zigbee, Cellular Internet Access, An Overview of Cellular Network Architecture, 3G Cellular Data Networks: Extending the Internet to Cellular Subscribers, On to 4G: LTE, Mobility Management: Principles, Addressing, Routing to a Mobile Node, Mobile IP, Managing Mobility in Cellular Networks, Routing Calls to a Mobile User, Handoffs in GSM, Wireless and Mobility: Impact on Higher-Layer Protocols

UNIT-5

Explaining IP Multicasting, VOIP, Unified Communication, Virtual Networking, Data center Networking, Introduction to Optical Networking, SONET /SDH Standard, Next generation cellular networks, Secure Socket Layer, IP Sec, TLS, Kerberos, Domain name system Protection.

TEXT BOOKS:

1. Computer Networking: A Top-Down Approach, 6/e, James F. Kurose and Keith W. Ross, Pearson Education, 2012.
2. Larry L. Peterson and Bruce S. Davie, Computer Networks: A systems approach, Morgan Kaufman, 5th Edition, 2012
3. Data Communications and Networking, *Behrouz A. Forouzan*, Fourth Edition, Tata McGraw Hill
4. High Speed Networks and Internets – Performance and Quality of Service, *William Stallings*, Second Edition, Pearson Education.
5. Top-Down Network Design, *Priscilla Oppenheimer*, Second Edition, Pearson Education (CISCO Press)

REFERENCE BOOKS:

1. Advance Computer Network, By Dayanand Ambawade, Dr. Deven shah, Prof. Mahendra Mehra, Wiley India
2. CCNA Intro – Study Guide – Todd Lammle, Sybex
3. Computer Networks by Mayank Dave, Cengage.
4. Guide to Networking Essentials, *Greg Tomsho, Ed Tittel, David Johnson*, Fifth Edition, Thomson.
5. Computer Networks, *Andrew S. Tanenbaum*, Fourth Edition, Prentice Hall.
6. An Engineering Approach to Computer Networking, *S. Keshav*, Pearson Education.
7. Campus Network Design Fundamentals, *Diane Teare, Catherine Paquet*, Pearson Education (CISCO Press)
8. Computer Communications Networks, Mir, Pearson Education.
9. Chwan-Hwa (John) Wu, J. David Irwin, Introduction to computer networks and Cyber Security, CRC press, Taylor & Francis Group, 2014
10. Andrew S. Tanenbaum, David J. Wetherall, Computer Networks, Pearson, 5th Edition, 2014
11. G. Wright and W. Stevens, TCP/IP Illustrated, Volume 1 and Volume 2, Addison-Wesley, 1996

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech- I Year – I Sem CSE

SOFTWARE ENGINEERING (PC - 3)

Prerequisites:

- A course on “Computer Programming and Data Structures”
- A course on “Object Oriented Programming Through Java”

Course Objectives:

- The aim of the course is to provide an understanding of the working knowledge of the techniques for estimation, design, testing and quality management of large software development projects.
- Topics include process models, software requirements, software design, software testing, software process/product metrics, risk management, quality management and UML diagrams

Course Outcomes:

- Ability to translate end-user requirements into system and software requirements, using e.g. UML, and structure the requirements in a Software Requirements Document (SRD).
- Identify and apply appropriate software architectures and patterns to carry out high level design of a system and be able to critically compare alternative choices.
- Will have experience and/or awareness of testing problems and will be able to develop a simple testing report

UNIT-I:

Introduction to Software Engineering: The evolving role of software, changing nature of software, software myths.

A Generic view of process: Software engineering- a layered technology, a process framework, the capability maturity model integration (CMMI), process patterns, process assessment, personal and team process models.

Process models: The waterfall model, incremental process models, evolutionary process models, the unified process.

UNIT-II:

Software Requirements: Functional and non-functional requirements, user requirements, system requirements, interface specification, the software requirements document.

Requirements engineering process: Feasibility studies, requirements elicitation and analysis, requirements validation, requirements management.

System models: Context models, behavioral models, data models, object models, structured methods.

UNIT-III:

Design Engineering: Design process and design quality, design concepts, the design model.

Creating an architectural design: software architecture, data design, architectural styles and patterns, architectural design, conceptual model of UML, basic structural modeling, class diagrams, sequence diagrams, collaboration diagrams, use case diagrams, component diagrams.

UNIT-IV:

Testing Strategies: A strategic approach to software testing, test strategies for conventional software, black-box and white-box testing, validation testing, system testing, the art of debugging.

Product metrics: Software quality, metrics for analysis model, metrics for design model, metrics for source code, metrics for testing, metrics for maintenance.

UNIT-V:

Metrics for Process and Products: Software measurement, metrics for software quality.

Risk management: Reactive Vs proactive risk strategies, software risks, risk identification, risk projection, risk refinement, RMMM, RMMM plan.

Quality Management: Quality concepts, software quality assurance, software reviews, formal technical reviews, statistical software quality assurance, software reliability, the ISO 9000 quality standards.

TEXT BOOKS:

1. Software Engineering, A practitioner's Approach - Roger S. Pressman, 6th edition, Mc Graw Hill International Edition.
2. Software Engineering- Sommerville, 7th edition, Pearson Education.
3. The unified modeling language user guide Grady Booch, James Rumbaugh, Ivar Jacobson, Pearson Education.

REFERENCES:

1. Software Engineering, an Engineering approach- James F. Peters, Witold Pedrycz, John Wiely.
2. Software Engineering principles and practice- Waman S Jawadekar, The Mc Graw-Hill Companies.
3. Fundamentals of object oriented design using UML Meiler page-Jones: Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

**NETWORK SECURITY AND CRYPTOGRAPHY
(Professional Elective- 1)**

Course Objectives:

- Understand the basic categories of threats to computers and networks
- Understand various cryptographic algorithms.
- Describe public-key cryptosystem.
- Describe the enhancements made to IPv4 by IPSec
- Understand Intrusions and intrusion detection
- Discuss the fundamental ideas of public-key cryptography.
- Generate and distribute a PGP key pair and use the PGP package to send an encrypted e-mail message.
- Discuss Web security and Firewalls

Course Outcomes:

- Student will be able to understand basic cryptographic algorithms, message and web authentication and security issues.
- Ability to identify information system requirements for both of them such as client and server.
- Ability to understand the current legal issues towards information security

UNIT – I

Security Concepts: Introduction, The need for security, Security approaches, Principles of security, Types of Security attacks, Security services, Security Mechanisms, A model for Network Security

Cryptography Concepts and Techniques: Introduction, plain text and cipher text, substitution techniques, transposition techniques, encryption and decryption, symmetric and asymmetric key cryptography, steganography, key range and key size, possible types of attacks.

UNIT – II

Symmetric key Ciphers: Block Cipher principles, DES, AES, Blowfish, RC5, IDEA, Block cipher operation, Stream ciphers, RC4.

Asymmetric key Ciphers: Principles of public key cryptosystems, RSA algorithm, Elgamal Cryptography, Diffie-Hellman Key Exchange, Knapsack Algorithm.

UNIT – III

Cryptographic Hash Functions: Message Authentication, Secure Hash Algorithm (SHA-512),
Message authentication codes: Authentication requirements, HMAC, CMAC, Digital signatures, Elgamal Digital Signature Scheme.

Key Management and Distribution: Symmetric Key Distribution Using Symmetric & Asymmetric Encryption, Distribution of Public Keys, Kerberos, X.509 Authentication Service, Public – Key Infrastructure

UNIT – IV

Transport-level Security: Web security considerations, Secure Socket Layer and Transport Layer Security, HTTPS, Secure Shell (SSH)

Wireless Network Security: Wireless Security, Mobile Device Security, IEEE 802.11 Wireless LAN, IEEE 802.11i Wireless LAN Security

UNIT – V

E-Mail Security: Pretty Good Privacy, S/MIME **IP Security:** IP Security overview, IP Security architecture, Authentication Header, Encapsulating security payload, combining security associations, Internet Key Exchange

Case Studies on Cryptography and security: Secure Multiparty Calculation, Virtual Elections, Single sign On, Secure Inter-branch Payment Transactions, Cross site Scripting Vulnerability.

TEXT BOOKS:

1. Cryptography and Network Security - Principles and Practice: William Stallings, Pearson Education, 6th Edition
2. Cryptography and Network Security : Atul Kahate, Mc Graw Hill, 3rd Edition

REFERENCE BOOKS:

1. Cryptography and Network Security: C K Shyamala, N Harini, Dr T R Padmanabhan, Wiley India, 1st Edition.
2. Cryptography and Network Security : Forouzan Mukhopadhyay, Mc Graw Hill, 3rd Edition
3. Information Security, Principles, and Practice: Mark Stamp, Wiley India.
4. Principles of Computer Security: WM. Arthur Conklin, Greg White, TMH
5. Introduction to Network Security: Neal Krawetz, Cengage Learning
6. Network Security and Cryptography: Bernard Menezes, Cengage Learning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

**MOBILE APPLICATION DEVELOPMENT
(Professional Elective- 1)**

Course Objectives:

- To demonstrate their understanding of the fundamentals of Android operating systems
- To demonstrate their skills of using Android software development tools
- To demonstrate their ability to develop software with reasonable complexity on mobile platform
- To demonstrate their ability to deploy software to mobile devices
- To demonstrate their ability to debug programs running on mobile devices

Unit - I:

Introduction to Android Operating System: Android OS design and Features – Android development framework, SDK features, Installing and running applications on Eclipse platform, Creating AVDs, Types of Android applications, Best practices in Android programming, Android tools. Android application components – Android Manifest file, Externalizing resources like values, themes, layouts, Menus etc, Resources for different devices and languages, Runtime Configuration Changes Android Application Lifecycle – Activities, Activity lifecycle, activity states, monitoring state changes

Unit - II:

Android User Interface: Measurements – Device and pixel density independent measuring units
Layouts – Linear, Relative, Grid and Table Layouts
User Interface (UI) Components – Editable and non editable TextViews, Buttons, Radio and Toggle Buttons, Checkboxes, Spinners, Dialog and pickers
Event Handling – Handling clicks or changes of various UI components
Fragments – Creating fragments, Lifecycle of fragments, Fragment states, Adding fragments to Activity, adding, removing, and replacing fragments with fragment transactions, interfacing between fragments and Activities, Multi-screen Activities

Unit - III

Intents and Broadcasts: Intent – Using intents to launch Activities, Explicitly starting new Activity, Implicit Intents, Passing data to Intents, Getting results from Activities, Native Actions, using Intent to dial a number or to send SMS

Broadcast Receivers – Using Intent filters to service implicit Intents, Resolving Intent filters, finding and using Intents received within an Activity

Notifications – Creating and Displaying notifications, Displaying Toasts

Unit - IV

Persistent Storage: Files – Using application specific folders and files, creating files, reading data from files, listing contents of a directory Shared Preferences – Creating shared preferences, saving and retrieving data using Shared Preference

Database – Introduction to SQLite database, creating and opening a database, creating tables, inserting retrieving and deleting data, Registering Content Providers, Using content Providers (insert, delete, retrieve and update)

Unit - V

Advanced Topics: Alarms – Creating and using alarms

Using Internet Resources – Connecting to internet resource, using download manager

Location Based Services – Finding Current Location and showing location on the Map, updating location

TEXT BOOKS:

1. Professional Android 4 Application Development, Reto Meier, Wiley India, (Wrox) , 2012
2. Android Application Development for Java Programmers, James C Sheusi, Cengage Learning, 2013

REFERENCES:

1. Beginning Android 4 Application Development, Wei-Meng Lee, Wiley India (Wrox), 2013

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

**GRAPH THEORY
(Professional Elective- 1)**

Unit - I:

Introduction-Discovery of graphs, Definitions, Subgraphs, Isomorphic graphs, Matrix representations of graphs, Degree of a vertex, Directed walks, paths and cycles, Connectivity in digraphs, Eulerian and Hamilton digraphs, Eulerian digraphs, Hamilton digraphs, Special graphs, Complements, Larger graphs from smaller graphs, Union, Sum, Cartesian Product, Composition, Graphic sequences, Graph theoretic model of the LAN problem, Havel-Hakimi criterion, Realization of a graphic sequence.

Unit - II:

Connected graphs and shortest paths - Walks, trails, paths, cycles, Connected graphs, Distance, Cut-vertices and cut-edges, Blocks, Connectivity, Weighted graphs and shortest paths, Weighted graphs, Dijkstra's shortest path algorithm, Floyd-Warshall shortest path algorithm.

Unit III:

Trees- Definitions and characterizations, Number of trees, Cayley's formula, Kircho-matrix-tree theorem, Minimum spanning trees, Kruskal's algorithm, Prim's algorithm, Special classes of graphs, Bipartite Graphs, Line Graphs, Chordal Graphs, Eulerian Graphs, Fleury's algorithm, Chinese Postman problem, Hamilton Graphs, Introduction, Necessary conditions and sufficient conditions.

Unit IV:

Independent sets coverings and matchings – Introduction, Independent sets and coverings: basic equations, Matchings in bipartite graphs, Hall's Theorem, Kőnig's Theorem, Perfect matchings in graphs, Greedy and approximation algorithms.

Unit - V:

Vertex Colorings- Basic definitions, Cliques and chromatic number, Mycielski's theorem, Greedy coloring algorithm, Coloring of chordal graphs, Brooks theorem, Edge Colorings, Introduction and Basics, Gupta-Vizing theorem, Class-1 and Class-2 graphs, Edge-coloring of bipartite graphs, Class-2 graphs, Hajos union and Class-2 graphs, A scheduling problem and equitable edge-coloring.

TEXTBOOKS:

1. J. A. Bondy and U. S. R. Murty. Graph Theory, volume 244 of Graduate Texts in Mathematics. Springer, 1st edition, 2008.
2. J. A. Bondy and U. S. R. Murty. Graph Theory with Applications
<https://www.imo.unimontreal.ca/~hahn/IFT3545/GTWA.pdf>

REFERENCES:

Lecture Videos: <http://nptel.ac.in/courses/111106050/13>

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

INTERNET OF THINGS (Professional Elective- 1)

Course Objectives:

- To introduce the terminology, technology and its applications
- To introduce the concept of M2M (machine to machine) with necessary protocols
- To introduce the Python Scripting Language which is used in many IoT devices
- To introduce the Raspberry PI platform, that is widely used in IoT applications
- To introduce the implementation of web based services on IoT devices

Unit - I

Introduction to Internet of Things –Definition and Characteristics of IoT,
Physical Design of IoT – IoT Protocols, IoT communication models, IoT Communication APIs
IoT enabled Technologies – Wireless Sensor Networks, Cloud Computing, Big data analytics,
Communication protocols, Embedded Systems, IoT Levels and Templates
Domain Specific IoTs – Home, City, Environment, Energy, Retail, Logistics, Agriculture, Industry,
health and Lifestyle

Unit - II

IoT and M2M – Software defined networks, network function virtualization, difference between SDN
and NFV for IoT
Basics of IoT System Management with NETCOZF, YANG- NETCONF, YANG, SNMP NETOPEER

Unit - III

Introduction to Python - Language features of Python, Data types, data structures, Control of flow,
functions, modules, packaging, file handling, data/time operations, classes, Exception handling
Python packages - JSON, XML, HTTPLib, URLLib, SMTPLib

Unit - IV

IoT Physical Devices and Endpoints - Introduction to Raspberry PI-Interfaces (serial, SPI, I2C)
Programming – Python program with Raspberry PI with focus of interfacing external gadgets,
controlling output, reading input from pins.

Unit - V

IoT Physical Servers and Cloud Offerings – Introduction to Cloud Storage models and communication
APIs
Webserver – Web server for IoT, Cloud for IoT, Python web application framework
Designing a RESTful web API

TEXT BOOKS:

1. Internet of Things - A Hands-on Approach, Arshdeep Bahga and Vijay Madiseti, Universities Press, 2015, ISBN: 9788173719547
2. Getting Started with Raspberry Pi, Matt Richardson & Shawn Wallace, O'Reilly (SPD), 2014, ISBN: 9789350239759

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

**GAME THEORY
(Professional Elective- 2)**

UNIT - I:

Introduction: Game Theory, Games and Solutions Game Theory and the Theory of Competitive Equilibrium, Rational Behavior, The Steady State and Deductive Interpretations, Bounded Rationality Terminology and Notation

Nash Equilibrium- Strategic Games, Nash Equilibrium Examples Existence of a Nash Equilibrium, Strictly Competitive Games, Bayesian Games: Strategic Games with Imperfect Information

UNIT - II:

Mixed, Correlated, and Evolutionary Equilibrium -Mixed Strategy Nash Equilibrium Interpretations of Mixed Strategy Nash Equilibrium Correlated Equilibrium Evolutionary Equilibrium

Rationalizability and Iterated Elimination of Dominated Actions-Rationalizability Iterated Elimination of Strictly Dominated Actions, Iterated Elimination of Weakly Dominated Actions

UNIT - III:

Knowledge and Equilibrium -A Model of Knowledge Common Knowledge, Can People Agree to Disagree? , Knowledge and Solution Concepts, The Electronic Mail Game

UNIT - IV:

Extensive Games with Perfect Information -Extensive Games with Perfect Information Subgame Perfect Equilibrium Two Extensions of the Definition of a Game The Interpretation of a Strategy , Two Notable Finite Horizon Games , Iterated Elimination of Weakly Dominated Strategies

Bargaining Games -Bargaining and Game Theory , A Bargaining Game of Alternating Offers Subgame Perfect Equilibrium Variations and Extensions

UNIT - V:

Repeated Games - The Basic Idea Infinitely Repeated Games vs.\ Finitely Repeated Games Infinitely Repeated Games: Definitions Strategies as Machines Trigger Strategies: Nash Folk Theorems Punishing for a Limited Length of Time: A Perfect Folk Theorem for the Limit of Means Criterion Punishing the Punisher: A Perfect Folk Theorem for the Overtaking Criterion Rewarding Players Who Punish: A Perfect Folk Theorem for the Discounting Criterion The Structure of Subgame Perfect Equilibria Under the Discounting Criterion Finitely Repeated Game

TEXT BOOKS:

1. M. J. Osborne and A. Rubinstein, A course in Game Theory, MIT Press
2. Roger Myerson, Game Theory, Harvard University Press
3. D. Fudenberg and J. Tirole, Game Theory, MIT Press

REFERENCES:

1. J. von Neumann and O. Morgenstern, Theory of Games and Economic Behavior, New York: John Wiley and Sons.
2. R.D. Luce and H. Raiffa, Games and Decisions, New York: John Wiley and Sons.,
3. G. Owen, Game Theory, (Second Edition), New York: Academic Press,

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

**PARALLEL AND DISTRIBUTED ALGORITHMS
(Professional Elective- 2)**

Course Objectives:

- To learn parallel and distributed algorithms development techniques for shared memory and message passing models.
- To study the main classes of parallel algorithms.
- To study the complexity and correctness models for parallel algorithms.

UNIT-I

Basic Techniques, Parallel Computers for increase Computation speed, Parallel & Cluster Computing

UNIT-II

Message Passing Technique- Evaluating Parallel programs and debugging, Portioning and Divide and Conquer strategies examples

UNIT-III

Pipelining- Techniques computing platform, pipeline programs examples

UNIT-IV

Synchronous Computations, load balancing, distributed termination examples, programming with shared memory, shared memory multiprocessor constructs for specifying parallelism sharing data parallel programming languages and constructs, open MP

UNIT-V

Distributed shared memory systems and programming achieving constant memory distributed shared memory programming primitives, algorithms – sorting and numerical algorithms.

TEXT BOOK:

1. Parallel Programming, Barry Wilkinson, Michael Allen, Pearson Education, 2nd Edition.

REFERENCE BOOK:

1. Introduction to Parallel algorithms by Jaja from Pearson, 1992.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

**SOFTWARE ARCHITECTURE AND DESIGN PATTERNS
(Professional Elective- 2)**

Course Objectives: After completing this course, the student should be able to:

- To understand the concept of patterns and the Catalog.
- To discuss the Presentation tier design patterns and their affect on: sessions, client access, validation, and consistency.
- To understand the variety of implemented bad practices related to the Business and Integration tiers.
- To highlight the evolution of patterns.
- To how to add functionality to designs while minimizing complexity
- To understand what design patterns really are, and are not
- To learn about specific design patterns.
- To learn how to use design patterns to keep code quality high without overdesign.

UNIT - I

Envisioning Architecture

The Architecture Business Cycle, What is Software Architecture, Architectural patterns, reference models, reference architectures, architectural structures and views.

Creating an Architecture

Quality Attributes, Achieving qualities, Architectural styles and patterns, designing the Architecture, Documenting software architectures, Reconstructing Software Architecture.

UNIT - II

Analyzing Architectures

Architecture Evaluation, Architecture design decision making, ATAM, CBAM.

Moving from one system to many

Software Product Lines, Building systems from off the shelf components, Software architecture in future.

UNIT - III

Patterns

Pattern Description, Organizing catalogs, role in solving design problems, Selection and usage.

Creational and Structural patterns

Abstract factory, builder, factory method, prototype, singleton, adapter, bridge, composite, façade, flyweight.

UNIT - IV

Behavioral patterns

Chain of responsibility, command, Interpreter, iterator, mediator, memento, observer, state, strategy, template method, visitor.

UNIT - V

Case Studies

A-7E – A case study in utilizing architectural structures, The World Wide Web - a case study in interoperability, Air Traffic Control – a case study in designing for high availability, Celsius Tech – a case study in product line development,

TEXT BOOKS:

1. Software Architecture in Practice, second edition, Len Bass, Paul Clements & Rick Kazman, Pearson Education, 2003.
2. Design Patterns, Erich Gamma, Pearson Education, 1995.

REFERENCE BOOKS:

1. Beyond Software architecture, Luke Hohmann, Addison wesley, 2003.
2. Software architecture, David M. Dikel, David Kane and James R. Wilson, Prentice Hall PTR, 2001
3. Software Design, David Budgen, second edition, Pearson education, 2003
4. Head First Design patterns, Eric Freeman & Elisabeth Freeman, O'REILLY, 2007.
5. Design Patterns in Java, Steven John Metsker & William C. Wake, Pearson education, 2006
6. J2EE Patterns, Deepak Alur, John Crupi & Dan Malks, Pearson education, 2003.
7. Design Patterns in C#, Steven John metsker, Pearson education, 2004.
8. Pattern Oriented Software Architecture, F. Buschmann & others, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

**EMBEDDED SYSTEMS
(Professional Elective- 2)**

Course Objectives:

- To explain various embedded system applications and design requirements.
- To construct embedded system hardware.
- To develop software programs to control embedded system.
- To generate product specification for embedded system.

UNIT - I

Introduction to Embedded Systems: Embedded Systems, Processor Embedded into a System, Embedded Hardware Units and Devices in a System, Embedded Software, Complex System Design, Design Process in Embedded System, Formalization of System Design, Classification of Embedded Systems

UNIT - II

8051 and Advanced Processor Architecture: 8051 Architecture, 8051 Micro controller Hardware, Input/output Ports and Circuits, External Memory, Counter and Timers, Serial data Input/output, Interrupts, Introduction to Advanced Architectures, Real World Interfacing, Processor and Memory organization - **Devices and Communication Buses for Devices Network:** Serial and parallel Devices & ports, Wireless Devices, Timer and Counting Devices, Watchdog Timer, Real Time Clock, Networked Embedded Systems, Internet Enabled Systems, Wireless and Mobile System protocols

UNIT - III

Embedded Programming Concepts: Software programming in Assembly language and High Level Language, Data types, Structures, Modifiers, Loops and Pointers, Macros and Functions, object oriented Programming, Embedded Programming in C++ & JAVA

UNIT IV

Real – Time Operating Systems: OS Services, Process and Memory Management, Real – Time Operating Systems, Basic Design Using an RTOS, Task Scheduling Models, Interrupt Latency, Response of Task as Performance Metrics - **RTOS Programming:** Basic functions and Types of RTOSes, RTOS VxWorks, Windows CE

UNIT - V

Embedded Software Development Process and Tools: Introduction to Embedded Software Development Process and Tools, Host and Target Machines, Linking and Locating Software, Getting Embedded Software into the Target System, Issues in Hardware-Software Design and Co-Design - **Testing, Simulation and Debugging Techniques and Tools:** Testing on Host Machine, Simulators, Laboratory Tools

TEXT BOOK:

1. Embedded Systems, Raj Kamal, Second Edition TMH.

REFERENCE BOOKS:

1. Embedded/Real-Time Systems, Dr. K.V.K.K. Prasad, dream Tech press
2. The 8051 Microcontroller and Embedded Systems, Muhammad Ali Mazidi, Pearson.
3. The 8051 Microcontroller, Third Edition, Kenneth J Ayala, Thomson.

4. An Embedded Software Primer, David E. Simon, Pearson Education.
5. Micro Controllers, Ajay V Deshmukhi, TMH.
6. Microcontrollers, Raj kamal, Pearson Education.
7. Introduction to Embedded Systems, Shibu K.V, TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. I Year – I Sem CSE

ADVANCED ALGORITHMS LAB

Course Objectives:

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.

Sample Problems on Data structures:

1. Write Java programs that use both recursive and non-recursive functions for implementing the following searching methods:
 - a) Linear search
 - b) Binary search
2. Write Java programs to implement the following using arrays and linked lists
 - a) List ADT
3. Write Java programs to implement the following using an array.
 - a) Stack ADT
 - b) Queue ADT
4. Write a Java program that reads an infix expression and converts the expression to postfix form. (Use stack ADT).
5. Write a Java program to implement circular queue ADT using an array.
6. Write a Java program that uses both a stack and a queue to test whether the given string is a palindrome or not.
7. Write Java programs to implement the following using a singly linked list.
 - a) Stack ADT
 - b) Queue ADT
8. Write Java programs to implement the deque (double ended queue) ADT using
 - a) Array
 - b) Singly linked list
 - c) Doubly linked list.
9. Write a Java program to implement priority queue ADT.
10. Write a Java program to perform the following operations:
 - a) Construct a binary search tree of elements.
 - b) Search for a key element in the above binary search tree.
 - c) Delete an element from the above binary search tree.
11. Write a Java program to implement all the functions of a dictionary (ADT) using Hashing.
12. Write a Java program to implement Dijkstra's algorithm for Single source shortest path problem.
13. Write Java programs that use recursive and non-recursive functions to traverse the given binary tree in
 - a) Preorder
 - b) Inorder
 - c) Postorder.
14. Write Java programs for the implementation of bfs and dfs for a given graph.
15. Write Java programs for implementing the following sorting methods:
 - a) Bubble sort
 - b) Insertion sort
 - c) Quick sort
 - d) Merge sort
 - e) Heap sort
 - f) Radix sort
 - g) Binary tree sort
16. Write a Java program to perform the following operations:
 - a) Insertion into a B-tree
 - b) Searching in a B-tree
17. Write a Java program that implements Kruskal's algorithm to generate minimum cost spanning tree.
18. Write a Java program that implements KMP algorithm for pattern matching.

REFERENCE BOOKS:

1. Data Structures and Algorithms in java, 3rd edition, A.Drozdek, Cengage Learning.
2. Data Structures with Java, J.R.Hubbard, 2nd edition, Schaum's Outlines, TMH.

3. Data Structures and algorithms in Java, 2nd Edition, R.Lafore, Pearson Education.
4. Data Structures using Java, D.S.Malik and P.S. Nair, Cengage Learning.
5. Data structures, Algorithms and Applications in java, 2nd Edition, S.Sahani, Universities Press.
6. Design and Analysis of Algorithms, P. H. Dave and H.B.Dave, Pearson education.
7. Data Structures and java collections frame work, W. J. Collins, Mc Graw Hill.
8. Java: the complete reference, 7th edition, Herbert Schildt, TMH.
9. Java for Programmers, P. J. Deitel and H. M. Deitel, Pearson education / Java: How to Program P. J. Deitel and H. M. Deitel , 8th edition, PHI.
10. Java Programming, D.S. Malik, Cengage Learning.
11. A Practical Guide to Data Structures and Algorithms using Java, S. Goldman & K. Goldman, Chapman & Hall/CRC, Taylor & Francis Group.

(Note: Use packages like java.io, java.util, etc)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH IN EMBEDDED SYSTEMS.
EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH**

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Embedded System Design	25	75	4	0	0	4
PC-2	ARM Processor Architectures	25	75	4	0	0	4
PC-3	Real Time Operating Systems	25	75	4	0	0	4
PE-1	Advanced Computer Architecture CMOS VLSI Design CPLD and FPGA Architectures and Applications	25	75	3	0	0	3
PE-2	Digital System Design Embedded C TCP / IP Internetworking	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Embedded Systems Laboratory	25	75	0	0	3	2
Seminar I	Seminar - I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Embedded Computing	25	75	4	0	0	4
PC-5	System on Chip Architecture	25	75	4	0	0	4
PC-6	Sensors and Actuators	25	75	4	0	0	4
PE-3	Design for Testability Wireless Communications and Networks Scripting Languages	25	75	3	0	0	3
PE4	Advanced Digital Signal Processing Network Security and Cryptography Hardware Software Co-Design	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Advanced Embedded Systems Laboratory	25	75	0	0	3	2
Seminar II	Seminar - II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)

EMBEDDED COMPUTING (PC - 4)

UNIT – I

Programming on Linux Platform:

System Calls, Scheduling, Memory Allocation, Timers, Embedded Linux, Root File System, Busy Box.

Operating System Overview: Processes, Tasks, Threads, Multi-Threading, Semaphore, Message Queue.

UNIT – II

Introduction to Software Development Tools:

GNU GCC, make, gdb, static and dynamic linking, C libraries, compiler options, code optimization switches, lint, code profiling tools,.

UNIT – III

Interfacing Modules:

Sensor and actuator interface, data transfer and control, GPS, GSM module interfacing with data processing and display, OpenCV for machine vision, Audio signal processing.

UNIT – IV

Networking Basics:

Sockets, ports, UDP, TCP/IP, client server model, socket programming, 802.11, Bluetooth, ZigBee, SSH, firewalls, network security.

UNIT – V

IA32 Instruction Set: application binary interface, exception and interrupt handling, interrupt latency, assemblers, assembler directives, macros, simulation and debugging tools.

TEXT BOOKS

1. Peter Barry and Patrick Crowley, "Modern Embedded Computing", 1st Edition., Elsevier/Morgan Kaufmann, 2012.
2. Linux Application Development - Michael K. Johnson, Erik W. Troan, Addison Wesley, 1998.
3. Assembly Language for x86 Processors by Kip R. Irvine
4. Intel® 64 and IA-32 Architectures Software Developer Manuals

REFERENCE BOOKS

1. Abraham Silberschatz, Peter B. Galvin and Greg Gagne, "Operating System Concepts", Wiley
2. Maurice J. Bach, "The Design of the UNIX Operating System", Prentice-Hall
3. W. Richard Stevens, "UNIX Network Programming", Pearson

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)

SYSTEM ON CHIP ARCHITECTURE (PC - 5)

UNIT – I

Introduction to the System Approach: System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT – II

Processors: Introduction , Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT – III

Memory Design for SOC: Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation , SOC Memory System, Models of Simple Processor – memory interaction.

UNIT – IV

Interconnect Customization and Configuration: Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses , Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance-Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT – V

Application Studies / Case Studies: SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

TEXT BOOKS:

1. Computer System Design System-on-Chip - Michael J. Flynn and Wayne Luk, Wiley India Pvt. Ltd.
2. ARM System on Chip Architecture – Steve Furber –2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer
2. Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology) – Jason Andrews – Newnes, BK and CDROM.
3. System on Chip Verification – Methodologies and Techniques –Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)
SENSORS AND ACTUATORS (PC - 6)

UNIT - I

Sensors / Transducers: Principles, Classification, Parameters, Characteristics, Environmental Parameters (EP), Characterization.

Mechanical and Electromechanical Sensors: Introduction, Resistive Potentiometer, Strain Gauge, Resistance Strain Gauge, Semiconductor Strain Gauges, Inductive Sensors- Sensitivity and Linearity of the Sensor, Types- Capacitive Sensors, Electrostatic Transducer, Force/Stress Sensors using Quartz Resonators, Ultrasonic Sensors.

UNIT - II

Thermal Sensors: Introduction, Gas thermometric Sensors, Thermal Expansion Type Thermometric Sensors, Acoustic Temperature Sensor, Dielectric Constant and Refractive Index Thermo-sensors, Helium Low Temperature Thermometer, Nuclear Thermometer, Magnetic Thermometer, Resistance Change Type Thermometric Sensors, Thermo-EMF Sensors, Junction Semiconductor Types, Thermal Radiation Sensors, Quartz Crystal Thermo-electric Sensors, NQR Thermometry, Spectroscopic Thermometry, Noise Thermometry, Heat Flux Sensors.

Magnetic Sensors: Introduction, Sensors and the Principles Behind, Magneto-resistive Sensors, Anisotropic Magneto-resistive Sensing, Semiconductor Magneto-resistors, Hall Effect and Sensors, Inductance and Eddy Current Sensors, Angular/Rotary Movement Transducers, Synchros, Synchro-resolvers, Eddy Current Sensors, Electromagnetic Flowmeter, Switching Magnetic Sensors, SQUID Sensors.

UNIT - III

Radiation Sensors: Introduction – Basic Characteristics – Types of Photosensistors/Photo detectors– X-ray and Nuclear Radiation Sensors– Fiber Optic Sensors.

Electro Analytical Sensors: Introduction – The Electrochemical Cell – The Cell Potential – Standard Hydrogen Electrode (SHE) – Liquid Junction and Other Potentials – Polarization – Concentration Polarization— Reference Electrodes - Sensor Electrodes – Electro ceramics in Gas Media .

UNIT - IV

Smart Sensors: Introduction, Primary Sensors, Excitation, Amplification, Filters, Converters, Compensation, Information Coding/Processing, Data Communication, Standards for Smart Sensor Interface, the Automation.

Sensors Applications: Introduction, On-board Automobile Sensors (Automotive Sensors), Home Appliance Sensors, Aerospace Sensors, Sensors for Manufacturing, Sensors for environmental Monitoring.

UNIT - V

Actuators: Pneumatic and Hydraulic Actuation Systems- Actuation systems, Pneumatic and hydraulic systems, Directional Control valves, Pressure control valves, Cylinders, Servo and proportional control valves, Process control valves, Rotary actuators, Mechanical Actuation Systems- Types of motion, Kinematic chains, Cams, Gears, Ratchet and pawl, Belt and chain drives, Bearings, Mechanical aspects of motor selection, Electrical Actuation Systems, Electrical systems, Mechanical switches, Solid-state switches, Solenoids, D.C. Motors, A.C. Motors, Stepper motors.

TEXT BOOKS:

1. D. Patranabis, "Sensors and Transducers", PHI Learning Private Limited.
2. W. Bolton, "Mechatronics", Pearson Education Limited.

REFERENCE BOOKS:

1. Patranabis, "Sensors and Actuators", 2nd Edition, PHI, 2013.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)

DESIGN FOR TESTABILITY (PE - 3)

UNIT - I

Introduction to Testing: Testing Philosophy, Role of Testing, Digital and Analog VLSI Testing, VLSI Technology Trends affecting Testing, Types of Testing, Fault Modeling: Defects, Errors and Faults, Functional Versus Structural Testing, Levels of Fault Models, Single Stuck-at Fault.

UNIT - II

Logic and Fault Simulation: Simulation for Design Verification and Test Evaluation, Modeling Circuits for Simulation, Algorithms for True-value Simulation, Algorithms for Fault Simulation, ATPG.

UNIT - III

Testability Measures: SCOAP Controllability and Observability, High Level Testability Measures, Digital DFT and Scan Design: Ad-Hoc DFT Methods, Scan Design, Partial-Scan Design, Variations of Scan.

UNIT - IV

Built-In Self-Test: The Economic Case for BIST, Random Logic BIST: Definitions, BIST Process, Pattern Generation, Response Compaction, Built-In Logic Block Observers, Test-Per-Clock, Test-Per-Scan BIST Systems, Circular Self Test Path System, Memory BIST, Delay Fault BIST.

UNIT - V

Boundary Scan Standard: Motivation, System Configuration with Boundary Scan: TAP Controller and Port, Boundary Scan Test Instructions, Pin Constraints of the Standard, Boundary Scan Description Language: BDSL Description Components, Pin Descriptions.

TEXT BOOK:

1. M.L. Bushnell, V. D. Agrawal, "Essential of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits", Kluwer Academic Publishers.

REFERENCE BOOKS:

1. M. Abramovici, M. A. Breuer and A.D Friedman, "Digital Systems and Testable Design", Jaico Publishing House.
2. P. K. Lala, "Digital Circuits Testing and Testability", Academic Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)

WIRELESS COMMUNICATIONS AND NETWORKS (PE - 3)

UNIT - I

The Cellular Concept-System Design Fundamentals: Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference , Power Control for Reducing interference, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring .

UNIT – II

Mobile Radio Propagation: Large-Scale Path Loss: Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, The Three Basic Propagation Mechanisms, Reflection-Reflection from Dielectrics, Brewster Angle, Reflection from perfect conductors, Ground Reflection (Two-Ray) Model, Diffraction-Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- Longley-Rice Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models-Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT – III

Mobile Radio Propagation: Small –Scale Fading and Multipath: Small Scale Multipath propagation-Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel- Relationship between Bandwidth and Received power, Small-Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small-Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler spread in Clarke's model, Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT - IV

Equalization and Diversity: Introduction, Fundamentals of Equalization, Training A Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity Techniques-Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT - V

Wireless Networks: Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11, IEEE 802.11 Medium Access Control, Comparison of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, Hiper Lan, WLL.

TEXT BOOKS:

1. Theodore, S. Rappaport, "Wireless Communications, Principles, Practice", 2nd Ed., 2002, PHI.
2. Andrea Goldsmith, "Wireless Communications", 2005 Cambridge University Press.
3. Kaveh Pah Laven and P. Krishna Murthy, "Principles of Wireless Networks", 2002, PE
4. Gottapu Sasibhushana Rao, "Mobile Cellular Communication", Pearson Education, 2012.

REFERENCE BOOKS:

1. Kamilo Feher, "Wireless Digital Communications", 1999, PHI.
2. William Stallings, "Wireless Communication and Networking", 2003, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)

SCRIPTING LANGUAGES (PE - 3)

UNIT - I

Introduction to Scripts and Scripting: Characteristics and uses of scripting languages, Introduction to PERL, Names and values, Variables and assignment, Scalar expressions, Control structures, Built-in functions, Collections of Data, Working with arrays, Lists and hashes, Simple input and output, Strings, Patterns and regular expressions, Subroutines, Scripts with arguments.

UNIT - II

Advanced PERL: Finer points of Looping, Subroutines, Using Pack and Unpack, Working with files, Navigating the file system, Type globs, Eval, References, Data structures, Packages, Libraries and modules, Objects, Objects and modules in action, Tied variables, Interfacing to the operating systems, Security issues.

UNIT - III

TCL: The TCL phenomena, Philosophy, Structure, Syntax, Parser, Variables and data in TCL, Control flow, Data structures, Simple input/output, Procedures, Working with Strings, Patterns, Files and Pipes, Example code.

UNIT - IV

Advanced TCL: The eval, source, exec and up-level commands, Libraries and packages, Namespaces, Trapping errors, Event-driven programs, Making applications 'Internet-aware', 'Nuts-and-bolts' internet programming, Security issues, running untrusted code, The C interface.

UNIT - V

TK and JavaScript: Visual tool kits, Fundamental concepts of TK, TK by example, Events and bindings, Geometry managers, PERL-TK. JavaScript – Object models, Design Philosophy, Versions of JavaScript, The Java Script core language, Basic concepts of Python.

Object Oriented Programming Concepts (Qualitative Concepts Only): Objects, Classes, Encapsulation, Data Hierarchy.

TEXT BOOKS:

1. David Barron, "The World of Scripting Languages", Wiley Student Edition, 2010.
2. Brent Welch, Ken Jones and Jeff Hobbs., "Practical Programming in Tcl and Tk", 4th Edition, Prentice Hall
3. Herbert Schildt, "Java the Complete Reference", 7th Edition, TMH.

REFERENCE BOOKS:

1. Clif Flynt, "Tcl/Tk: A Developer's Guide", 2003, Morgan Kaufmann Series.
2. John Ousterhout, "Tcl and the Tk Toolkit", 2nd Edition, 2009, Kindel Edition.
3. Wojciech Kocjan and Piotr Beltowski, "Tcl 8.5 Network Programming book", Packt Publishing.
4. Bert Wheeler, "Tcl/Tk 8.5 Programming Cookbook", 2011, Packt Publishing Limited.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER (EMBEDDED SYSTEMS)

ADVANCED DIGITAL SIGNAL PROCESSING (PE - 4)

UNIT – I

Review of DFT, FFT, IIR Filters and FIR Filters: Introduction to filter structures (IIR & FIR). Implementation of Digital Filters, specifically 2nd Order Narrow Band Filter and 1st Order All Pass Filter. Frequency sampling structures of FIR, Lattice structures, Forward prediction error, Backward prediction error, Reflection coefficients for lattice realization, Implementation of lattice structures for IIR filters, Advantages of lattice structures.

UNIT - II

Non-Parametric Methods: Estimation of spectra from finite duration observation of signals, Non-parametric Methods: Bartlett, Welch & Blackman-Tukey methods, Comparison of all Non-Parametric methods

UNIT - III

Parametric Methods: Autocorrelation & Its Properties, Relation between auto correlation & model parameters, AR Models - Yule-Walker & Burg Methods, MA & ARMA models for power spectrum estimation, Finite word length effect in IIR digital Filters – Finite word-length effects in FFT algorithms.

UNIT – IV

Multi Rate Signal Processing: Introduction, Decimation by a factor D, Interpolation by a factor I, Sampling rate conversion by a rational factor I/D, Multistage Implementation of Sampling Rate Conversion, Filter design & Implementation for sampling rate conversion. Examples of up-sampling using an All Pass Filter.

UNIT – V

Applications of Multi Rate Signal Processing: Design of Phase Shifters, Interfacing of Digital Systems with Different Sampling Rates, Implementation of Narrow Band Low Pass Filters, Implementation of Digital Filter Banks, Sub-band Coding of Speech Signals, Quadrature Mirror Filters, Transmultiplexers, Over Sampling A/D and D/A Conversion.

TEXT BOOKS:

1. .G. Proakis & D. G. Manolakis, "Digital Signal Processing: Principles, Algorithms & Applications", J 4th Edition, PHI.
2. Alan V Oppenheim & Ronald W Schaffer, "Discrete Time signal processing", PHI.
3. Emmanuel C. Ifeache, Barrie. W. Jervis, "DSP – A Practical Approach", 2nd Edition, Pearson Education.

REFERENCE BOOKS:

1. S. M .Kay, "Modern spectral Estimation: Theory & Application" 1988, PHI.
2. P. P. Vaidyanathan, "Multi Rate Systems and Filter Banks", Pearson Education.
3. Kaluri V. Rangarao, Ranjan K. Mallik, "Digital Signal Processing: A Practitioner's Approach", ISBN: 978-0-470-01769-2, 210 pages, November 2006 John Wiley.
4. S. Salivahanan, A. Vallavaraj, C. Gnanapriya, "Digital Signal Processing", 2000, TMH

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)

NETWORK SECURITY AND CRYPTOGRAPHY (PE - 4)

UNIT- I

Introduction: Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security, Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Block Cipher Design Principles.

UNIT- II

Encryption Algorithms: Triple DES, International Data Encryption algorithm, Blowfish, RC5, Characteristics of Advanced Symmetric block ciphers.

Conventional Encryption: Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT - III

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography.

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT - IV

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD File, Message digest Algorithm, Secure Hash Algorithm.

Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT – V

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Key Management. Web Security: Web Security requirements, secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. William Stallings, "Cryptography and Network Security: Principles and Practice", Pearson Education.
2. William Stallings, "Network Security Essentials (Applications and Standards)", Pearson Education.

REFERENCE BOOKS:

1. Eric Maiwald, "Fundamentals of Network Security", Dreamtech press.
2. Charlie Kaufman, Radia Perlman and Mike Speciner, "Network Security - Private Communication in a Public World", Pearson/PHI.
3. Whitman, "Principles of Information Security", Thomson.
4. Robert Bragg, Mark Rhodes, "Network Security: The complete reference", TMH
5. Buchmann, "Introduction to Cryptography", Springer.

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)**

HARDWARE SOFTWARE CO-DESIGN (PE - 4)

UNIT – I

Co - Design Issues: Co- Design Models, Architectures, Languages, A Generic Co-design Methodology.

Co - Synthesis Algorithms: Hardware software synthesis algorithms: hardware – software partitioning distributed system co-synthesis.

UNIT – II

Prototyping and Emulation: Prototyping and emulation techniques, prototyping and emulation environments, future developments in emulation and prototyping architecture specialization techniques, system communication infrastructure.

Target Architectures: Architecture Specialization techniques, System Communication infrastructure, Target Architecture and Application System classes, Architecture for control dominated systems (8051-Architectures for High performance control), Architecture for Data dominated systems (ADSP21060, TMS320C60), Mixed Systems.

UNIT – III

Compilation Techniques and Tools for Embedded Processor Architectures: Modern embedded architectures, embedded software development needs, compilation technologies, practical consideration in a compiler development environment.

UNIT – IV

Design Specification and Verification: Design, co-design, the co-design computational model, concurrency coordinating concurrent computations, interfacing components, design verification, implementation verification, verification tools, interface verification

UNIT – V

Languages for System – Level Specification and Design-I: System – level specification, design representation for system level synthesis, system level specification languages,

Languages for System – Level Specification and Design-II: Heterogeneous specifications and multi-language co-simulation, the cosyma system and lycos system.

TEXT BOOKS:

1. Jorgen Staunstrup, "Hardware / Software Co- Design Principles and Practice", Wayne Wolf, 2009, Springer.
2. Giovanni De Micheli, Mariagiovanna Sami, "Hardware / Software Co- Design", 2002, Kluwer Academic Publishers

REFERENCE BOOKS:

1. Patrick R. Schaumont, "A Practical Introduction to Hardware/Software Co-design", 2010, Springer

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER (EMBEDDED SYSTEMS)**

ADVANCED EMBEDDED SYSTEMS LAB

The following programs to understand the use of RTOS with ARM Processor on IDE Environment Arm Tool chain and Library:

1. Create an application that creates two tasks that wait on a timer whilst the main task loops.
2. Write an application that creates a task which is scheduled when a button is pressed, which illustrates the use of an event set between an ISR and a task.
3. Write an application to demonstrate the Interruptible ISRs(Requires timer to have higher priority than external interrupt button
4. Write an application to test message queues and memory blocks
5. Write an application to test byte queues
6. Write an application that creates two tasks of the same priority and sets the time slice period to illustrate time slicing

Interfacing Programs:

7. Write an application that creates two tasks to blinking two different LEDs at different timings.
8. Write an application that creates two tasks displaying two different messages in LCD displays in two lines
9. Sending messages to mailbox by one task and reading the message from mailbox by another task
10. Sending messages to PC through serial port by three different tasks on priority basis
11. Basic Audio processing on IDE environment
12. Design and Simulation of Adder, Logic Gates, Decoders, Multiplexers, Flip-flops, Counters on FPGA Board.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH IN EMBEDDED SYSTEMS.
EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH**

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Embedded System Design	25	75	4	0	0	4
PC-2	ARM Processor Architectures	25	75	4	0	0	4
PC-3	Real Time Operating Systems	25	75	4	0	0	4
PE-1	Advanced Computer Architecture CMOS VLSI Design CPLD and FPGA Architectures and Applications	25	75	3	0	0	3
PE-2	Digital System Design Embedded C TCP / IP Internetworking	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Embedded Systems Laboratory	25	75	0	0	3	2
Seminar I	Seminar	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Embedded Computing	25	75	4	0	0	4
PC-5	System On Chip Architecture	25	75	4	0	0	4
PC-6	Sensors and Actuators	25	75	4	0	0	4
PE-3	Design for Testability Wireless Communication and Networks Scripting Languages	25	75	3	0	0	3
PE4	Advanced Digital Signal Processors Network Security and Cryptography Hardware Software Co-Design	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Advanced Embedded Systems Laboratory	25	75	0	0	3	2
Seminar II	Seminar	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

EMBEDDED SYSTEM DESIGN (PC-1)

UNIT -I

Introduction to Embedded Systems: Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT -II

Typical Embedded System: Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT -III

Embedded Firmware: Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT -IV

RTOS Based Embedded System Design: Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT -V

Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOKS:

1. Shibu K.V, "Introduction to Embedded Systems", McGraw Hill.

REFERENCE BOOKS:

1. Raj Kamal, "Embedded Systems", TMH.
2. Frank Vahid, Tony Givargis, "Embedded System Design", John Wiley.
3. Lyla, "Embedded Systems", Pearson, 2013
4. David E. Simon, "An Embedded Software Primer", Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

ARM PROCESSOR ARCHITECTURES (PC-2)

UNIT – I

ARM Architecture and Instruction Set: ARM Design Philosophy, Registers, PSR, Pipeline, Interrupts and Vector Table, Architecture Revision, ARM Processor Families. Instruction Set: Data Processing Instructions, Branch, Load, Store Instructions, PSR Instructions, Conditional Instructions.

UNIT – II

ARM Programming Model: Thumb Instruction Set: Register Usage, Other Branch Instructions, Data Processing Instructions, Single-Register and Multi Register Load-Store Instructions, Stack, Interrupts, Software Interrupt Instructions, Exception handling

UNIT – III

ARM Programming using High Level Language: Simple C Programs using Function Calls, Pointers, Structures, Integer and Floating Point Arithmetic, Assembly Code using Instruction Scheduling, Register Allocation, Conditional Execution and Loops.

UNIT – IV

Memory Management: Cache Architecture, Polices, Flushing and Caches, MMU, Page Tables, Translation, Access Permissions, Content Switch.

UNIT – V

Integer and Floating Point Arithmetic on ARM: Double precision Integer Multiplication, Division, Square roots, Endian Reversal and Bit Operations, Random Number Generation, DSP on ARM – FIR filters, IIR filters.

TEXT BOOKS:

1. Andrew N. Sloss, Dominic Symes, Chris Wright, "ARM Systems Developer's Guides- Designing & Optimizing System Software", 2008, Elsevier.

REFERENCE BOOKS:

1. Jonathan W. Valvano – Brookes / Cole, "Embedded Microcomputer Systems, Real Time Interfacing", 1999, Thomas Learning.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

REAL TIME OPERATING SYSTEMS (PC-3)

UNIT – I

Introduction: Introduction to UNIX/LINUX, Overview of Commands, File I/O,(open, create, close, lseek, read, write), Process Control (fork, vfork, exit, wait, waitpid, exec).

UNIT - II

Real Time Operating Systems: Brief History of OS, Defining RTOS, The Scheduler, Objects, Services, Characteristics of RTOS, Defining a Task, tasks States and Scheduling, Task Operations, Structure, Synchronization, Communication and Concurrency.
Defining Semaphores, Operations and Use, Defining Message Queue, States, Content, Storage, Operations and Use

UNIT - III

Objects, Services and I/O: Pipes, Event Registers, Signals, Other Building Blocks, Component Configuration, Basic I/O Concepts, I/O Subsystem

UNIT - IV

Exceptions, Interrupts and Timers: Exceptions, Interrupts, Applications, Processing of Exceptions and Spurious Interrupts, Real Time Clocks, Programmable Timers, Timer Interrupt Service Routines (ISR), Soft Timers, Operations.

UNIT - V

Case Studies of RTOS: RT Linux, MicroC/OS-II, Vx Works, Embedded Linux, and Tiny OS.

TEXT BOOKS:

1. Qing Li, "Real Time Concepts for Embedded Systems", Elsevier, 2011

REFERENCE BOOKS:

1. Rajkamal, "Embedded Systems- Architecture, Programming, and Design", 2007, TMH.
2. W. Richard Stevens, Stephen A. Rago, "Advanced UNIX Programming", 2006, 2nd Edition, Pearson.
3. Dr. Craig Hollabaugh, "Embedded Linux: Hardware, Software and Interfacing", 2008, 1st Edition, Pearson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

ADVANCED COMPUTER ARCHITECTURE (PE-1)

UNIT- I

Fundamentals of Computer Design: Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, measuring and reporting performance, quantitative principles of computer design, Amdahl's law.

Instruction set principles and examples- Introduction, classifying instruction set- memory addressing-type and size of operands, operations in the instruction set.

UNIT – II

Pipelines: Introduction ,basic RISC instruction set ,Simple implementation of RISC instruction set, Classic five stage pipe line for RISC processor, Basic performance issues in pipelining , Pipeline hazards, Reducing pipeline branch penalties.

Memory Hierarchy Design: Introduction, review of ABC of cache, Cache performance, Reducing cache miss penalty, Virtual memory.

UNIT - III

Instruction Level Parallelism the Hardware Approach: Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, high performance instruction delivery- hardware based speculation.

ILP Software Approach: Basic compiler level techniques, static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues -Hardware verses Software.

UNIT – IV

Multi Processors and Thread Level Parallelism: Multi Processors and Thread level Parallelism-Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – memory architecture, Synchronization.

UNIT – V

Inter Connection and Networks: Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.

Intel Architecture: Intel IA- 64 ILP in embedded and mobile markets Fallacies and pit falls

TEXT BOOKS:

1. John L. Hennessy, David A. Patterson, "Computer Architecture: A Quantitative Approach", 3rd Edition, An Imprint of Elsevier.

REFERENCE BOOKS:

1. John P. Shen and Miikko H. Lipasti, "Modern Processor Design : Fundamentals of Super Scalar Processors", 2002, Beta Edition, McGrawHill
2. Kai Hwang, Faye A.Brigs., "Computer Architecture, and Parallel Processing", McGraw Hill.,
3. Dezso Sima, Terence Fountain, Peter Kacsuk , "Advanced Computer Architecture - A Design Space Approach", Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

CMOS VLSI DESIGN (PE-1)

UNIT-I

MOS Transistor: Introduction, Ideal I-V Characteristics, C-V Characteristics, Non ideal I-V Effects, DC Transfer Characteristics.

CMOS Process Technology: CMOS Technologies, Layout Design Rules, CMOS Process Enhancements, Technology related CAD Issues.

UNIT-II

Circuit Characterization: Delay Estimation, Logical effort and Transistor Sizing, Power Dissipation, Interconnect, Design Margin, Reliability, Scaling.

UNIT-III

Combinational and Sequential Circuit Design: Circuit Families, More circuit families, Low power Logic Design, Comparison of Circuit Families, Sequencing Static Circuits, Circuit Design of Latches and Flip-Flops, Static Sequencing Element Methodology, Sequencing Dynamic Circuits, Synchronizers.

UNIT-IV

Datapath Subsystems: Addition, Subtraction, Comparators, Counters, Boolean Logical Operations, Coding, Shifters, Multiplication, Division.

Array Subsystems: SRAM, DRAM, Read-only Memory, Serial Access Memories, Content addressable Memory, PLAs, Array Yield, Reliability and Self-Test.

UNIT-V

Design Methodology and Tools: Design Methodology, Design Flows, Design Economics, Data Sheets and Documentation, CMOS Physical Design Styles, Interchange Formats.

Special Purpose Subsystem: Packaging.

TEXTBOOKS:

1. Neil Weste, David Harris, Ayan Banerjee, "CMOS VLSI DESIGN", 3rd Edition, Pearson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

CPLD AND FPGA ARCHITECTURES AND APPLICATIONS (PE-1)

UNIT-I

Introduction to Programmable Logic Devices: Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation.

UNIT-II

Field Programmable Gate Arrays: Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, Applications of FPGAs.

UNIT -III

SRAM Programmable FPGAs: Introduction, Programming Technology, Device Architecture, The Xilinx XC2000, XC3000 and XC4000 Architectures.

UNIT -IV

Anti-Fuse Programmed FPGAs: Introduction, Programming Technology, Device Architecture, The Actel ACT1, ACT2 and ACT3 Architectures.

UNIT -V

Design Applications: General Design Issues, Counter Examples, A Fast Video Controller, A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.

TEXT BOOKS:

1. Stephen M. Trimberger, "Field Programmable Gate Array Technology", Springer International Edition.
2. Charles H. Roth Jr, LizyKurian John, "Digital Systems Design", Cengage Learning.

REFERENCE BOOKS:

1. John V. Oldfield, Richard C. Dorf, "Field Programmable Gate Arrays", Wiley India.
2. Pak K. Chan/Samiha Mourad, "Digital Design Using Field Programmable Gate Arrays", Pearson Low Price Edition.
3. Ian Grout, "Digital Systems Design with FPGAs and CPLDs", Elsevier, Newnes.
4. Wayne Wolf, "FPGA based System Design", Prentice Hall Modern Semiconductor Design Series.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

DIGITAL SYSTEM DESIGN (PE-2)

UNIT -I

Minimization and Transformation of Sequential Machines: The Finite State Model – Capabilities and limitations of FSM – State equivalence and machine minimization – Simplification of incompletely specified machines. Fundamental mode model – Flow table – State reduction – Minimal closed covers – Races, Cycles and Hazards.

UNIT -II

Digital Design: Digital Design Using ROMs, PALs and PLAs, BCD Adder, 32 – bit adder, State graphs for control circuits, Scoreboard and Controller, A shift and add multiplier, Array multiplier, Keypad Scanner, Binary divider.

UNIT -III

SM Charts: State machine charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, dice game controller.

UNIT -IV

Fault Modeling & Test Pattern Generation: Logic Fault model – Fault detection & Redundancy- Fault equivalence and fault location –Fault dominance – Single stuck at fault model – Multiple stuck at fault models –Bridging fault model. Fault diagnosis of combinational circuits by conventional methods – Path sensitization techniques, Boolean Difference method – Kohavi algorithm – Test algorithms – D algorithm, PODEM, Random testing, Transition count testing, Signature analysis and test bridging faults.

UNIT -V

Fault Diagnosis in Sequential Circuits: Circuit Test Approach, Transition Check Approach – State identification and fault detection experiment, Machine identification, Design of fault detection experiment

TEXT BOOKS:

1. Charles H. Roth, "Fundamentals of Logic Design", 5th Edition, Cengage Learning.
2. Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman, "Digital Systems Testing and Testable Design", John Wiley & Sons Inc.
3. N. N. Biswas, "Logic Design Theory", PHI

REFERENCE BOOKS:

1. Z. Kohavi , "Switching and Finite Automata Theory", 2nd Edition, 2001, TMH
2. Morris Mano, M.D.Ciletti, "Digital Design", 4th Edition, PHI.
3. Samuel C. Lee , "Digital Circuits and Logic Design", PHI

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

EMBEDDED C (PE-2)

UNIT – I

Programming Embedded Systems in C: Introduction ,What is an embedded system, Which processor should you use, Which programming language should you use, Which operating system should you use, How do you develop embedded software, Conclusions.

Introducing the 8051 Microcontroller Family: Introduction, What's in a name, The external interface of the Standard 8051, Reset requirements ,Clock frequency and performance, Memory issues, I/O pins, Timers, Interrupts, Serial interface, Power consumption ,Conclusions.

UNIT – II

Reading Switches: Introduction, Basic techniques for reading from port pins, Example: Reading and writing bytes, Example: Reading and writing bits (simple version), Example: Reading and writing bits (generic version), The need for pull-up resistors, Dealing with switch bounce, Example: Reading switch inputs (basic code), Example: Counting goats, Conclusions.

UNIT – III

Adding Structure to the Code: Introduction, Object-oriented programming with C, The Project Header (MAIN.H), The Port Header (PORT.H), Example: Restructuring the 'Hello Embedded World' example, Example: Restructuring the goat-counting example, Further examples, Conclusions.

UNIT – IV

Meeting Real-Time Constraints: Introduction, Creating 'hardware delays' using Timer 0 and Timer 1, Example: Generating a precise 50 ms delay, Example: Creating a portable hardware delay, Why not use Timer 2?, The need for 'timeout' mechanisms, Creating loop timeouts, Example: Testing loop timeouts, Example: A more reliable switch interface, Creating hardware timeouts, Example: Testing a hardware timeout, Conclusions.

UNIT – V

Case Study: Intruder Alarm System: Introduction, The software architecture, Key software components used in this example, running the program, the software, Conclusions.

TEXT BOOKS:

1. Michael J. Pont, "Embedded C", 2nd Edition, Pearson Education, 2008

REFERENCE BOOKS:

1. Nigel Gardner, "PIC micro MCU C-An introduction to programming", The Microchip PIC in CCS C -

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

TCP / IP INTERNETWORKING (PE-2)

UNIT - I

Network Models: Layered Tasks, The OSI Model, Layers in OSI Model, TCP/IP Protocol suite, Addressing.

Connecting devices: Passive Hubs, Repeaters, Active Hubs, Bridges, Two Layer Switches, Routers, Three Layer Switches, Gateway, Backbone Networks.

UNIT - II

Internetworking Concepts: Principles of Internetworking, Connectionless Interconnection, Application Level Interconnection, Network Level Interconnection, Properties of the Internet, Internet Architecture, Interconnection through IP Routers

TCP, UDP & IP: TCP Services, TCP Features, Segment, A TCP Connection, Flow Control, Error Control, Congestion Control, Process to Process Communication, User Datagram, Checksum, UDP Operation, IP Datagram, Fragmentation, Options, IP Addressing: Classful Addressing, IPV6.

UNIT - III

Congestion and Quality of Service: Data Traffic, Congestion, Congestion Control, Congestion Control in TCP, Congestion Control in Frame Relay, Source Based Congestion Avoidance, DEC Bit Scheme, Quality of Service, Techniques to Improve QOS: Scheduling, Traffic Shaping, Admission Control, Resource Reservation, Integrated Services and Differentiated Services.

UNIT - IV

Queue Management: Concepts of Buffer Management, Drop Tail, Drop Front, Random Drop, Passive Buffer Management Schemes, Drawbacks of PQM, Active Queue Management: Early Random Drop, RED Algorithm.

UNIT - V

Stream Control Transmission Protocol: SCTP Services, SCTP Features, Packet Format, Flow Control, Error Control, Congestion Control.

Mobile Network Layer: Entities and Terminology, IP Packet Delivery, Agents, Addressing, Agent Discovery, Registration, Tunneling and Encapsulating, Inefficiency in Mobile IP.

Mobile Transport Layer : Classical TCP Improvements, Indirect TCP, Snooping TCP, Mobile TCP, Fast Retransmit/Fast Recovery, Transmission, Timeout Freezing, Selective Retransmission, Transaction Oriented TCP.

TEXT BOOKS:

1. Behrouz A Forouzan, "TCP/IP Protocol Suite", 3rd Edition, TMH.
2. B.A. Forouzan, "Data communication & Networking", 4th Edition, TMH.

REFERENCES:

1. Mahbub Hasan & Raj Jain, " High performance TCP/IP Networking", PHI -2005
2. Douglas. E.Comer, "Internetworking with TCP/IP ", Volume I PHI
3. Larry L. Perterson and Bruce S. Davie , "Computer Networks- A Systems Approach", 2011, Morgan Kaufmann
4. Jochen Schiiler, "Mobile Communications" , Pearson , 2nd Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS**

EMBEDDED SYSTEMS LABORATORY

Note: Minimum of 10 Experiments have to be conducted

Part-I

- A. Write a program
 - i. Write a simple program to print "hello world"
 - ii. Write a simple program to show a delay.
 - iii. Write a program for counting the number of times that a switch is pressed & released.
 - iv. Write a c program to test loop time outs.
 - v. Write a c program to test hardware based timeout loops.
 - vi. Illustrate the use of port header file (port M) using an interface consisting of a keypad and liquid crystal display.
- B. Write a program to create a portable hardware delay.
- C. Write a loop application to copy values from P1 to P2
- D. Develop a simple EOS showing traffic light sequencing.
- E. Write a program to drive SEOS using Timer 0.

Part-II

The following programs are to be implemented on ARM Processor

- 1. Simple assembly program for addition, Subtraction, Multiplication, Division, Operating Modes, System Calls and Interrupts, Loops, Branches.
- 2. Write an Assembly program to configure and control general purpose input/output (GPIO) port pins
- 3. To read digital values from external peripherals and execute them with the target board
- 4. Program for reading and writing of a file
- 5. To demonstrate time delay program using built in timer / counter feature on IDE environment
- 6. To demonstrate a simple interrupt handler and setting up a timer
- 7. Program to demonstrate a simple interrupt handlers. Press button to generate an interrupt and trace the program flow with debug terminal
- 8. Program to interface 8 Bit LED and Switch Interface
- 9. Program to implement Buzzer Interface on IDE environment

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. (ELECTRICAL POWER SYSTEMS/ POWER SYSTEM CONTROL AND AUTOMATION/
ELECTRICAL POWER ENGINEERING)**

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Power System Analysis	25	75	4	0	0	4
PC-2	Advanced Power System Protection	25	75	4	0	0	4
PC-3	Modern Control Theory	25	75	4	0	0	4
PE-1	1. EHV AC Transmission 2. High Voltage Engineering 3. Advanced Digital Signal Processing	25	75	3	0	0	3
PE-2	1. Power Quality 2. Microcontrollers and applications 3. Distribution Automation	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Power & Energy Systems Lab - I	25	75	0	0	3	2
Seminar I	Seminar – I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Power System Dynamics and Control	25	75	4	0	0	4
PC-5	Flexible AC Transmission Systems (FACTS)	25	75	4	0	0	4
PC-6	Power System Operation and Deregulation	25	75	4	0	0	4
PE-3	1. Gas Insulated Systems(GIS) 2. Programmable Logic Controllers and applications 3. Energy Auditing Conservation and Management	25	75	3	0	0	3
PE4	1. Reactive Power Compensation and Management 2. Power System Reliability 3. Voltage Stability	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Power and Energy Systems Lab - II	25	75	0	0	3	2
Seminar II	Seminar –II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/ PSC&A)

POWER SYSTEM DYNAMICS AND CONTROL (Professional Core - IV)

Prerequisite: Computer Methods in Power Systems

Course objectives:

- To remember the dynamic characteristics of power system equipment,
- To recognize dynamic performance of power systems
- To illustrate the system stability and controls.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Choose the fundamental dynamic behavior and controls of power systems to perform basic stability analysis.
- Comprehend concepts in modeling and simulating the dynamic phenomena of power systems Interpret results of system stability studies
- Analyze theory and practice of modeling main power system components, such as synchronous machines, excitation systems and governors

UNIT- I:

Basic Concepts: Power system stability states of operation and system security - system dynamics - problems system model analysis of steady State stability and transient stability - simplified representation of Excitation control.

UNIT- II:

Modeling of Synchronous Machine: Synchronous machine - park's Transformation-analysis of steady state performance per - unit quantities-Equivalent circuits of synchronous machine-determination of parameters of equivalent circuits.

UNIT- III:

Excitation System: Excitation system modeling-excitation systems block Diagram - system representation by state equations- Dynamics of a synchronous generator connected to infinite bus - system model Synchronous machine model-stator equations rotor equations - Synchronous machine model with field circuit - one equivalent damper winding on q axis (model 1.1) - calculation of Initial conditions.

UNIT- IV:

Analysis of Single Machine System: Small signal analysis with block diagram - Representation Characteristic equation and application of Routh Hurwitz criterion- synchronizing and damping torque analysis-small signal model - State equations.

UNIT - V:

Application of Power System Stabilizers: Basic concepts in applying PSS - Control signals - Structure and tuning of PSS - Washout circuit - Dynamic compensator analysis of single machine infinite bus system with and without PSS.

TEXT BOOKS:

1. K. R. PADIYAR," Power system dynamics " - B.S. Publications.
2. P.M. Anderson and A. A. Fouad, "Power system control and stability", IEEE Press

REFERENCE:

1. R. Ramanujam, "Power Systems Dynamics"- PHI Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/ PSC&A)

FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS) (Professional Core - V)

Prerequisite: Power Electronics and Power Systems-II

Course Objectives:

- To understand the fundamentals of FACTS Controllers, Importance of controllable parameters and types of FACTS controllers & their benefits
- To recall the objectives of Shunt and Series compensation
- To explain control of STATCOM and SVC and their comparison And the regulation of STATCOM
- To analyze the functioning and control of GCSC, TSSC and TCSC

Course Outcomes: Upon the completion of the subject, the student will be able to

- Choose proper controller for the specific application based on system requirements
- Understand various systems thoroughly and their requirements
- Interpret the control circuits of Shunt Controllers SVC & STATCOM for various functions viz. Transient stability Enhancement, voltage instability prevention and power oscillation damping
- Detect the Power and control circuits of Series Controllers GCSC, TSSC and TCSC

UNIT - I:

Facts Concepts: Transmission interconnections power flow in an AC system, loading capability limits, Dynamic stability considerations, importance of controllable parameters basic types of FACTS controllers, benefits from FACTS controllers.

UNIT - II:

Voltage Source Converters : Single phase three phase full wave bridge converters transformer connections for 12 pulse 24 and 48 pulse operation. Three level voltage source converter, pulse width modulation converter, basic concept of current source Converters, and comparison of current source converters with voltage source converters.

UNIT - III:

Static Shunt Compensation: Objectives of shunt compensation, mid-point voltage regulation voltage instability prevention, improvement of transient stability, Power oscillation damping, Methods of controllable VAR generation, variable impedance type static VAR generators switching converter type VAR generators hybrid VAR generators.

UNIT - IV:

SVC And STATCOM: The regulation and slope transfer function and dynamic performance, transient stability enhancement and power oscillation damping operating point control and summary of compensator control.

UNIT - V:

Static Series Compensators: Concept of series capacitive compensation, improvement of transient stability, power oscillation damping, and functional requirements of GTO thyristor controlled series capacitor (GSC), thyristor switched series capacitor (TSSC), and thyristor controlled series capacitor (TCSC), Control schemes for GSC TSSC and TCSC.

TEXT BOOKS:

1. Hingorani H G and Gyugyi. L "Understanding FACTS-Concepts and Technology of Flexible AC Transmission Systems" New York, IEEE Press, 2000.
2. Padiyar.K.R, " FACTS Controllers in Power Transmission and Distribution" New Age Int. Publishers, 2007

REFERENCES:

1. Zhang, Xiao-Ping, Rehtanz, Christian, Pal, Bikash "Flexible AC Transmission Systems: Modeling and Control", Springer, 2012
2. Yong-Hua Song, Allan Johns, "Flexible AC Transmission Systems", IET,1999

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/ PSC&A)

POWER SYSTEM OPERATION AND DEREGULATION (Professional Core - VI)

Prerequisite: Power System Operation and Control

Course objectives:

- To find OPF with security constraints.
- To generalize modeling of load frequency control of a power system
- To compute reactive power control of a power system.
- To apply the concept of deregulation and ATC.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Know the optimal scheduling of power plants
- Outline the modeling of turbine and generator
- Compute the steady state behavior of the power system for voltage and frequency fluctuations.
- Analyze ATC and the cost of transmission

UNIT- I:

Optimal Power Flow: Introduction- Solution to the optimal power flow-gradient method-Newton's method-Linear sensitivity analysis- Linear programming methods- Security constrained OPF-Interior point algorithm- Bus incremental costs

UNIT- II:

Power System Security: Introduction –Factors affecting power system security-Contingency analysis-Detection of network problems-Linear sensitivity analysis-AC power flow methods-contingency selection-concentric relaxation-Bounding area method

UNIT- III:

State Estimation in Power Systems: Introduction- Power system state estimation- Maximum likelihood Weighted Least squares estimation-Matrix formulation- State estimation of AC network- State estimation by orthogonal decomposition- detection and identification of Bad measurements- Estimation of quantities not being measured- Network observability and pseudo measurements

UNIT- IV:

Power System Deregulation: Introduction- motivation for restructuring of power systems- Electricity market entities model-benefits of deregulation- terminology-deregulation in Indian power sector- Operations in power markets-power pools-transmission networks and electricity markets.

UNIT-V:

Available Transfer Capability: Introduction methods: of determination of ATC - ATC calculation considering the effect of contingency analysis- Transmission open access and pricing-cost components of transmission system- transmission pricing methods-Incremental cost based transmission pricing.

TEXT BOOKS:

1. J. Wood & B.F. Woollenberg- John Wiley Power Generation, "Operation and Control"-2nd edition.
2. P. Venkatesh. B. V. Manikandan, S. Charles Raja- A. Srinivasan, "Electrical power systems:

Analysis, security, Deregulation”– PHI 2012

REFERENCES:

1. Bhattacharya, Kankar, Bollen, Math, Daalder, Jaap E. “Operation of Restructured Power System”, 2001, Springer.
2. Venkatesh P. , Manikandan B. V., Raja S. Charles , Srinivasan A. Electrical Power Systems: Analysis, Security And Deregulation, Phi Learning Pvt Ltd

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PSC&A)

**GAS INSULATED SYSTEMS (GIS)
(Professional Elective – III)**

Prerequisite: Switch Gear and Protection

Course objectives:

- To know the GIS concepts and principles
- To choose Air Insulated Substation and GIS
- To demonstrate the design and constructional aspects of GIS
- To analyze transient phenomenon, problems and diagnostic methods in GIS

Course Outcomes: Upon the completion of the subject, the student will be able to

- Know the advantages of GIS systems over air insulated systems
- Observe constructional design features of GIS design
- Discriminate the Problems and design diagnostic methods of GIS

UNIT – I:

Introduction to GIS and Properties Of SF_6

Characteristics of GIS- Introduction to SF_6 - Physical properties-Chemical properties - Electrical properties-Specification of SF_6 gas for GIS application - Handling of SF_6 gas before use - Safe handling of SF_6 gas in electrical equipment - Equipment for handling the SF_6 Gas - SF_6 and environment.

UNIT – II:

Layout of GIS Stations: Advancement of GIS station - Comparison with Air Insulated Substation - Economics of GIS - User Requirements for GIS - Main Features for GIS - Planning and Installation components of a GIS station.

UNIT – III:

Design and Construction of GIS Station: Introduction - Rating of GIS components - Design Features - Estimation of different types of Electrical Stresses -Design Aspects of GIS components - Insulation Design for Components - Insulation Design for GIS - Thermal Considerations in the Design of GIS - Effect of very Fast Transient Over-voltages (VFTO) on the GIS design - Insulation Coordination systems - Gas handling and Monitoring System Design.

UNIT - IV:

Fast Transient Phenomena in GIS: Introduction- Disconnecter Switching in Relation to Very fast Transients-Origin of VFTO-Propagation and Mechanism of VFTO-VFTO Characteristics- Effects of VFTO-Testing of GIS for VFTO.

UNIT – V:

Special Problems in GIS and GIS Diagnostics: Introduction - particles their effects and their control- Insulating Spacers and their Reliability - SF_6 Gas Decomposition - Characteristics of imperfections in insulation - Insulation Diagnostic methods - PD Measurement and UHF Method.

TEXT BOOKS:

1. M. S. Naidu, "Gas Insulated Substations"- IK International Publishing House.
2. Hermann J. Koch, "Gas Insulated Substations", June 2014, Wiley-IEEE Press

REFERENCES:

1. Olivier Gallot - Lavellee, "Dielectric materials and Electrostatics" , Wiley-IEEE Press
2. Jaun Martinez, "Dielectric Materials for Electrical Engineering", Wiley-IEEE Press

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PSC&A)

PROGRAMMABLE LOGIC CONTROLLERS AND APPLICATIONS
(Professional Elective – III)

Course Objectives

- It is to provide and ensure a comprehensive understanding of using advanced controllers in measurement and control instrumentation.
- To illustrate about data acquisition - process of collecting information from field instruments.
- To analyze Programmable Logic Controller (PLC), IO Modules and internal features.
- To Comprehend Programming in Ladder Logic, addressing of IO.
- To apply PID and its Tuning.

Course Outcomes

- Describe the main functional units in a PLC and be able to explain how they interact.
- They should know different bus types used in automation industries.
- Development of ladder logic programming for simple process.
- At the end of each chapter, review question, problems given to reinforce their understanding of the concepts.

UNIT - I:

PLC Basics PLC system, I/O modules and interfacing CPU processor programming equipment programming formats, construction of PLC ladder diagrams, devices connected to I/O modules.

UNIT - II:

PLC Programming input instructions, outputs, operational procedures, programming examples using contacts and coils. Drill-press operation.

Digital logic gates programming in the Boolean algebra system, conversion examples Ladder diagrams for process control Ladder diagrams and sequence listings, ladder diagram construction and flow chart for spray process system.

UNIT - III:

PLC Registers: Characteristics of Registers module addressing holding registers input registers, output registers. PLC Functions Timer functions and industrial applications counters counter function industrial applications, Architecture functions, Number comparison functions, number conversion functions.

UNIT - IV:

Data handling functions: SKIP, Master control Relay Jump Move FIFO, FAL, ONS, CLR and Sweep functions and their applications. Bit Pattern and changing a bit shift register, sequence functions and applications, controlling of two axes and three axis Robots with PLC, Matrix functions.

UNIT - V:

Analog PLC operation: Analog modules and systems Analog signal processing multi bit data processing , analog output application examples, PID principles position indicator with PID control, PID modules, PID tuning, PID functions

TEXT BOOKS:

1. Programmable Logic Controllers – Principle and Applications by John W. Webb & Ronald A. Reiss, Fifth Edition, PHI

2. Digital Design by Morris Mano, PHI, 3rd Edition 2006.

REFERENCE BOOKS:

1. Programmable logic Controllers, Frank D. Petruzella, 4th Edition, McGraw Hill Publishers.
2. Programmable Logic Controllers – Programming Method and Applications by JR. Hackworth & F.D Hackworth Jr. – Pearson, 2004.
3. Programmable logic controllers and their Engineering Applications, 2nd Edition, Alan J. Crispin.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/ PSC&A)

**ENERGY AUDITING CONSERVATION AND MANAGEMENT
(Professional Elective – III)**

Course Objectives:

- To know the necessity of conservation of energy
- To generalize the methods of energy management
- To illustrate the factors to increase the efficiency of electrical equipment
- To detect the benefits of carrying out energy audits.

Course Outcomes: Upon the completion of this course, the student will be able to

- Tell energy audit of industries
- Predict management of energy systems
- Sequence the methods of improving efficiency of electric motor
- Analyze the power factor and to design a good illumination system
- Determine pay back periods for energy saving equipment

UNIT- I:

Basic Principles of Energy Audit: Energy audit- definitions, concept , types of audit, energy index, cost index ,pie charts, Sankey diagrams, load profiles, Energy conservation schemes- Energy audit of industries- energy saving potential, energy audit of process industry, thermal power station, building energy audit.

UNIT- II:

Energy Management: Principles of energy management, organizing energy management program, initiating, planning, controlling, promoting, monitoring, reporting- Energy manger, Qualities, and functions, language, Questionnaire – check list for top management.

UNIT- III:

Energy Efficient Motors: Energy efficient motors, factors affecting efficiency, loss distribution, constructional details , characteristics - variable speed , variable duty cycle systems, RMS hp- voltage variation-voltage unbalance- over motoring- motor energy audit

UNIT- IV:

Power Factor Improvement, Lighting and Energy Instruments: Power factor – methods of improvement, location of capacitors, pf with non linear loads, effect of harmonics on power factor, power factor motor controllers - Good lighting system design and practice, lighting control, lighting energy audit - Energy Instruments- wattmeter, data loggers, thermocouples, pyrometers, lux meters, tongue testers ,application of PLC's.

UNIT- V:

Economic Aspects and Analysis: Economics Analysis-Depreciation Methods, time value of money, rate of return , present worth method , replacement analysis, life cycle costing analysis- Energy efficient motors- calculation of simple payback method, net present worth method- Power factor correction, lighting - Applications of life cycle costing analysis, return on investment .

TEXT BOOKS:

1. Energy management by W.R. Murphy AND G. McKay Butter worth, Heinemann publications.
2. Energy management by Paul o' Callaghan, Mc-graw Hill Book company-1st edition, 1998

REFERENCES:

1. Energy efficient electric motors by John .C. Andreas, Marcel Dekker Inc Ltd-2nd edition, 1995
2. Energy management hand book by W. C. Turner, John wiley and sons
3. Energy management and good lighting practice : fuel efficiency- booklet 12- EEO

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/ PSC&A)

**REACTIVE POWER COMPENSATION AND MANAGEMENT
(Professional Elective – IV)**

Prerequisite: Power Systems - II

Course Objectives:

- To identify the necessity of reactive power compensation
- To describe load compensation
- To select various types of reactive power compensation in transmission systems
- To contrast reactive power coordination system
- To characterize distribution side and utility side reactive power management.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Distinguish the importance of load compensation in symmetrical as well as un symmetrical loads
- Observe various compensation methods in transmission lines
- Construct model for reactive power coordination
- Distinguish demand side reactive power management & user side reactive power management

UNIT - I:

Load Compensation: Objectives and specifications – reactive power characteristics – inductive and capacitive approximate biasing – Load compensator as a voltage regulator – phase balancing and power factor correction of unsymmetrical loads- examples.

UNIT - II:

Steady – State Reactive Power Compensation in Transmission System: Uncompensated line – types of compensation – Passive shunt and series and dynamic shunt compensation –examples

Transient state reactive power compensation in transmission systems: Characteristic time periods – passive shunt compensation – static compensations- series capacitor compensation – compensation using synchronous condensers – examples

UNIT - III:

Reactive Power Coordination: Objective – Mathematical modeling – Operation planning – transmission benefits – Basic concepts of quality of power supply – disturbances- steady –state variations – effects of under voltages – frequency –Harmonics, radio frequency and electromagnetic interferences

UNIT - IV:

Demand Side Management: Load patterns – basic methods load shaping – power tariffs- KVAR based tariffs penalties for voltage flickers and Harmonic voltage levels

Distribution side Reactive power Management: System losses –loss reduction methods – examples – Reactive power planning – objectives – Economics Planning capacitor placement – retrofitting of capacitor banks

UNIT - V:

User Side Reactive Power Management: KVAR requirements for domestic appliances – Purpose of using capacitors – selection of capacitors – deciding factors – types of available capacitor, characteristics and Limitations

Reactive power management in electric traction systems and arc furnaces: Typical layout of traction systems – reactive power control requirements – distribution transformers- Electric arc furnaces – basic operations- furnaces transformer –filter requirements – remedial measures –power factor of an arc furnace

TEXT BOOKS:

1. Reactive power control in Electric power systems by T. J. E. Miller, John Wiley and sons, 1982.
2. Reactive power Management by D. M. Tagore, Tata McGraw Hill, 2004.

REFERENCES:

1. Wolfgang Hofmann, Jurgen Schlabach, Wolfgang Just “Reactive Power Compensation: A Practical Guide, April, 2012, Wiley publication.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS /PSC&A)

**POWER SYSTEM RELIABILITY
(Professional Elective – IV)**

Prerequisite: Reliability Engineering

Course Objectives:

- To identify the generation system model and recursive relation for capacitive model building
- To calculate the equivalent transitional rates, cumulative probability and cumulative frequency
- To classify the risk, system and load point reliability indices
- To evaluate the basic reliability indices

Course Outcomes: Upon the completion of the subject, the student will be able to

- Find loss of load and energy indices for generation systems model
- Describe merging generation and load models
- Apply various indices for distribution systems

UNIT- I:

Generating System Reliability Analysis – I: Generation system model – capacity outage probability tables – Recursive relation for capacitive model building – sequential addition method – unit removal – Evaluation of loss of load and energy indices – Examples.

UNIT- II:

Generating System Reliability Analysis – II: Frequency and Duration methods – Evaluation of equivalent transitional rates of identical and non-identical units – Evaluation of cumulative probability and cumulative frequency of non-identical generating units – 2- level daily load representation - merging generation and load models – Examples.

UNIT- III:

Operating Reserve Evaluation: Basic concepts - risk indices – PJM methods – security function approach – rapid start and hot reserve units – Modelling using STPM approach.

Bulk Power System Reliability Evaluation: Basic configuration – conditional probability approach – system and load point reliability indices – weather effects on transmission lines – Weighted average rate and Markov model – Common mode failures.

UNIT- IV:

Inter Connected System Reliability Analysis: Probability array method – Two inter connected systems with independent loads – effects of limited and unlimited tie capacity - imperfect tie – Two connected Systems with correlated loads – Expression for cumulative probability and cumulative frequency.

Distribution System Reliability Analysis – I (Radial configuration): Basic Techniques – Radial networks –Evaluation of Basic reliability indices, performance indices – load point and system reliability indices – customer oriented, loss and energy oriented indices – Examples.

UNIT - V:

Distribution System Reliability Analysis - II (Parallel Configuration): Basic techniques – inclusion of bus bar failures, scheduled maintenance – temporary and transient failures – weather effects – common mode failures –Evaluation of various indices – Examples

Substations and Switching Stations: Effects of short-circuits - breaker operation – Open and Short-circuit failures – Active and Passive failures – switching after faults – circuit breaker model – preventive maintenance – exponential maintenance times.

TEXT BOOKS:

1. Reliability Evaluation of Power systems by R. Billinton, R. N. Allan, BS Publications, 2007.
2. Reliability Modeling in Electric Power Systems by J. Endrenyi, John Wiley and Sons, 1978

REFERENCES:

1. Reliability Engineering: Theory and Practice by Alessandro Birolini, Springer Publications.
2. An Introduction to Reliability and Maintainability Engineering by Charles Ebeling, TMH Publications.
3. Reliability Engineering by E. Balaguruswamy, TMH Publications.
4. Reliability Engineering by Elsayed A. Elsayed, Prentice Hall Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/ PSC&A)

VOLTAGE STABILITY (Professional Elective – IV)

Prerequisite: Computers Methods in Power Systems

Course Objectives:

- To choose SEC Planning and Operational Standards of Security
- To estimate Reactive Power Control in Generation/Transmission Interconnected Networks
- To apply stability/Instability in Generation/Transmission Interconnected Networks
- To analyze design and Operational Solutions
- To characterize voltage Control in Distribution Networks

Course Outcomes: Upon the completion of the subject, the student will be able to

- Understand issues related to power system stability and control.
- Demonstrate various load models in voltage stability analysis.
- Detect reactive power compensation techniques & their practical importance

UNIT – I:

Introduction to Voltage Stability: Definitions: Voltage Stability, Voltage Collapse, Voltage Security; Physical relation indicating dependency of voltage on reactive power flow; Factors affecting Voltage collapse and instability; Previous cases of voltage collapse incidences.

UNIT – II:

Graphical Analysis of Voltage Stability: Comparison of Voltage and angular stability of the system; Graphical Methods describing voltage collapse phenomenon: P-V and Q-V curves; detailed description of voltage collapse phenomenon with the help of Q-V curves.

UNIT – III:

Analysis of Voltage Stability: Analysis of voltage stability on SMLB system: Analytical treatment and analysis.

Voltage Stability Indices: Voltage collapse proximity indicator; Determinant of Jacobin as proximity indicators; Voltage stability margin.

UNIT – IV:

Power System Loads: Loads that influences voltage stability: Discharge lights, Induction Motor, Air-conditioning, heat pumps, electronic power supplies, OH lines and cables.

Reactive Power Compensation: Generation and Absorption of reactive power; Series and Shunt compensation; Synchronous condensers, SVC s; OLTC s; Booster Transformers.

UNIT-V:

Voltage Stability Margin: Stability Margin: Compensated and un-compensated systems.

Voltage Security: Definition; Voltage security; Methods to improve voltage stability and its practical aspects.

TEXT BOOKS:

1. "Performance, operation and control of EHV power transmission system"-A. CHAKRABARTHY, D. P. KOTARI and A. K. MUKOPADYAY, A. H. Wheeler Publishing, I Edition, 1995.
2. "Power System Dynamics: Stability and Control" – K. R. PADIYAR, II Edition, B. S. Publications.

REFERENCES:

1. "Power System Voltage Stability"- C. W. TAYLOR, McGraw Hill, 1994.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/ PSC&A)

POWER AND ENERGY SYSTEMS LAB – II

Prerequisites: Power System Analysis, Power System Protection

Course Objectives:

- To understand the Performance of Transformers and Synchronous Machines
- To select the Transmission Lines, UG Cables, String Insulators, CTs and PTs.
- To analyze the characteristics of OC, UV/OV, negative sequence relays.

Course Outcomes: Upon the completion of the lab, the student will be able to

- Test and evaluate the performance of Power Transformers and Synchronous Machines.
 - Test and evaluate the performance of Transmission lines, UG Cables, Insulators and other Auxiliary Power Systems Equipment
 - Test, Evaluate/Choose the various types of Relays (Electromagnetic, Static and Microprocessor based relays)
1. Determination of Equivalent circuit of a 3-Winding Transformer.
 2. Determination of Sequence Impedances of a Cylindrical Rotor Synchronous Machine.
 3. Fault Analysis:
 - i. Single Line to Ground fault (L-G).
 - ii. Line to Line fault (L-L).
 - iii. Double Line to Ground fault (L-L-G).
 - iv. Triple Line to Ground fault (L-L-L-G).
 4. Determination of Sub-transient reactance's of a Salient Pole Synchronous Machine.
 5. Determination of Sequence Impedances of Three Phase Transformer
 6. Characteristics of Over Current Relays
 - i. IDMT Electromagnetic Relay (7051 A).
 - ii. Microprocessor based Relay (7051 B)
 7. Characteristics of Percentage biased Differential Relay.
 - i. Electromagnetic Relay (7054 A).
 - ii. Static Relay (7054 B).
 8. Characteristics of Over Voltage Relay.
 - I. Electromagnetic Relay (7053 A).
 - II. Microprocessor based Relay (7053 B).
 9. Characteristics of Under Voltage (UV) and Negative sequence Relays
 - i. Uv Electromagnetic Relay (7052 A).
 - ii. Uv Microprocessor Based Relay (7052 B).
 - iii. Static Negative Sequence Relay (7055 B).
 10. Performance and Testing of Generator Protection System.
 11. Performance and Testing of Transformer Protection System.
 12. Performance and Testing of Feeder Protection System.
 13. Performance and Testing of Transmission Line Model.
 14. Differential protection on Single Phase Transformer.

Note: From the above list minimum 10 experiments are to be conducted

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH (POWER ELECTRONICS/ POWER AND INDUSTRIAL DRIVES/
POWER ELECTRONICS AND ELECTRIC DRIVES)**

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Machine Modeling and Analysis	25	75	4	0	0	4
PC-2	Modern Control Theory	25	75	4	0	0	4
PC-3	Power Electronic Devices and Converters	25	75	4	0	0	4
PE-1	1. Special Machines 2. High Frequency Magnetic Components 3. Programmable Logic Controllers and Applications	25	75	3	0	0	3
PE-2	1. Electric Traction systems 2. Advanced Digital Signal Processing 3. Digital Control Systems	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Power Converters Simulation Lab	25	75	0	0	3	2
Seminar I	Seminar - I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Power Electronic Applications to Renewable Energy	25	75	4	0	0	4
PC-5	Embedded Systems for Power Electronic Applications	25	75	4	0	0	4
PC-6	Power Electronic Control of Drives	25	75	4	0	0	4
PE-3	1. HVDC & FACTS 2. Switched Mode Power Supplies (SMPS) 3. AI Techniques in Electrical Engineering	25	75	3	0	0	3
PE4	1. Dynamics of Electrical Machines 2. Hybrid Electric Vehicles 3. Smart Grid Technologies	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Power Converters and Drives Lab	25	75	0	0	3	2
Seminar II	Seminar -II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

**POWER ELECTRONIC APPLICATIONS TO RENEWABLE ENERGY
(Professional core - IV)**

Course Objectives:

- To understand the various Non-Conventional sources of energy
- To explain the DC to DC converters for Solar PV source of energy
- To explain the inverters and its control techniques for a grid connected system
- To understand the characteristics of a solar PV and wind power sources
- To explain the types of distributed generators and batteries in DG and micro grid system

Course Outcomes: Upon the completion of the course the student will be able to

- To acquire knowledge on Non-Conventional energy sources
- To analyze various technologies and for renewable energy systems
- To develop stand alone DG sets and micro grid systems from renewable energy sources

UNIT - I

Introduction to renewable sources: world energy scenario, Wind, solar, hydro, geothermal, availability and power extraction.

Introduction to solar energy: Photovoltaic effect, basics of power generation, P-V & I-V characteristics, effect of insolation, temperature, diurnal variation, shading, Modules, connections, ratings, Power extraction (MPP) tracking and MPPT schemes; standalone systems, grid interface, storage, AC-DC loads.

UNIT - II

DC-DC converters for solar PV: buck/boost/buck-boost /flyback /forward/cuk, bidirectional converters, Interleaved and multi-input converters.

UNIT - III

Grid connected Inverters: 1ph, 3ph inverters with & w/o x'mer, Heric, H6, Multilevel Neutral point clamp, Modular multilevel, CSI; Control schemes: unipolar, bipolar, PLL and synchronization, power balancing / bypass, Parallel power processing; Grid connection issues: leakage current, Islanding, harmonics, active/reactive power feeding, unbalance.

UNIT - IV

Introduction to wind energy: P-V, I-V characteristic, wind power system: turbine-generator-inverter, mechanical control, ratings; Power extraction (MPP) and MPPT schemes. Generators for wind: DC generator with DC to AC converters; Induction generator with & w/o converter.

UNIT - V

Synchronous generator with back to back controlled/ uncontrolled converter; Doubly fed induction generator with rotor side converter topologies; permanent magnet based generators. Battery: Types, charging discharging. Introduction to AC and DC microgrids.

TEXT BOOKS:

1. Sudipta Chakraborty, Marcelo G. Simes, and William E. Kramer. Power Electronics for Renewable and Distributed Energy Systems: A Sourcebook of Topologies, Control and Integration. Springer Science & Business, 2013.

2. Nicola Femia, Giovanni Petrone, Giovanni Spagnuolo, Massimo Vitelli, Power Electronics and control for maximum Energy Harvesting in Photovoltaic Systems, CRC Press, 2013.
3. Chetan Singh Solanki, Solar Photovoltaics: fundamentals, Technologies and Applications, Prentice Hall of India, 2011.

REFERENCE BOOKS:

1. N. Mohan, T.M. Undeland & W. P. Robbins, Power Electronics: Converter, Applications & Design, John Wiley & Sons, 1989
2. Muhammad H. Rashid, Power Electronics: Circuits, Devices, and Applications, Pearson Education India, 2004
3. E. Guba, P. Sanchis, A. Ursa, J. Lpez, and L. Marroyo, Ground currents in single-phase transformerless photovoltaic systems, Progress in Photovoltaics: Research and Applications, vol. 15, no. 7, 2007.
4. Remus Teodorescu, Marco Liserre, Pedro Rodriguez, Grid Converters for Photovoltaic and Wind Power Systems, John Wiley and Sons, Ltd., 2011.
5. Ali Keyhani, Design of Smart Power Grid Renewable Energy Systems, Wiley-IEEE Press, 2011.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

EMBEDDED SYSTEMS FOR POWER ELECTRONIC APPLICATIONS (Professional core - V)

Course Objectives:

- To learn the fundamentals of Embedded System Processor
- To understand the AVR family processors and its programming in assembly level
- To explain the interfacing of keyboard, conversion of analog to digital and vice versa
- To develop knowledge on the applications of embedded system programming in to drives and UPS systems

Course Outcomes: Upon the completion of the course the student will be able to

- To describe the differences between the general computing system and the embedded system, also recognize the classification of embedded systems
- To become aware of the architecture of the AVR processor and its programming aspects (Assembly Level)
- To acquire knowledge on key board interfacing, conversion from ADC and DAC
- To equipped to design and develop control of drives using embedded system programming

UNIT - I:

Introduction to Embedded System: An embedded system, processor, hardware unit, software embedded into a system, Example of an embedded system, Real time and embedded OS. Structural unit in a processor selection for embedded systems.

UNIT - II

AVR system - AVR family processors, Architecture, Addressing modes, Instruction overview, Branch, Call, and Time Delay Loop, AVR I/O Port Programming.

UNIT - III

Assembly level programming, Higher level language programming, AVR Programming in C, Timer Programming, Interrupt Programming.

UNIT - IV

AVR LCD and Keyboard Interfacing, ADC, DAC, and different Sensor Interfacing, Relay, Opt isolator interface.

UNIT - V

Stepper Motor Interfacing, Servo motor interfacing, PWM Programming, RTC, PC interface, data acquisition system.

Case studies

DC motor control, Induction Motor control (VSI and CSI fed) , UPS Applications , Special Machine control(PMBLDC).

TEXT BOOKS:

1. M A Mazidi, S Naimi "AVR Microcontroller and Embedded Systems: Using Assembly and C"
2. Rajkamal "Embedded System Architecture: Programming & Design", TMH Edition, 2007.
3. J. W. Valvano, "Embedded Microcomputer System: Real time interfacing", Cengage-Engineering, 1st Edition, 2000.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

**POWER ELECTRONIC CONTROL OF DRIVES
(Professional core - VI)**

Prerequisites: Power Electronics, AC and DC Machines, Control Systems

Course Objectives

- To understand the drive system and converter, chopper fed DC separately excited motor
- To understand principle operation of scalar control of ac motor and corresponding speed-torque-slip characteristics
- To comprehend the vector control for ac motor drive (IM and SM)
- To explain the static resistance control and Slip power recovery drive
- To explain synchronous motor drive characteristics and its control strategies
- To comprehend the brushless dc motor principle of operation.

Course Outcomes: Upon the completion of the course the student will be able to

- Analyze drive characteristics and converter as well chopper fed dc drives
- Develop induction motor for variable speed operations using scalar and vector control techniques.
- Identify the difference between the rotor resistance control and static rotor resistance control method and significance of slip power recovery drives
- Develop Controllers for synchronous motor and variable reluctance motor can be developed

UNIT- I:

Introduction to drive systems: Basic power electronic drive system, components - Different types of loads, shaft-load coupling systems - Stability of power electronic drive.

DC Motor Speed Control: Three Phase full converter fed separately excited motor for one, two and four quadrant applications for speed control, closed loop operation; dc chopper controlled separately excited motor for one, two and four quadrant application for speed control of closed loop operation

UNIT- II:

Stator Side Control of Induction Drives: Scalar control – Voltage fed inverter control – Open loop volts/Hz control – speed control slip regulation – speed control with torque and flux control – current controlled voltage fed inverter drive – current – fed inverter control – Independent current and frequency control – Speed and flux control in Current –Fed inverter drive – Volts/Hz control of Current –fed inverter drive – Efficiency optimization control by flux program.

UNIT– III:

Rotor Side Control of Induction Drives: Slip power recovery drives – Static Kramer Drive – Phasor diagram – Torque expression – speed control of Kramer Drive – Static Scheribus Drive – modes of operation.

Vector control of Induction Motor Drives: Principles of Vector control – Vector control methods – Direct methods of vector control – Indirect methods of vector control

UNIT – IV:

Control of Synchronous Motor Drives: Synchronous motor and its characteristics – Control strategies – Constant torque angle control – Unity power factor control – Constant mutual flux linkage control.

Controllers: Flux weakening operation – Maximum speed – Direct flux weakening algorithm – Constant Torque mode controller – Flux Weakening controller – indirect flux weakening – Maximum permissible torque – speed control scheme – Implementation strategy speed controller design.

UNIT – V:

Variable Reluctance Motor Drive: Variable Reluctance motor drive – Torque production in the variable reluctance motor Drive characteristics and control principles – Current control variable reluctance motor service drive.

Brushless DC Motor Drives: Three phase full wave Brushless dc motor – Sinusoidal type of Brushless dc motor- current controlled Brushless dc motor Servo drive.

REFERENCES:

1. Fundamentals of Electrical Drives – G. K. Dubey – Narora publications – 1995.
2. Electric Motor Drives Pearson Modeling, Analysis and control – R. Krishnan – Publications – 1st edition – 2002.
3. Modern Power Electronics and AC Drives B K Bose – Pearson Publications 1st edition
4. Power Electronics and Control of AC Motors – MD Murthy and FG Turn Bull Pergman Press 1st edition
5. Power Electronics and AC Drives – BK Bose – Prentice Hall Eagle wood diffs New Jersey - 1st edition
6. Power Electronic circuits Deices and Applications – M H Rashid – PHI – 1995.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

**HVDC & FACTS
(Professional Elective – III)**

Prerequisites: Power Electronics, Power Systems

Course Objectives:

- To understand the fundamentals of FACTS Controllers,
- To know the importance of controllable parameters and types of FACTS controllers & their benefits
- To study HVDC Transmission system
- To understand the control aspects of HVDC System

Course Outcomes: Upon the completion of the course the student will be able to

- Choose proper FACTS controller for the specific application based on system requirements
- Analyze the control circuits of Shunt Controllers, Series controllers & Combined controllers for various functions viz. Transient stability Enhancement, voltage instability prevention and power oscillation damping
- Compare EHV AC and HVDC system and to describe various types of DC links
- Describe various methods for the control of HVDC systems and to perform power flow analysis in AC/DC systems

UNIT - I

Facts concepts: Reactive power control in electrical power transmission, principles of conventional reactive power compensators. Introduction to FACTS, flow of power in AC parallel paths, meshed systems, basic types of FACTS controllers, definitions of FACTS controllers, brief description of FACTS controllers.

UNIT - II

Static shunt and series compensators: Shunt compensation - objectives of shunt compensation, methods of controllable VAR generation, static VAR compensators - SVC, STATCOM, SVC and STATCOM comparison. Series compensation - objectives of series compensation, thyristor switched series capacitors (TCSC), static series synchronous compensator (SSSC), power angle characteristics, and basic operating control schemes.

UNIT - III

Combined compensators: Unified power flow controller (UPFC) - Introduction, operating principle, independent real and reactive power flow controller and control structure. Interline power flow controller (IPFC), Introduction to Active power filtering, Concepts relating to Reactive power compensation and harmonic current compensation using Active power filters.

UNIT - IV

HVDC transmission: HVDC Transmission system: Introduction, comparison of AC and DC systems, applications of DC transmission, types of DC links, Layout of HVDC Converter station and various equipments. HVDC Converters, analysis of bridge converters with and without overlap, inverter operation, equivalent circuit representation of rectifier and inverter configurations

UNIT - V

Control of HVDC system: Principles of control, desired features of control, converter control characteristics, power reversal, Ignition angle control, current and extinction angle control. Harmonics-introduction, generation, ac filters and dc filters.

Introduction to multiterminal DC systems and applications, comparison of series and parallel MTDC systems,

Voltage Source Converter based HVDC systems

TEXT BOOKS:

1. Hingorani ,L.Gyugyi, 'Concepts and Technology of Flexible AC Transmission System', IEEE Press New York, 2000 ISBN –078033 4588.
2. Padiyar, K.R., 'HVDC transmission systems', Wiley Eastern Ltd., 2010.

REFERENCES:

1. Song, Y.H. and Allan T. Johns, 'Flexible AC Transmission Systems (FACTS)', Institution of Electrical Engineers Press, London, 1999.
2. Mohan Mathur R. and Rajiv K.Varma , 'Thyristor - based FACTS controllers for Electrical
3. Transmission systems', IEEE press, Wiley Inter science , 2002.
4. Padiyar K.R., 'FACTS controllers for Transmission and Distribution systems' New Age International Publishers, 1st Edition, 2007.
5. Enrique Acha, Claudio R.Fuerte-Esqivel, Hugo Ambriz-Perez, Cesar Angeles-Camacho 'FACTS –Modeling and simulation in Power Networks' John Wiley & Sons, 2002.
6. Jos Arrillaga, 'High voltage Direct Current Transmission' IET Power and Energy Series 29

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

**SWITCHED MODE POWER SUPPLIES (SMPS)
(Professional Elective – III)**

Prerequisites: Power Electronics, Electronic devices and circuits

Course objectives:

- To understand various modes of operation of DC-DC Converter
- To analyze control aspects of converter
- To design various Switched Mode Power Supply components
- To get awareness on EMI, Protection of converter system

Course Outcomes: Upon the completion of the course the student will be able to

- Analyze various modes of operation of Dc-Dc converter
- Design different controllers for converter
- Design various components of dc-dc converter
- Analyze dc-dc converter in view of EMI and thermal considerations

UNIT – I

Basic Converter Circuits: Buck Regulator, Buck- Boost Regulator, Boost Regulator, Cuk Converters and Resonant Converters. Choice of switching frequency.

UNIT – II

Isolated SMPS: Fly back Converter, Forward Converter, Half-Bridge and Full Bridge Converters, Push-Pull Converter and SMPS with multiple outputs. Choice of switching frequency.

UNIT – III

Control Aspects: PWM Controllers, Isolation in feedback loop, Power Supplies with multiple output. Stability analysis using Bode Diagrams.

UNIT – IV

Design Considerations: Selection of output filter capacitor, Selection of energy storage inductor, Design of High Frequency Inductor and High frequency Transformer, Selection of switches. Snubber circuit design, Design of driver circuits.

UNIT – V

Electro Magnetic Interference (EMI): EMI Filter Components, Conducted EMI suppression, Radiated EMI suppression, Measurement.

Protection: Over current protection, over voltage protection, Inrush current protection.

Thermal Model: Thermal Resistance, Cooling Considerations, Selection of Heat sinks, Simple Heat sink calculations.

TEXT BOOKS:

1. Switched Mode Power Supplies, Design and Construction, H. W. Whittington, B. W. Flynn and D. E. MacPherson, Universities Press, 2009 Edition.
2. Mohan N. Undeland . T & Robbins W., Power Electronics Converters, Application and Design. John Wiley, 3rd edition, 2002
3. Umanand L., Bhat S.R., Design of magnetic components for switched Mode Power Converters. , Wiley Eastern Ltd.,1992

4. Robert. W. Erickson, D. Maksimovic .Fundamentals of Power Electronics., Springer International Edition, 2005
5. Course Material on Switched Mode Power Conversion, V. Ramanarayanan.

REFERENCE BOOKS:

1. Krein P.T .Elements of Power Electronics., Oxford University Press
2. M. H. Rashid, Power Electronics. Prentice-Hall of India

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

**AI Techniques in Electrical Engineering
(Professional Elective – III)**

Course Objectives:

- To locate soft commanding methodologies, such as artificial neural networks, Fuzzy logic and genetic Algorithms.
- To observe the concepts of feed forward neural networks and about feedback neural networks.
- To practice the concept of fuzziness involved in various systems and comprehensive knowledge of fuzzy logic control and to design the fuzzy control
- To analyze genetic algorithm, genetic operations and genetic mutations.

Course Outcomes: Upon the completion of this course, the student will be able to

- Understand feed forward neural networks, feedback neural networks and learning techniques.
- Analyze fuzziness involved in various systems and fuzzy set theory.
- Develop fuzzy logic control for applications in electrical engineering
- Develop genetic algorithm for applications in electrical engineering.

UNIT – I:

Artificial Neural Networks: Introduction-Models of Neural Network - Architectures – Knowledge representation – Artificial Intelligence and Neural networks – Learning process – Error correction learning – Hebbian learning – Competitive learning – Boltzman learning – Supervised learning – Unsupervised learning – Reinforcement learning - learning tasks.

UNIT- II:

ANN Paradigms : Multi – layer perceptron using Back propagation Algorithm-Self – organizing Map – Radial Basis Function Network – Functional link, network – Hopfield Network.

UNIT – III:

Fuzzy Logic: Introduction – Fuzzy versus crisp – Fuzzy sets - Membership function – Basic Fuzzy set operations – Properties of Fuzzy sets – Fuzzy cartesian Product – Operations on Fuzzy relations – Fuzzy logic – Fuzzy Quantifiers - Fuzzy Inference - Fuzzy Rule based system - Defuzzification methods.

UNIT – IV:

Genetic Algorithms: Introduction-Encoding – Fitness Function-Reproduction operators - Genetic Modeling – Genetic operators - Crossover - Single-site crossover – Two-point crossover – Multi point crossover-Uniform crossover – Matrix crossover - Crossover Rate - Inversion & Deletion – Mutation operator –Mutation – Mutation Rate-Bit-wise operators - Generational cycle-convergence of Genetic Algorithm.

UNIT-V:

Applications of AI Techniques: Load forecasting – Load flow studies – Economic load dispatch – Load frequency control – Single area system and two area system – Small Signal Stability (Dynamic stability) Reactive power control – speed control of DC and AC Motors.

TEXT BOOK:

1. S. Rajasekaran and G. A. V. Pai, "Neural Networks, Fuzzy Logic & Genetic Algorithms"- PHI, New Delhi, 2003.

REFERENCES:

1. P. D. Wasserman, Van Nostrand Reinhold, "Neural Computing Theory & Practice" - New York, 1989.
2. Bart Kosko, "Neural Network & Fuzzy System" Prentice Hall, 1992.
3. G. J. Klir and T. A. Folger, "Fuzzy sets, Uncertainty and Information"-PHI, Pvt.Ltd,1994.
4. D. E. Goldberg," Genetic Algorithms"- Addison Wesley 1999

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

DYNAMICS OF ELECTRICAL MACHINES
(Professional Elective - IV)

Course Objective: This course deals with generalized modeling and analysis of different electrical machines used for industrial drive applications.

Course Outcomes: Upon the completion of this course, the student will be able to

- Understand electrical machines and its characteristics
- Analyse the behavior of electrical machines under steady state and transient state
- Model electrical machines under dynamic conditions

UNIT- I:

Basic Machine Theory: Electromechanical Analogy – Magnetic Saturation – Rotating field theory – Operation of Inductor motor – equivalent circuit – Steady state equations of DC machines – operations of synchronous motor – Power angle characteristics

UNIT- II:

Electrodynamical Equation & Their Solutions: Spring and Plunger system - Rotational motion – mutually coupled coils – Lagrange's equation – Application of Lagrange's equation solution of Electro dynamical equations.

UNIT- III:

Dynamics of DC Machines: Separately excited d. c. generators – steady state analysis – transient analysis – Separately excited d. c. motors – steady state analysis – transient analysis – interconnection of machines – Ward Leonard system of speed control.

UNIT- IV:

Induction Machine Dynamics: Induction machine dynamics during starting and braking – accelerating time – induction machine dynamic during normal operation – Equation for dynamical response of the induction motor.

UNIT- V:

Synchronous Machine Dynamics: Electromechanical equation – motor operation – generator operation – small oscillations – general equations for small oscillations – representation of the oscillation equations in state variable form.

REFERENCE BOOKS:

1. Sen Gupta D.P. and J. W. "Electrical Machine Dynamics" Macmillan Press Ltd 1980.
2. Bimbhra P.S. "Generalized Theory of Electrical Machines" Khanna Publishers 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

**HYBRID ELECTRIC VEHICLES
(Professional Elective - IV)**

Course Prerequisites: Physics, Basic Electrical Engineering.

Course Objectives:

- To study the concepts and drive train configurations of electric drive vehicles
- To provide different electric propulsion systems and energy storage devices
- To explain the technology, design methodologies and control strategy of hybrid electric vehicles
- To emphasize battery charger topologies for plug in hybrid electric vehicles

Course Outcomes: Upon the completion of this course, the student will be able to

- Understand the concepts and drivetrain configurations of electric drive vehicles
- Interpret different electric propulsion systems and energy storage devices
- Appreciate the technology, design methodologies and control strategy of hybrid electric vehicles
- Realize battery charger topologies for plug in hybrid electric vehicles

UNIT - I

Introduction to Electric Vehicles: Sustainable Transportation - EV System - EV Advantages - Vehicle Mechanics - Performance of EVs - Electric Vehicle drivetrain - EV Transmission Configurations and components-Tractive Effort in Normal Driving - Energy Consumption - EV Market - Types of Electric Vehicle in Use Today - Electric Vehicles for the Future.

UNIT - II

Electric Vehicle Modelling - Consideration of Rolling Resistance - Transmission Efficiency - Consideration of Vehicle Mass - Tractive Effort - Modelling Vehicle Acceleration - Modelling Electric Vehicle Range -Aerodynamic Considerations - Ideal Gearbox Steady State Model - EV Motor Sizing - General Issues in Design.

UNIT - III

Introduction to electric vehicle batteries - electric vehicle battery efficiency - electric vehicle battery capacity - electric vehicle battery charging - electric vehicle battery fast charging - electric vehicle battery discharging - electric vehicle battery performance – testing.

UNIT - IV

Hybrid Electric Vehicles - HEV Fundamentals -Architectures of HEVs- Interdisciplinary Nature of HEVs - State of the Art of HEVs - Advantages and Disadvantages - Challenges and Key Technology of HEVs - Concept of Hybridization of the Automobile-Plug-in Hybrid Electric Vehicles - Design and Control Principles of Plug-In Hybrid Electric Vehicles - Fuel Cell Hybrid Electric Drive Train Design - HEV Applications for Military Vehicles.

UNIT - V

Advanced topics - Battery Charger Topologies, Charging Power Levels, and Infrastructure for Plug-In Electric and Hybrid Vehicles - The Impact of Plug-in Hybrid Electric Vehicles on Distribution Networks – Sizing Ultra capacitors for Hybrid Electric Vehicles.

TEXT BOOKS:

1. Modern Electric, Hybrid Electric and Fuel Cell Vehicles – Fundamentals, Theory and Design – Mehrdad Ehsani, Uimin Gao and Ali Emadi - Second Edition - CRC Press, 2010.
2. Electric Vehicle Technology Explained - James Larminie, John Lowry - John Wiley & Sons Ltd, - 2003.
3. Electric Vehicle Battery Systems – Sandeep Dhameja – Newnes - New Delhi – 2002.
4. Hybrid electric Vehicles Principles and applications With practical perspectives -Chris Mi, Dearborn - M. Abul Masrur, David Wenzhong Gao - A John Wiley & Sons, Ltd., - 2011.
5. Electric & Hybrid Vehicles – Design Fundamentals - Iqbal Hussain, Second Edition, CRC Press, 2011.
6. Research Papers:
 - i) The Impact of Plug-in Hybrid Electric Vehicles on Distribution Networks: a Review and Outlook - Robert C. Green II, Lingfeng Wang and Mansoor Alam - 2010 IEEE.
 - ii) Sizing Ultracapacitors for Hybrid Electric Vehicles - H. Douglas P Pillay -2005 IEEE.
 - iii) Review of Battery Charger Topologies, Charging Power Levels, and Infrastructure for Plug-In Electric and Hybrid Vehicles - Murat Yilmaz, and Philip T. Krein, - IEEE transactions on power electronics, vol. 28, no. 5, May 2013.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

SMART GRID TECHNOLOGIES
(Professional Elective – IV)

Prerequisites: Electrical Distribution Systems, Power Systems

Course Objectives:

- To understand various aspects of smart grid
- To study various smart transmission and distribution technologies
- To appreciate distribution generation and smart consumption
- To know the regulations and market models for smart grid

Course Outcomes: Upon the completion of the subject, the student will be able to

- Understand technologies for smart grid
- Appreciate the smart transmission as well distribution systems
- Realize the distribution generation and smart consumption
- Know the regulations and market models for smart grid

UNIT - I:

Introduction to Smart Grids: Definition, justification for smart grids, smart grid conceptual model, smart grid architectures, Interoperability, communication technologies, role of smart grids standards, intelligrid initiative, national smart grid mission (NSGM) by Govt. of India

UNIT - II:

Smart Transmission Technologies: Substation automation, Supervisory control and data acquisition (SCADA), energy management system (EMS), phasor measurement units (PMU), Wide area measurement systems (WAMS)

UNIT - III:

Smart Distribution Technologies: Distribution automation, outage management systems, automated meter reading (AMR), automated metering infrastructure (AMI), fault location isolation and service restoration (FLISR), Outage Management Systems (OMS), Energy Storage, Renewable Integration

UNIT - IV:

Distributed Generation and Smart Consumption: Distributed energy resources (DERs), smart appliances, low voltage DC (LVDC) distribution in homes / buildings, home energy management system (HEMS), Net Metering, Building to Grid B2G, Vehicle to Grid V2G, Solar to Grid, Microgrid

UNIT - V:

Regulations and Market Models for Smart Grid: Demand Response, Tariff Design, Time of the day pricing (TOD), Time of use pricing (TOU), Consumer privacy and data protection, consumer engagement etc

Cost benefit analysis of smart grid projects

TEXT BOOKS:

1. Clark W Gellings, "The Smart Grid, Enabling Energy Efficiency and Demand Side Response"- CRC Press, 2009.
2. Jean Claude Sabonnadière, Nouredine Hadjsaïd, "Smart Grids", Wiley-ISTE, IEEE Press, May 2012

REFERENCES:

1. Janaka Ekanayake, Kithsiri Liyanage, Jianzhong. Wu, Akihiko Yokoyama, Nick Jenkins, "Smart Grid: Technology and Applications"- Wiley, 2012.
2. James Momoh, "Smart Grid: Fundamentals of Design and Analysis" - Wiley, IEEE Press, 2012.
3. India Smart Grid Knowledge Portal

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER CONVERTERS AND DRIVES LAB

Pre-requisites: All core subjects

Course Objectives:

- Show awareness of the impact of power electronic control circuits on utility supply
- To observe the difference of the conventional and power electronic control of drives.
- Have a better understanding of the close relationship between hardware and simulation models of actual systems.
- To familiarize the student with various power electronic converter topologies and their speed Control application (open loop and closed loop operation)

Course Outcomes: Student will be able to

1. Conduct experiments on drives for different modes of operation using different converter topologies.
2. Select the suitable controller for getting the desired speed performance of drive.
3. Validate the results

List of Experiments

1. Speed Measurement and closed loop control using PMDC motor.
2. Thyristorised drive for PMDC Motor with speed measurement and closed Loop control.
3. IGBT used single 4 quadrant chopper drive for PMDC motor with speed measurement and closed loop control.
4. Thyristorised drive for 1Hp DC motor with closed loop control.
5. 3-Phase input, thyristorised drive, 3 Hp DC motor with closed loop
6. 3-Phase input IGBT, 4 quadrant chopper drive for DC motor with closed Loop control equipment.
7. Cyclo-converter based AC Induction motor control equipment.
8. Speed control of 3 phase wound rotor Induction motor.
9. Single-phase fully controlled converter with inductive load.
10. Single phase half wave controlled converter with inductive load.
11. Isolated Gate Drive circuits for MOSFET / IGBT based circuits.
12. Characteristics of solar PV Systems.
13. Maximum Power Point Tracking Charge Controllers.
14. Inverter control for Solar PV based systems.

Note: Any ten experiments can be conducted.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. Tech. SOFTWARE ENGINEERING
EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH**

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Algorithms	25	75	4	0	0	4
PC-2	Software Development Methodologies	25	75	4	0	0	4
PC-3	Software Requirements and Estimation	25	75	4	0	0	4
PE-1	1. Cloud Computing 2. Database Internals 3. Cryptography and Network Security 4. Internet Technologies and Services	25	75	3	0	0	3
PE-2	1. Big Data Analytics 2. Web Mining 3. Machine Learning 4. Software Metrics and Quality Assurance	25	75	3	0	0	3
OE-1	*Open Elective – 1	25	75	3	0	0	3
Laboratory I	Software Development Methodologies Lab	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Software Architecture and Design Patterns	25	75	4	0	0	4
PC-5	Software Process and Project Management	25	75	4	0	0	4
PC-6	Software Quality Assurance and Testing	25	75	4	0	0	4
PE-3	1. Scripting Languages 2. Information Retrieval Systems 3. Semantic Web and Social Networks 4. E-Commerce	25	75	3	0	0	3
PE-4	1. Software Security Engineering 2. Cyber Security 3. Information Security and Audit 4. Systems Engineering	25	75	3	0	0	3
OE-2	*Open Elective – 2	25	75	3	0	0	3
Laboratory II	Software Testing Lab	25	75	0	0	3	2
Seminar II	Seminar -II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

SOFTWARE ARCHITECTURE AND DESIGN PATTERNS (PC – 4)

Course Objectives: After completing this course, the student should be able to:

- To understand the concept of patterns and the Catalog.
- To discuss the Presentation tier design patterns and their affect on: sessions, client access, validation and consistency.
- To understand the variety of implemented bad practices related to the Business and Integration tiers.
- To highlight the evolution of patterns.
- To learn how to add functionality to designs while minimizing complexity
- To learn what design patterns really are, and are not
- To know about specific design patterns.
- To learn how to use design patterns to keep code quality high without over design.

UNIT - I

Envisioning Architecture: The Architecture Business Cycle, What is Software Architecture, Architectural patterns, reference models, reference architectures, architectural structures and views.

Creating an Architecture: Quality Attributes, Achieving qualities, Architectural styles and patterns, designing the Architecture, Documenting software architectures, Reconstructing Software Architecture.

UNIT - II

Analyzing Architectures: Architecture Evaluation, Architecture design decision making, ATAM, CBAM.

Moving from one system to many: Software Product Lines, Building systems from off the shelf components, Software architecture in future.

UNIT - III

Patterns: Pattern Description, Organizing catalogs, role in solving design problems, Selection and usage.

Creational and Structural patterns: Abstract factory, builder, factory method, prototype, singleton, adapter, bridge, composite, façade, flyweight.

UNIT - IV

Behavioural patterns: Chain of responsibility, command, Interpreter, iterator, mediator, memento, observer, state, strategy. template method, visitor.

UNIT - V

Case Studies: A-7E – A case study in utilizing architectural structures, The World Wide Web - a case study in interoperability, Air Traffic Control – a case study in designing for high availability, Celsius Tech – a case study in product line development

TEXT BOOKS:

1. Software Architecture in Practice, second edition, Len Bass, Paul Clements & Rick Kazman, Pearson Education, 2003.
2. Design Patterns, Erich Gamma, Pearson Education.

REFERENCE BOOKS:

1. Beyond Software architecture, Luke Hohmann, Addison wesley, 2003.

2. Software architecture, David M. Dikel, David Kane and James R. Wilson, Prentice Hall PTR, 2001
3. Software Design, David Budgen, second edition, Pearson education, 2003
4. Head First Design patterns, Eric Freeman & Elisabeth Freeman, O'REILLY, 2007.
5. Design Patterns in Java, Steven John Metsker & William C. Wake, Pearson education, 2006
6. J2EE Patterns, Deepak Alur, John Crupi & Dan Malks, Pearson education, 2003.
7. Design Patterns in C#, Steven John metsker, Pearson education, 2004.
8. Pattern Oriented Software Architecture, F.Buschmann & others, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

SOFTWARE PROCESS AND PROJECT MANAGEMENT (PC – 5)

Course Objectives: At the end of the course, the student shall be able to:

- To describe and determine the purpose and importance of project management from the perspectives of planning, tracking and completion of project.
- To compare and differentiate organization structures and project structures.
- To implement a project to manage project schedule, expenses and resources with the application of suitable project management tools.

UNIT - I

Software Process Maturity: Software maturity Framework, Principles of Software Process Change, Software Process Assessment, The Initial Process, The Repeatable Process, The Defined Process, The Managed Process, The Optimizing Process.

Process Reference Models Capability Maturity Model (CMM), CMMi, PCMM, PSP, TSP.

UNIT - II

Software Project Management Renaissance Conventional Software Management, Evolution of Software Economics, Improving Software Economics, The old way and the new way.

Life-Cycle Phases and Process artifacts Engineering and Production stages, inception phase, elaboration phase, construction phase, transition phase, artifact sets, management artifacts, engineering artifacts and pragmatic artifacts, model based software architectures.

UNIT - III

Workflows and Checkpoints of process Software process workflows, Iteration workflows, Major milestones, Minor milestones, Periodic status assessments.

Process Planning Work breakdown structures, Planning guidelines, cost and schedule estimating process, iteration planning process, Pragmatic planning.

UNIT - IV

Project Organizations Line-of- business organizations, project organizations, evolution of organizations, process automation.

Project Control and process instrumentation The seven core metrics, management indicators, quality indicators, life-cycle expectations, Pragmatic software metrics, metrics automation.

UNIT - V

CCPDS-R Case Study and Future Software Project Management Practices Modern Project Profiles, Next-Generation software Economics, Modern Process Transitions.

TEXT BOOKS:

1. Managing the Software Process, *Watts S. Humphrey*, Pearson Education.
2. Software Project Management, *Walker Royce*, Pearson Education.

REFERENCE BOOKS:

1. Effective Project Management: Traditional, Agile, Extreme, Robert Wysocki, Sixth edition, Wiley India, rp2011.
2. An Introduction to the Team Software Process, Watts S. Humphrey, Pearson Education, 2000
3. Process Improvement essentials, James R. Persse, O'Reilly, 2006
4. Software Project Management, Bob Hughes & Mike Cotterell, fourth edition, TMH, 2006
5. Applied Software Project Management, Andrew Stellman & Jennifer Greene, O'Reilly, 2006.

6. Head First PMP, Jennifer Greene & Andrew Stellman, O'Reilly, 2007
7. Software Engineering Project Management, Richard H. Thayer & Edward Yourdon, 2nd edition, Wiley India, 2004.
8. The Art of Project Management, Scott Berkun, SPD, O'Reilly, 2011.
9. Applied Software Project Management, Andrew Stellman & Jennifer Greene, SPD, O'Reilly, 2011.
10. Agile Project Management, Jim Highsmith, Pearson education, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

SOFTWARE QUALITY ASSURANCE AND TESTING (PC – 6)

Course Objectives: The student should be able to:

- To understand software testing and quality assurance as a fundamental component of software life cycle
- To define the scope of SW T & QA projects
- To efficiently perform T & QA activities using modern software tools
- To estimate cost of a T & QA project and manage budgets
- To prepare test plans and schedules for a T&QA project
- To develop T & QA project staffing requirements
- To effectively manage a T & QA project

UNIT - I

Software Quality Assurance and Standards: The Software Quality challenge, What is Software Quality, Software Quality factors, The components of Software Quality Assurance system, Software Quality Metrics, Costs of Software Quality, Quality Management Standards, Management and its role in Software Quality Assurance, SQA unit and other actors in SQA system. - **(Chapters: 1-4, 21-23, 25, 26) of T3**

Quality Standards: ISO 9000 and Companion ISO Standards, CMM, CMMI, PCMM, Malcom Balridge, 3 Sigma, 6 Sigma and other latest quality standards **(Refer Internet and R11, R12, R13).**

UNIT - II

Software Testing Strategy and Environment: Minimizing Risks, Writing a Policy for Software Testing, Economics of Testing, Testing-an organizational issue, Management Support for Software Testing, Building a Structured Approach to Software Testing, Developing a Test Strategy

Building Software Testing Process: Software Testing Guidelines, workbench concept, Customizing the Software Testing Process, Process Preparation checklist - **(Chapters: 2,3) of T1**

Software Testing Techniques: Dynamic Testing – Black Box testing techniques, White Box testing techniques, Static testing, Validation Activities, Regression testing -**(Chapters: 4, 5, 6, 7, 8) of T2**

UNIT - III

Software Testing Tools: Selecting and Installing Software Testing tools – **(Chapter 4) of T1.**

Automation and Testing Tools - **(Chapter 15) of T2** Load Runner, Win runner and Rational Testing Tools, Silk test, Java Testing Tools, JMetra, JUNIT and Cactus. **(Refer Internet and R9, R10)**

UNIT - IV

Testing Process

Seven Step Testing Process – I: Overview of the Software Testing Process, Organizing of Testing, Developing the Test Plan, Verification Testing, Validation Testing. **(Chapters 6, 7, 8, 9, 10) of T1**

UNIT - V

Seven Step Testing Process – II: Analyzing and Reporting Test results, Acceptance and Operational Testing, Post-Implementation Analysis

Specialized Testing Responsibilities: Software Development Methodologies, Testing Client/Server Systems **(Chapters 12, 13, 14, 15) of T1.**

TEXT BOOKS:

1. Effective Methods for Software Testing, Third edition, *William E. Perry*, Wiley India, 2009
2. Software Testing – Principles and Practices, *Naresh Chauhan*, Oxford University Press,

- 2010.
3. Software Quality Assurance – From Theory to Implementation, *Daniel Galin*, Pearson Education, 2009.

REFERENCE BOOKS:

1. Testing Computer Software, Cem Kaner, Jack Falk, Hung Quoc Nguyen, Wiley India, rp2012.
2. Software Testing – Principles, Techniques and Tools, *M.G.Limaye*, Tata McGraw-Hill, 2009.
3. Software Testing - A Craftsman's approach, *Paul C. Jorgensen*, Third edition, Auerbach Publications, 2010.
4. Foundations of Software Testing, *Aditya P. Mathur*, Pearson Education, 2008.
5. Software Testing and Quality Assurance – Theory and Practice, *Kshirasagar Naik, Priyadashi Tripathy*, Wiley India, 2010.
6. Software Testing, *Ron Patton*, Second edition, Pearson Education, 2006.
7. Software Testing and Analysis – Process, Principles and Techniques, *Mauro Pezze, Michal Young*, Wiley India, 2008.
8. Software Testing Techniques, Boris Beizer, Second edition, Wiley India, 2006
9. Foundations of Software Testing, Dorothy Graham, et al., Cengage learning, 2007, rp 2010.
10. Software Testing - Effective Methods, Tools and Techniques, *Renu Rajani, Pradeep Oak*, Tata McGraw-Hill, rp2011.
11. Software Automation Testing Tools for Beginners, *Rahul Shende*, Shroff Publishers and Distributors, 2012.
12. Software Testing Tools, *K.V.K.K. Prasad*, Dream Tech Press, 2008.
13. Software Testing Concepts and Tools, *Nageswara Rao Pusuluri*, Dream Tech press, 2007.
14. Software Quality Assurance, *Milind Limaye*, Tata McGraw-Hill, 2011.
15. Software Quality – Theory and Management, *Alan C. Gillies*, Second edition, Cengage Learning, 2009.
16. Software Quality – A Practitioner's approach, *Kamna Malik, Praveen Choudhary*, Tata McGraw-Hill, 2008.
17. Software Quality Models and Project Management in a Nutshell, *Shailesh Mehta*, Shroff Publishers and Distributors, 2010.
18. Software Quality Engineering – Testing, Quality Assurance and Quantifiable Improvement, *Jeff Tian*, Wiley India, 2006.
19. Software Quality, *Mordechai Ben-Menachem/Garry S. Marliss*, Cengage Learning, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

SCRIPTING LANGUAGES (PE - 3)

Course Objectives:

- To be able to introduce core programming basics and program design with functions using Python programming language.
- To understand the high-performance programs designed to strengthen the practical expertise.

UNIT - I:

Introduction to Python, Installing Python. How a Program Works, Using Python, Program Development Cycle, Input, Processing, and Output, Displaying Output with the Print Function, Comments, Variables, Reading Input from the Keyboard, Performing Calculations (Operators. Type conversions, Expressions), More about Data Output. Decision Structures and Boolean Logic: if, if-else, if-elif-else Statements, Nested Decision Structures, Comparing Strings, Logical Operators, Boolean Variables. Repetition Structures: Introduction, while loop, for loop, Calculating a Running Total, Input Validation Loops, Nested Loops.

Data types and Expressions: Strings, Assignment and Comments, Numeric Data Types and Character Sets, Expressions, Functions and Modules.

UNIT - II:

Control Statements: Definite Iteration, Formatting Text for Output, Selection, Conditional Iteration.

File and Exceptions: Introduction to File Input and Output, Using Loops to Process Files, Processing Records, Exceptions.

Functions: Introduction, Defining and Calling a Void Function, Designing a Program to Use Functions, Local Variables, Passing Arguments to Functions, Global Variables and Global Constants, Value-Returning Functions-Generating Random Numbers, The math Module, Storing Functions in Modules.

UNIT - III:

Strings and Text Files : Accessing Characters and Substrings in a String, Strings and Number System, String Methods, Basic String Operations, String Slicing, Testing, Searching, and Manipulating Strings. Text Files, Data Encryption, Lists, Introduction to Lists, List slicing, Finding Items in Lists with the in Operator, List Methods and Useful Built-in Functions, Copying Lists, Processing Lists, Two-Dimensional Lists, Tuples Sequences, Tuples. Dictionaries and Sets: Dictionaries, Sets, Serializing Objects. Recursion: Introduction, Problem Solving with Recursion, Examples of Recursive Algorithms.

UNIT - IV:

Design with Classes: Classes and Objects, Classes and Functions, Classes and Methods, Working with Instances, Inheritance and Polymorphism. Object-Oriented Programming: Procedural and Object-Oriented Programming, Classes, techniques for Designing Classes.

UNIT - V:

Graphical User Interfaces: Behavior of terminal based programs and GUI-based programs, Coding simple GUI-based programs, other useful GUI resources. GUI Programming: Graphical User Interfaces, Using the tkinter Module, Display text with Label Widgets, Organizing Widgets with Frames, Button Widgets and Info Dialog Boxes, Getting Input with Entry Widget, Using Labels as Output Fields, Radio Buttons, Check Buttons.

Simple Graphics and Image Processing: Overview of Turtle Graphics, Two dimensional Shapes, Colors and RGB System, Image Processing.

TEXT BOOKS:

1. Kenneth A. Lambert, The Fundamentals of Python: First Programs, 2011, Cengage Learning.
2. Think Python First Edition, **by Allen B. Downey, Orielly publishing**

REFERENCE BOOKS:

1. Introduction to Computation and Programming Using Python. , John V. Guttag, The MIT Press James Payne, Beginning Python using Python 2.6 and Python 3, Wrox publishing
2. Paul Gries, Practical Programming: An Introduction to Computer Science using Python 3 The Pragmatic Bookshelf, 2nd edition (4 Oct. 2013)
3. Charles Dierach, Introduction to Computer Science using Python

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech - I Year – II Semester (Software Engineering)

INFORMATION RETRIEVAL SYSTEMS (PE - 3)

Course Objectives: On completion of this course you should have gained a good understanding of the foundation concepts of information retrieval techniques and be able to apply these concepts into practice. Specifically, you should be able to:

- To use different information retrieval techniques in various application areas
- To apply IR principles to locate relevant information large collections of data
- To analyse performance of retrieval systems when dealing with unmanaged data sources
- To implement retrieval systems for web search tasks.

UNIT - I

Boolean retrieval. The term vocabulary and postings lists. Dictionaries and tolerant retrieval. Index construction. Index compression.

UNIT - II

Scoring, term weighting and the vector space model. Computing scores in a complete search system. Evaluation in information retrieval. Relevance feedback and query expansion.

UNIT - III

XML retrieval. Probabilistic information retrieval. Language models for information retrieval. Text classification. Vector space classification.

UNIT - IV

Support vector machines and machine learning on documents, flat clustering, Hierarchical clustering, Matrix decompositions and latent semantic indexing.

UNIT - V

Web search basics, Web crawling and indexes, Link analysis.

TEXT BOOKS:

1. Introduction to Information Retrieval , Christopher D. Manning and Prabhakar Raghavan and Hinrich Schütze, Cambridge University Press, 2008.

REFERENCE BOOKS:

1. Information Storage and Retrieval Systems: Theory and Implementation, Kowalski, Gerald, Mark T Maybury, Springer.
2. Modern Information Retrieval, Ricardo Baeza-Yates, Pearson Education, 2007.
3. Information Retrieval: Algorithms and Heuristics, David A Grossman and Ophir Frieder, 2nd Edition, Springer, 2004.
4. Information Retrieval Data Structures and Algorithms, William B Frakes, Ricardo Baeza-Yates, Pearson Education, 1992.
5. Information Storage & Retrieval, Robert Korfhage , John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

SEMANTIC WEB AND SOCIAL NETWORKS (PE - 3)

Course Objectives:

- To learn Web Intelligence
- To learn Knowledge Representation for the Semantic Web
- To learn Ontology Engineering
- To learn Semantic Web Applications, Services and Technology
- To learn Social Network Analysis and semantic web

UNIT – I:

Web Intelligence: Thinking and Intelligent Web Applications, The Information Age ,The World Wide Web, Limitations of Today's Web, The Next Generation Web, Machine Intelligence, Artificial Intelligence, Ontology, Inference engines, Software Agents, Berners-Lee www, Semantic Road Map, Logic on the semantic Web.

UNIT - II:

Knowledge Representation for the Semantic Web: Ontologies and their role in the semantic web, Ontologies Languages for the Semantic Web –Resource Description Framework(RDF) / RDF Schema, Ontology Web Language(OWL),UML/XML/XML Schema.

UNIT- III:

Ontology Engineering: Ontology Engineering, Constructing Ontology, Ontology Development Tools, Ontology Methods, Ontology Sharing and Merging, Ontology Libraries and Ontology Mapping, Logic, Rule and Inference Engines.

UNIT- IV:

Semantic Web Applications, Services and Technology: Semantic Web applications and services, Semantic Search, e-learning, Semantic Bioinformatics, Knowledge Base ,XML Based Web Services, Creating an OWL-S Ontology for Web Services, Semantic Search Technology, Web Search Agents and Semantic Methods,

UNIT - V:.

Social Network Analysis and semantic web: What is social Networks analysis, development of the social networks analysis, Electronic Sources for Network Analysis – Electronic Discussion networks, Blogs and Online Communities, Web Based Networks. Building Semantic Web Applications with social network features.

TEXT BOOKS:

1. Thinking on the Web - Berners Lee, Godel and Turing, Wiley interscience, 2008.
2. Social Networks and the Semantic Web, Peter Mika, Springer, 2007.

REFERENCE BOOKS:

1. Semantic Web Technologies, Trends and Research in Ontology Based Systems, J. Davies, R. Studer, P. Warren, John Wiley & Sons.
2. Semantic Web and Semantic Web Services - Liyang Lu Chapman and Hall/CRC Publishers,(Taylor & Francis Group)
3. Information Sharing on the semantic Web - Heiner Stuckenschmidt; Frank Van Harmelen, Springer Publications.
4. Programming the Semantic Web, T. Segaran, C. Evans, J. Taylor, O'Reilly, SPD.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech - I Year – II Semester (Software Engineering)

E-COMMERCE (PE - 3)

Course Objectives:

- To identify the major categories and trends of e-commerce applications.
- To identify the essential processes of an e-commerce system.
- To identify several factors and web store requirements needed to succeed in e-commerce.
- To discuss the benefits and trade-offs of various e-commerce clicks and bricks alternatives.
- To understand the main technologies behind e-commerce systems and how these technologies interact.
- To discuss the various marketing strategies for an online business.
- To define various electronic payment types and associated security risks and the ways to protect against them.

UNIT - I

Electronic Commerce-Frame work, anatomy of E-Commerce applications, E-Commerce Consumer applications, E-Commerce organization applications.

Consumer Oriented Electronic commerce - Mercantile Process models.

UNIT - II

Electronic payment systems - Digital Token-Based, Smart Cards, Credit Cards, Risks in Electronic Payment systems.

Inter Organizational Commerce - EDI, EDI Implementation, Value added networks.

UNIT - III

Intra Organizational Commerce - work Flow, Automation Customization and internal Commerce, Supply chain Management.

Corporate Digital Library - Document Library, digital Document types, corporate Data Warehouses.

UNIT- IV

Advertising and Marketing - Information based marketing, Advertising on Internet, on-line marketing process, market research.

Consumer Search and Resource Discovery-Information search and Retrieval, Commerce Catalogues, Information Filtering.

UNIT - V

Multimedia - key multimedia concepts, Digital Video and electronic Commerce, Desktop video processing's, Desktop video conferencing.

TEXT BOOK:

1. Frontiers of electronic commerce – Kalakata, Whinston, Pearson.

REFERENCE BOOKS:

1. E-Commerce fundamentals and applications Hendry Chan, Raymond Lee, Tharam Dillon, Ellizabeth Chang, John Wiley.
2. E-Commerce, S. Jaiswal – Galgotia.
3. E-Commerce, Efrain Turbon, Jae Lee, David King, H. Michael Chang.
4. Electronic Commerce – Gary P. Schneider – Thomson.
5. E-Commerce – Business, Technology, Society, Kenneth C.Taudon, Carol Guyerico Traver.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech - I Year – II Semester (Software Engineering)

SOFTWARE SECURITY ENGINEERING (PE - 4)

Course Objectives:

- Students will demonstrate knowledge of the distinction between critical and non-critical systems.
- Students will demonstrate the ability to manage a project including planning, scheduling, and risk assessment/management.
- Students will author a software requirements document.
- Students will demonstrate an understanding of the proper contents of a software requirements document.
- Students will author a formal specification for a software system.
- Students will demonstrate an understanding of distributed system architectures and application architectures.
- Students will demonstrate an understanding of the differences between real-time and non-real time systems.
- Students will demonstrate proficiency in rapid software development techniques.
- Students will be able to identify specific components of a software design that can be targeted for reuse.
- Students will demonstrate proficiency in software development cost estimation.
- Students will author a software testing plan.

UNIT – I

Security a software Issue: introduction, the problem, Software Assurance and Software Security, Threats to software security, Sources of software insecurity, Benefits of Detecting Software Security

What Makes Software Secure: Properties of Secure Software, Influencing the security properties of software, Asserting, and specifying the desired security properties?

UNIT – II

Requirements Engineering for secure software: Introduction, the SQUARE process Model, Requirements elicitation and prioritization

UNIT – III

Secure Software Architecture and Design: Introduction, software security practices for architecture and design: architectural risk analysis, software security knowledge for architecture and design: security principles, security guidelines and attack patterns

Secure coding and Testing: Code analysis, Software Security testing, Security testing considerations throughout the SDLC

UNIT – IV

Security and Complexity: System Assembly Challenges: introduction, security failures, functional and attacker perspectives for security analysis, system complexity drivers and security

UNIT – V

Governance and Managing for More Secure Software: Governance and security, Adopting an enterprise software security framework, How much security is enough?, Security and project management, Maturity of Practice

TEXT BOOK:

1. Software Security Engineering: Julia H. Allen, Pearson Education

REFERENCE BOOKS:

1. Developing Secure Software: Jason Grembi, Cengage Learning
2. Software Security : Richard Sinn, Cengage Learning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

CYBER SECURITY (PE - 4)

Course Objectives:

- To learn about cyber crimes and how they are planned
- To learn the vulnerabilities of mobile and wireless devices
- To learn about the crimes in mobile and wireless devices

UNIT - I

Introduction to Cybercrime:

Introduction, Cybercrime, and Information security, who are cybercriminals, Classifications of Cybercrimes, Cybercrime: The legal Perspectives and Indian Perspective, Cybercrime and the Indian ITA 2000, A Global Perspective on Cybercrimes.

Cyber offenses: How criminals Plan Them: Introduction, How Criminals plan the Attacks, Social Engineering, Cyber stalking, Cyber cafe and Cybercrimes, Botnets: The Fuel for Cybercrime, Attack Vector, Cloud Computing.

UNIT - II

Cybercrime: Mobile and Wireless Devices: Introduction, Proliferation of Mobile and Wireless Devices, Trends in Mobility, Credit card Frauds in Mobile and Wireless Computing Era, Security Challenges Posed by Mobile Devices, Registry Settings for Mobile Devices, Authentication service Security, Attacks on Mobile/Cell Phones, Mobile Devices: Security Implications for Organizations, Organizational Measures for Handling Mobile, Organizational Security Policies and Measures in Mobile Computing Era, Laptops.

UNIT - III

Cybercrimes and Cyber security: the Legal Perspectives: Introduction, Cyber Crime and Legal Landscape around the world, Why Do We Need Cyber laws: The Indian Context, The Indian IT Act, Challenges to Indian Law and Cybercrime Scenario In India, Digital signatures and the Indian IT Act, Amendments to the Indian IT Act, Cybercrime and Punishment
Cyber law, Technology and Students: Indian Scenario.

UNIT - IV

Understanding Computer Forensics: Introduction, Historical background of Cyber forensics, Digital Forensics Science, The Need for Computer Forensics, Cyber Forensics and Digital evidence, Forensics Analysis of Email, Digital Forensics Lifecycle, Chain of Custody concept, Network Forensics, Approaching a computer, Forensics Investigation, Challenges in Computer Forensics, Special Tools and Techniques
Forensics Auditing

UNIT - V

Cyber Security: Organizational Implications: Introduction, Cost of Cybercrimes and IPR issues, Web threats for Organizations, Security and Privacy Implications, Social media marketing: Security Risks and Perils for Organizations, Social Computing and the associated challenges for Organizations.

TEXT BOOK:

1. **Cyber Security:** *Understanding Cyber Crimes, Computer Forensics and Legal Perspectives*, Nina Godbole and Sunil Belapure, Wiley INDIA.

2. **Introduction to Cyber Security**, Chwan-Hwa (john) Wu, J. David Irwin. CRC Press T & F Group

REFERENCE BOOK:

1. **Cyber Security Essentials**, James Graham, Richard Howard and Ryan Otson, CRC Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

INFORMATION SECURITY AND AUDIT (PE - 4)

Course Objectives:

To introduce the fundamental concepts and techniques in computer and network security, giving students an overview of information security and auditing, and to expose students to the latest trend of computer attack and defense. Other advanced topics on information security such as mobile computing security, security and privacy of cloud computing, as well as secure information system development will also be discussed.

UNIT - I

A model for Internetwork security, Conventional Encryption Principles & Algorithms (DES, AES, RC4, Blowfish), Block Cipher Modes of Operation, Location of Encryption Devices, Key Distribution. Public key cryptography principles, public key cryptography algorithms (RSA, Diffie-Hellman, ECC), public Key Distribution.

UNIT - II

Approaches of Message Authentication, Secure Hash Functions (SHA-512, MD5) and HMAC, Digital Signatures, Kerberos, X.509 Directory Authentication Service, Email Security: Pretty Good Privacy (PGP)

IP Security: Overview, IP Security Architecture, Authentication Header, Encapsulating Security Payload, Combining Security Associations and Key Management.

UNIT - III

Web Security: Requirements, Secure Socket Layer (SSL) and Transport Layer Security (TLS), Secure Electronic Transaction (SET).

Firewalls: Firewall Design principles, Trusted Systems, Intrusion Detection Systems

UNIT - IV

Auditing For Security: Introduction, Basic Terms Related to Audits, Security audits, The Need for Security Audits in Organization, Organizational Roles and Responsibilities for Security Audit, Auditors Responsibility In Security Audits, Types Of Security Audits.

UNIT - V

Auditing For Security: Approaches to Audits, Technology Based Audits Vulnerability Scanning And Penetration Testing, Resistance to Security Audits, Phase in security audit, Security audit Engagement Costs and other aspects, Budgeting for security audits, Selecting external Security Consultants, Key Success factors for security audits.

TEXT BOOKS:

1. Cryptography and Network Security by William Stallings, Fourth Edition, Pearson Education 2007.
2. Network Security Essentials (Applications and Standards) by William Stallings Pearson Education, 2008.
3. Cryptography & Network Security by Behrouz A. Forouzan, TMH 2007.
4. Information Systems Security by Nina Godbole, WILEY 2008.

REFERENCE BOOKS:

1. Information Security by Mark Stamp, Wiley – INDIA, 2006.
2. Fundamentals of Computer Security, Springer.

3. Network Security: The complete reference, Robert Bragg, Mark Rhodes, TMH
4. Computer Security Basics by Rick Lehtinen, Deborah Russell & G. T. Gangemi Sr., SPD O'REILLY 2006.
5. Modern Cryptography by Wenbo Mao, Pearson Education 2007.
6. Principles of Information Security, Whitman, Thomson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech - I Year – II Semester (Software Engineering)

SYSTEMS ENGINEERING (PE - 4)

UNIT – I

Introduction: Introduction to Systems Engineering, System life cycle, System Vee model, Decision making during system design. Six functions of design process, System architectures, System requirements analysis, specifications, documentation, Overview of the Systems Engineering Design Process

UNIT-II

Modeling: System modeling through IDEF0 models, Modeling and SysML Modeling, Higraphs, N2 charts, Decision analysis for design tradeoffs, Decision making under uncertainty, System Failure Analysis

UNIT – III

System Architecture Development: Functional Architecture Development, Physical Architecture Development

UNIT- IV

Interface Design: Interface Design, Integration, and Qualification

UNIT- V

Supplementary Topics: Graphical Modeling Techniques, IDEF0 Model of the Engineering of a System

TEXTBOOKS:

1. Dennis M Beude, The engineering Design of Systems: Models and Methods, Wiley India, 2006.
2. Alexander Kossaikoff, William N Sweet, Systems Engineering: Principles and Practice, Wiley India, 2010

REFERENCES:

1. E K Antonsson and J Cagan (Ed) Formal Engineering Design Synthesis, Cambridge University press, NY, 2001.
2. A Ertas & J C Jones, The Engineering Design Process, John Wiley and Sons, 1993.
3. A Chakrabarti (Ed.), Engineering Design & Synthesis, Springer, 2002

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech- I Year – II Semester (Software Engineering)

SOFTWARE TESTING LAB

Course Objectives: The student should be able to:

- To understand software testing and quality assurance as a fundamental component of software life cycle
- To define the scope of SW T&QA projects
- To efficiently perform T&QA activities using modern software tools
- To estimate cost of a T&QA project and manage budgets
- To prepare test plans and schedules for a T&QA project
- To develop T&QA project staffing requirements
- To effectively manage a T&QA project

Software Testing Objectives:

To learn to use the following (or similar) automated testing tools to automate testing:

- a. Win Runner/QTP for functional testing.
- b. Load Runner for Load/Stress testing.
- c. Test Director for test management.
- d. JUnit, HTML Unit, CPPUnit.

Sample problems on testing:

1. Write programs in 'C' Language to demonstrate the working of the following constructs:
i) do...while ii) while....do iii) if...else iv) switch v) for
2. "A program written in 'C' language for Matrix Multiplication fails" Introspect the causes for its failure and write down the possible reasons for its failure.
3. Take any system (e.g. ATM system) and study its system specifications and report the various bugs.
4. Write the test cases for any known application (e.g. Banking application)
5. Create a test plan document for any application (e.g. Library Management System)
6. Refer Page no 115 in Text book 2(Foundations of software testing by Rex Black, Erik Van Veenendaal, Dorthy Graham) for the described scenario and observe the given
 - i. Equivalence Partitioning /Boundary Value Analysis
 - ii. Decision Tables
 - iii. State transition
 - iv. Statement and decision testing.consider any other scenario of your choice and do the same.
7. Refer Page no 158 in Text book 2(Foundations of software testing by Rex Black, Erik Van Veenendaal, Dorthy Graham) for the described scenario and observe the given **Incident Report** and consider any other scenario of your choice and do the same.
2. Study of any testing tool (e.g. Win runner)
3. Study of any web testing tool (e.g. Selenium)
4. Study of any bug tracking tool (e.g. Bugzilla, bugbit)
5. Study of any test management tool (e.g. Test Director)
6. Study of any open source-testing tool (e.g. Test Link)
7. Take a mini project (e.g. University admission, Placement Portal) and execute it. During the Life cycle of the mini project create the various testing documents* and final test report document.

Additional problems on testing:

1. Test the following using JUnit and CPP Unit:
 - i) Sorting problems
 - ii) Searching problems
 - iii) Finding gcd of two integers
 - iv) Finding factorial of a number.

2. Test web based forms using HTMLUnit.
 3. Test database stored procedures using SQLUnit.
- (Use sufficient number of test cases in solving above Problems)

***Note: To create the various testing related documents refer to the text “Effective Software Testing Methodologies by William E. Perry”**

TEXT BOOKS:

1. Software Testing Concepts and Tools, P. Nageswara Rao, Dream Tech Press, 2007.
2. Foundations of software testing by Rex Black, Erik Van Veenendaal, Dorthy Graham
3. Software Testing Concepts and Tools by Nageshwara Rao Pusuluri, Dream Tech Press
4. Software Testing Tools, K. V. K. K. Prasad, Dream Tech Press, 2008.
5. Software Testing with Visual Studio Team System 2008, S. Subashini, N. Satheesh kumar, Shroff Publishers Distributors.
6. Software Automation Testing Tools for Beginners, *Rahul Shende*, Shroff Publishers and Distributors, 2012.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech in STRUCTURAL ENGINEERING
Effective from Academic Year 2017- 18 admitted batch

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Theory of Elasticity	25	75	4	0	0	4
PC-2	Structural Dynamics	25	75	4	0	0	4
PC-3	Advanced Structural Analysis	25	75	4	0	0	4
PE-1	Advanced Concrete Technology Tall Buildings Advanced Foundation Engineering	25	75	3	0	0	3
PE-2	Advanced R.C. Design Soil Dynamic & Foundation Engineering Plastic Analysis & Design	25	75	3	0	0	3
OE-1	*Open Elective -I	25	75	3	0	0	3
Laboratory I	Advanced Structural Engineering Laboratory	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Advanced Steel Design	25	75	4	0	0	4
PC-5	Theory of Plates	25	75	4	0	0	4
PC-6	Pre-stressed Concrete	25	75	4	0	0	4
PE-3	Finite Element Method Bridge Engineering Design of Substructures	25	75	3	0	0	3
PE4	Earthquake Resistant Design of Buildings Repair & Rehabilitation of Buildings Stability of Structures	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	CAD Lab	25	75	0	0	3	2
Seminar II	Seminar-II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

ADVANCED STEEL DESIGN (PC - IV)

Course Objectives: To impart knowledge on behavior and design of various connections, industrial and steel girders.

Course Outcomes: The learner will be able to design different steel structures

UNIT - I:

Simple connections – Bolted, Pinned and Welded Connections: Bolted Connections- Load Transfer Mechanism – Failure of Bolted Joints – Specifications for Bolted Joints – Bearing – Type Connections – Tensile Strength of Plate – Strength and Efficiency of the Joint – Combined Shear and Tension – Slip – Critical Connections – Praying Action – Combined Shear and Tension for Slip-Critical Connections. Design of Groove welds- Design of Fillet Welds- Design of Intermittent fillet welds- Failure of Welds.

UNIT – II

Eccentric and Moment Connections: Introduction – Beams – Column Connections- Connections Subjected to Eccentric Shear – Bolted Framed Connections- Bolted Seat Connections – Bolted Bracket Connections. Bolted Moment Connections – Welded Framed Connections – Welded Bracket Connections - Moment Resistant Connections.

UNIT - III

Analysis and Design of Industrial Buildings : Dead loads, live loads and wind loads on roofs. Design wind speed and pressure, wind pressure on roofs; wind effect on cladding and louvers; Design of angular roof truss, tubular truss, truss for a railway platform. Design of purlins for roofs, design of built up purlins, design of knee braced trusses and stanchions. Design of bracings.

UNIT - IV:

Design of Steel Truss Girder Bridges :Types of truss bridges, component parts of a truss bridge, economic proportions of trusses, self weight of truss girders, design of bridge compression members, tension members; wind load on truss girder bridges; wind effect on top lateral bracing; bottom lateral bracing; portal Bracing; sway bracing.

UNIT - V:

Design of Steel Bunkers and Silos: Introduction – Janseen's Theory – Airy's Theory – Design of Parameters – Design Criteria – Analysis of Bins – Hopper Bottom –Design of Bins.

TEXT BOOKS:

1. Limit State Design of Steel Structures S. K. Duggal, McGraw Hill Education Private Ltd. New Delhi.
2. Design of Steel Structures, K. S. Sairam, Pearson Education.

REFERENCES:

1. Design of Steel Structures, N. Subramanian, Oxford University Press.
2. Design Steel Structures Volume – II, Dr. Ramachandra & Vivendra Gehlot, Scientific Publishers Journals Department.
3. Design of Steel Structures Gaylord & Gaylord, Publisher; Tata McGraw Hill, Education. Edition 2012.
4. Indian Standard Code – IS – 800-2007 General Construction in Steel- Code of Practice,
5. Steel Tables.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

THEORY OF PLATES (PC - V)

Course Objectives: To impart knowledge on the behavior of plates and to analyse the problems pertaining to beams on elastic foundation.

Course Outcomes: The learner will be able to understand the behavior of plates for loadings and boundary conditions.

UNIT - I

Cylindrical Bending: Different kind of plates – Assumptions – Derivation of differential equation for cylindrical bending of long rectangular plates - Analysis of uniformly loaded rectangular plates with edges simply supported and fixed subjected to uniform load.

Pure Bending of Plates: Slope and curvature of slightly bent plates – Relations between moments and curvature - Particular cases of pure bending - Strain energy in pure bending –Energy methods like Ritz and Galerkin Methods to rectangular plates subjected to simple loadings.

UNIT - II

Small Deflection Theory of Thin Rectangular Plates : Assumptions – Derivation of governing differential equation for thin plates – Boundary conditions – simply supported plate under sinusoidal load – Navier's solution – Application to different cases – Levy's solution for various boundary conditions subjected to different loadings like uniform and hydrostatic pressure.

UNIT - III

Circular Plates : Symmetrical loading – Relations between slope, deflection, moments and curvature – Governing differential equation – Uniformly loaded plates with clamped and simply supported edges – Central hole – bending by moments and shearing forces uniformly distributed.

Orthotropic Plates: Introduction – Bending of anisotropic plates - Derivation of governing differential equation – Determination of Rigidities in various cases like R.C. slabs, corrugated sheet – Application to the theory of grid works.

UNIT - IV

Plates on Elastic Foundations: Governing differential equation – deflection of uniformly loaded simply supported rectangular plate – Navier and Levy type solutions – Large plate loaded at equidistant points by concentrated forces P.

UNIT - V

Buckling of Plates: Governing equation for Bending of plate under the combined action of in-plane loading and lateral loads – Buckling of rectangular plates by compressive forces acting in one and two directions in the middle plane of plate

Finite Difference Methods: Introduction - Application to rectangular plates subjected to simple loading.

TEXT BOOK

1. Theory of Plates and Shells by Timoshenko, McGraw Hill Book Co., New York.

REFERENCES:

1. Theory and Analysis of Plates by P. Szilard, Prentice Hall.
2. Theory of Plates by K. Chandrasekhara, University Press.
3. Plate Analysis by N. K. Bairagi, Khanna Publishers. New Delhi.
4. Numerical Methods for Engineering Problems, N. Krishna Raju & K. U Muthu, Mac-Millan publishers

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)
PRE-STRESSED CONCRETE (PC - VI)

Course Objectives: To impart knowledge on basics of prestressing and designing of different structural elements using Prestressing techniques.

Course Outcomes: The learner will be able to understand the prestressing techniques, design the various structural elements using Prestressing techniques.

UNIT - I

General Principles of Prestressed Concrete : Pre-tensioning and post – tensioning – Prestressing by straight, concentric, eccentric, bent and parabolic tendons – Different methods and systems of prestressing like Hoyer system, Freyssinet system, Magnel Blaton system – Lee-Mc call system. **Losses of Prestress:** Loss of prestress in pre-tensioned and post-tensioned members due to various causes like elastic shortening of concrete, shrinkage of concrete, creep of concrete, relaxation of steel, slip in anchorage, bending of member and frictional loss – Analysis of sections for flexure.

UNIT - II

Design of Section for Flexure: Allowable stresses – Elastic design of simple beams having rectangular and I-section for flexure – kern lines – cable profile and cable layout.

Design of Sections for Shear: Shear and Principal Stresses – Improving shear resistance by different prestressing techniques – horizontal, sloping and vertical prestressing – Analysis of rectangular and I-beam – Design of shear reinforcement – IS: 1343: 2012 provisions.

UNIT - III

Deflections of Prestressed Concrete Beams : Short term deflections of uncracked members– Prediction of long-time deflections – load – deflection curve for a PSC beam – IS code requirements for max. Deflections.

UNIT - IV

Transfer of Prestress in Pretensioned Members : Transmission of prestressing force by bond – Transmission length – Flexural bond stresses – IS: 1343 : 2012 provisions – Anchorage zone stresses in post tensioned members – stress distribution in End block – Analysis by approximate, Guyon and Magnel methods – Anchorage zone reinforcement.

UNIT - V

Statically Indeterminate Structures : Advantages & disadvantages of continuous PSC beams – Primary and secondary moments – P and C lines – Linear transformation concordant and non-concordant cable profiles – Analysis of continuous beams and simple portal frames (single bay and single story)

TEXT BOOKS:

1. Prestressed concrete by Krishna Raju, Tata McGraw Hill Book – Co., New Delhi.
2. Prestressed Concrete by K.U. Muthu, et.al, PHI Learning Pvt. Ltd.,

REFERENCES:

1. Design of Prestressed Concrete Structures by T.Y. Lin and Burn, John Wiley, New York.
2. Prestressed Concrete by N. Rajagopalan, Alpha Science International.
3. Prestressed Concrete by S. RamamruthamDhanpatRai& Sons, Delhi.
4. IS 1343 -2012 Prestressed Concrete – Code of Practice, Bureau of Indian Standards.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

FINITE ELEMENT METHOD (PE – III)

Course Objectives: To impart knowledge about various finite element techniques and development of finite element code.

Course Outcome: The learner will be able to solve continuum problems using finite element analysis.

UNIT - I

Introduction: Concepts of FEM - steps involved - merits and demerits - energy principles –

Discretization - Raleigh - Ritz method of functional approximation.

Principles of Elasticity: Stress equations - strain displacement relationships in matrix form plane stress, plane strain and axi-symmetric bodies of revolution with axi-symmetric loading.

UNIT - II

One dimensional FEM: Stiffness matrix for beam and bar elements - shape functions for 1-D elements.

Two dimensional FEM: Different types of elements for plane stress and plane strain analysis - displacement models - generalized coordinates - shape functions - convergent and compatibility requirements - geometric invariance - natural coordinate system - area and volume coordinates - generation of element stiffness and nodal load matrices

UNIT - III

Isoparametric formulation: Concept - different isoparametric elements for 2D analysis - formulation of 4-noded and 8-noded isoparametric quadrilateral elements - Lagrange elements - serendipity elements.

Axi Symmetric Analysis: bodies of revolution - axi symmetric modeling - strain displacement relationship - formulation of axi symmetric elements.

Three dimensional FEM: Different 3-D elements-strain-displacement relationship - formulation of hexahedral and isoparametric solid element.

UNIT - IV

Introduction to Finite Element Analysis of Plates: Basic theory of plate bending - thin plate theory - stress resultants - Mindlin's approximations - formulation of 4-noded isoperimetric quadrilateral plate element – Shell Element.

UNIT - V

Introduction to non – linear finite analysis – basic methods – application to Special structures.

Text Books:

1. A First Course in a Finite Element by Daryl L .Logan, CL Engineers.
2. Concepts and Applications of Finite Element Analysis by Robert D. Cook, David S. Malkus and Michael E. Plesha, John Wiley & Sons.

References:

1. Introduction to Finite element Method by Tirupathi Chandra Patla and Belugunudu
2. Finite element Methods by OC Zienkiewicz
3. Finite element analysis, theory and programming by GS Krishna Murthy.
4. Introduction to Finite element Method by JN Reddy.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

BRIDGE ENGINEERING (PE - III)

Course Objectives: To impart knowledge on the behavior and design aspects of various types of bridges.

Course Outcomes: The learner will be able to analyze and design of different types of bridges

UNIT - I

Concrete Bridges: Introduction-Types of Bridges-Economic span length-Types of loading-Dead load-live load-Impact Effect-Centrifugal force-wind loads-Lateral loads-Longitudinal forces-Sesmic loads-Frictional resistance of expansion bearings-Secondary Stresses-Temperature Effect-Erection Forces and effects-Width of roadway and footway-General Design Requirements.

UNIT - II

Solid slab Bridges: Introduction-Method of Analysis and Design.

UNIT - III

Girder Bridges: Introduction-Method of Analysis and Design-Courbon's Theory, Grillage analogy

UNIT - IV

Pre-Stressed Concrete Bridges: Basic principles-General Design requirements-Mild steel reinforcement in prestressed concrete member-Concrete cover and spacing of pre-stressing steel-Slender beams-Composite Section-Propped-Design of Propped Composite Section-Unpropped composite section-Two-stage Prestressing-Shrinking stresses-General Design requirements for Road Bridges.

UNIT - V

Analysis of Bridge Decks: Harmonic analysis and folded plate theory-Grillage analogy- Finite strip method and FEM. Sub-structure of bridges: Substructure- Beds block-Piers- Pier Dimensions- Design loads for piers- Abutments- Design loads for Abutments.

TEXT BOOKS:

1. Essentials of Bridge Engineering by Johnson Victor, Oxford & IBH
2. Design of Bridges by N. Krishna Raju, Oxford & IBH

REFERENCES

1. Design of Concrete Bridges by M. G. Aswani, V. N. Vazirani and M. M. Ratwani.
2. Bridge Deck Behaviour by E. C. Hambly.
3. Design of Bridges by V. V. Sastry, Dhanpat Rai & Co
4. Concrete Bridge Design and Practice by V. K. Raina.
5. Design of Bridge Structures by Jagadeesh & Jayaram, PHI learning Pvt. Ltd.
6. IRC: 112, 2011, Code of Practice for Concrete Road Bridges.
7. IRC: 6 and 21 2000, Code of Practice for Concrete Road Bridges

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. I Year II - Sem (Structural Engg.)

DESIGN OF SUBSTRUCTURES (PE - III)

Course Objectives: To impart knowledge on geotechnical and structural design of different types of foundation appropriate to the type of soil for different structures.

Course Outcome: The learner will be able to design shallow and deep foundations from both geotechnical and structural considerations.

UNIT – I

Shallow Foundations: Basic requirements of foundation –Types and selection of foundations. Bearing capacity of foundations, structural design of isolated, combined, eccentric, strip, and strap footings, Detailing of reinforcement.

UNIT – II

Raft Foundations: Types of rafts, SBC of raft foundation and structural design of different raft foundations, Detailing of reinforcement.

UNIT – III

Pile Foundations: Types of piles, Load carrying capacity of single and pile groups, structural design of piles, pile caps and pile-raft foundation, Detailing of reinforcement.

UNIT – IV

Design of Retaining walls: Stability Checks and structural design of gravity, Cantilever retaining walls, Detailing of reinforcement.

UNIT – V

Machine Foundations: Vibration analysis of machine foundation - Design of foundation for Reciprocating machines and Impact machines - as per I S Codes, Detailing of reinforcement.

TEXT BOOKS:

1. Varghese P.C. Design of RC foundations, PHI Learning Pvt. Ltd.
2. Unnikrishnana Pillai & Devadas Menon, Reinforces Concrete Design, McGraw Hill Publishing Pvt. Ltd.

REFERENCE:

1. Bowles .J.E., "Foundation Analysis and Design", McGraw Hill Publishing co., New York, 1986
2. Tomlinson. M.J, "Foundation Design and Construction", Longman, Sixth Edition, New Delhi, 1995.
3. Das, B.M., Principles of Foundation Engineering, Design and Construction, Fourth Edition, PWS Publishing, 1999.
4. Narayan V. Nayak, Foundation design manual, Dhanpat Rai & Sons, 2006.
5. Prakash Shamsheer and Puri Vijay K, Foundations for Machines, Analysis and Design" John Wiley and Sons, USA, 1988.
6. IS 2911: Part 1: Sec 1: 1979 Code of practice for design and construction of pile foundations: Part 1 Concrete piles, Section 1 Driven cast in-situ concrete piles.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

EARTHQUAKE RESISTANT DESIGN OF BUILDINGS (PE - IV)

Course Objectives: To impart knowledge on the seismology and behavior of buildings during earthquakes.

Course Outcomes: The learner will be able to analyse and design buildings to resist seismic forces

UNIT - I

Engineering Seismology: Earthquake phenomenon cause of earthquakes-Faults- Plate tectonics- Seismic waves- Terms associated with earthquakes-Magnitude/Intensity of an earthquake-scales- Energy released-Earthquake measuring instruments-Seismoscope, Seismograph, accelerograph- Characteristics of strong ground motions- Seismic zones of India.

UNIT - II

Conceptual design: Introduction-Functional planning-Continuous load path-Overall form-simplicity and symmetry-elongated shapes-stiffness and strength-Horizontal and Vertical members-Twisting of buildings-Ductility-definition-ductility relationships-flexible buildings-framing systems-choice of construction materials-unconfined concrete-confined concrete-masonry-reinforcing steel. Introduction to earthquake resistant design: Seismic design requirements-regular and irregular configurations-basic assumptions-design earthquake loads-basic load combinations-permissible stresses-seismic methods of analysis-factors in seismic analysis-equivalent lateral force method-dynamic analysis-response spectrum method-Time history method.

UNIT - III

Reinforced Concrete Buildings: Principles of earthquake resistant design of RC members- Structural models for frame buildings- Seismic methods of analysis- Seismic design methods- IS code based methods for seismic design- Seismic evaluation and retrofitting- Vertical irregularities- Plan configuration problems- Lateral load resisting systems- Determination of design lateral forces- Equivalent lateral force procedure- Lateral distribution of base shear. Masonry Buildings: Introduction- Elastic properties of masonry assemblage- Categories of masonry buildings- Behaviour of unreinforced and reinforced masonry walls- Behaviour of walls- Box action and bands- Behaviour of infill walls- Improving seismic behaviour of masonry buildings- Load combinations and permissible stresses- Seismic design requirements- Lateral load analysis of masonry buildings.

UNIT - IV

Structural Walls and Non-Structural Elements: Strategies in the location of structural walls- sectional shapes- variations in elevation- cantilever walls without openings – Failure mechanism of non-structures- Effects of non-structural elements on structural system- Analysis of non-structural elements- Prevention of non-structural damage- Isolation of non-structures.

UNIT - V

Ductility Considerations in Earthquake Resistant Design of RC Buildings: Introduction- Impact of Ductility- Requirements for Ductility- Assessment of Ductility- Factors affecting Ductility- Ductile detailing considerations as per IS 13920. Behaviour of beams, columns and joints in RC buildings during earthquakes-Vulnerability of open ground storey and short columns during earthquakes. Capacity Based Design: Introduction to Capacity Design, Capacity Design for Beams and Columns- Case studies.

TEXT BOOKS:

1. Earthquake Resistant Design of structures – S. K. Duggal, Oxford University Press
2. Earthquake Resistant Design of structures – Pankaj Agarwal and Manish Shrikhande, Prentice Hall of India Pvt. Ltd.

REFERENCE BOOKS:

1. Seismic Design of Reinforced Concrete and Masonry Building – T. Paulay and M.J.N. Priestly, John Wiley & Sons
2. Masonry and Timber structures including earthquake Resistant Design – Anand S.Arya, Nemchand & Bros
3. Earthquake –Resistant Design of Masonry Building –Miha Tomazevic, Imperial College Press.
4. Earthquake Tips – Learning Earthquake Design and Construction C. V. R. Murty

REFERENCE CODES:

1. IS: 1893 (Part-1) -2016. "Criteria for Earthquake Resistant – Design of structures." B.I.S., New Delhi.
2. IS: 4326-1993, "Earthquake Resistant Design and Construction of Building", Code of Practice B.I.S., New Delhi.
3. IS: 13920- 2016, "Ductile detailing of concrete structures subjected to seismic force" – Guidelines, B.I.S., New Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

REPAIR & REHABILITATION OF BUILDINGS (PE - IV)

Course Objectives: To impart knowledge on the distress in structures.

Course Outcomes: The learner will be able to understand the reasons for distress in structures and will be able to suggest suitable solutions

UNIT – I

Introduction – Deterioration of Structures – Distress in Structures – Causes and Prevention. Mechanism of Damage – Types of Damage.

UNIT – II

Corrosion of Steel Reinforcement – Causes – Mechanism and Prevention. Damage of Structures due to Fire – Fire Rating of Structures – Phenomena of Desiccation.

UNIT – III

Inspection and Testing – Symptoms and Diagnosis of Distress - Damage assessment – NDT.

UNIT – IV

Repair of Structure – Common Types of Repairs – Repair in Concrete Structures – Repairs in Under Water Structures – Guniting – Shotcreting – Underpinning -Strengtheningof Structures – Strengthening Methods – Retrofitting – Jacketing.

UNIT – V

Health Monitoring of Structures – Use of Sensors – Building Instrumentation

REFERENCES

1. Concrete Technology by A. R. Santhakumar, Oxford University press
2. Defects and Deterioration in Buildings, E F & N Spon, London
3. Non-Destructive Evaluation of Concrete Structures by Bungey - Surrey University Press
4. Maintenance, Repair & Rehabilitation and Minor Works of Buildings by P. C. Varghese, PHI.
5. Maintenance and Repair of Civil Structures, B.L. Gupta and Amit Gupta, Standard Publications.
6. Concrete Repair and Maintenance Illustrated, RS Means Company Inc W. H. Ranso, (1981)
7. Building Failures: Diagnosis and Avoidance, EF & N Spon, London, B. A. Richardson, (1991).

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

STABILITY OF STRUCTURES (PE - IV)

Course Objectives: To impart knowledge on the elastic, inelastic buckling and torsional buckling of structures.

Course Outcomes: The learner will be able to understand buckling of bars and frames.

UNIT – I

Beam Columns; Differential equations for beam columns- beam columns with concentrated loads – continuous lateral loads-couples- beam columns with built in ends – continuous beams with axial load – application of trigonometrically series – Effects of initial curvature on deflections – Determination of allowable stresses.

UNIT - II

Elastic Buckling of bars and frames; Elastic Buckling of straight columns – Effect of shear stress on buckling – Eccentrically and laterally loaded columns- Buckling of frames-large deflections of buckled bars-Energy methods- Buckling of bars on elastic foundations- Buckle line of bar with intermediate compressive forces - Buckling of bars with change in cross-section – Effect of shear force on critical load- built up columns.

UNIT - III

In Elastic Buckling: Buckle line of straight bar- Double modulus theory – Tangent modulus theory, Inelastic lateral Buckling. Experiments and design formulae: Experiments on columns – Critical stress diagram – Empirical formulae for design –various end conditions

UNIT - IV

Torsion Buckling: Pure torsion of thin walled bars of open cross section – Non-uniform torsion of thin walled bars of open cross section- Torsional buckling – Buckling by torsion and flexure.

UNIT – V

Lateral buckling of simply supported Beams: Beams of Rectangular cross-section subjected to pure bending. Buckling of simply supported Rectangular plates: Derivation of equation of plate subjected to constant compression in one and two directions.

TEXT BOOKS

1. Theory of elastic Stability by Timshenko & Gere -McGraw Hill

REFERENCES

1. Stability of metallic structures by Blunch- McGraw Hill
2. Theory of Beam- Columns Vol. I by Chem. & Atste McGraw Hill
3. Stability Theory of Structures by Ashwini Kumar, Allied Publishers.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

CAD LABORATORY

Course Objective: To impart knowledge on the use of various softwares

Course Outcome: the student will be able to analyze and design structural elements of a building

1. Design of beam using Excel for flexural shear and with deflection check
 - a) Singly and doubly reinforced RC Beam
2. Design of Steel Beam using Excel for flexural shear and with deflection check
3. Design of RC slab one-way and two-way using Excel
4. Design of RC short & long columns subjected to biaxial bending.
5. Design of isolated footings using Excel
6. Analysis & design of 2-D steel truss
7. Analysis & Design of 2-D building frame
8. Analysis & Design of Multi-storey space frame (for mid rise) subjected to lateral loads
9. Plate bending using FEM
10. Modal analysis of a high rise building

Note: Exercises from 6-10 may be carried out using any relevant commercial software package.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech in STRUCTURAL ENGINEERING
Effective from Academic Year 2017- 18 admitted batch

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Theory of Elasticity	25	75	4	0	0	4
PC-2	Structural Dynamics	25	75	4	0	0	4
PC-3	Advanced Structural Analysis	25	75	4	0	0	4
PE-1	Advanced Concrete Technology Tall Buildings Advanced Foundation Engineering	25	75	3	0	0	3
PE-2	Advanced R.C. Design Soil Dynamic & Foundation Engineering Plastic Analysis & Design	25	75	3	0	0	3
OE-1	*Open Elective -I	25	75	3	0	0	3
Laboratory I	Advanced Structural Engineering Laboratory	25	75	0	0	3	2
Seminar I	Seminar-I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Advanced Steel Design	25	75	4	0	0	4
PC-5	Theory of Plates	25	75	4	0	0	4
PC-6	Pre-stressed Concrete	25	75	4	0	0	4
PE-3	Finite Element Method Bridge Engineering Design of Sub Structures	25	75	3	0	0	3
PE4	Earthquake Resistant Design of Buildings Repair & Rehabilitation of Buildings Stability of Structures	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	CAD Lab	25	75	0	0	3	2
Seminar II	Seminar-II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

THEORY OF ELASTICITY (PC-1)

Course Objectives: To impart knowledge on the basic concepts of theory of elasticity, and solve the Structural Engineering problems.

Course outcomes: The learner will be able to solve problems of elasticity and be able to apply numerical methods to solve continuum problems.

UNIT-I

Introduction: Elasticity - notation for forces and stress - components of stresses - components of strain - Hooks law. Plane stress and plane strain analysis - differential equations of equilibrium - boundary conditions – Strain Displacement Relations - compatibility equations - stress function

UNIT - II

Two dimensional problems in rectangular coordinates - solution by polynomials - Saint-Venants principle - determination of displacements - bending of simple beams – Simple Supported and Cantilever Beam.

UNIT - III

Two dimensional problems in polar coordinates - stress distribution symmetrical about an axis - pure bending of curved bars - strain components in polar coordinates - displacements for symmetrical stress distributions Edge Dislocation - general solution of two-dimensional problem in polar coordinates - application to Plates with Circular Holes – Rotating Disk. Bending of Prismatic Bars: Stress function - bending of cantilever - circular cross section - elliptical cross section - rectangular cross section.

UNIT - IV

Analysis of stress and strain in three dimensions - principal stress - stress ellipsoid - director surface - determination of principal stresses Stress Invariants - max shear stresses Stress Tensor – Strain Tensor- Homogeneous deformation - principal axes of strain-rotation. General Theorems: Differential equations of equilibrium - conditions of compatibility - determination of displacement - equations of equilibrium in terms of displacements - principle of super position - uniqueness of solution - the reciprocal theorem Strain Energy.

UNIT - V

Torsion of Circular Shafts - Torsion of Straight Prismatic Bars– Saint Venant's Method - torsion of prismatic bars - bars with elliptical cross sections - membrane analogy - torsion of a bar of narrow rectangular bars - solution of torsional problems by energy method - torsion of shafts, tubes , bars etc. - Torsion of Rolled Profile Sections.

TEXT BOOKS

1. Theory of Elasticity by Timoshenko, Mc-Graw hill Publications
2. Advanced Mechanics of Materials by Arthur P. Boresi, John Willey publishers

REFERENCES:

1. Theory of Elasticity by Y.C. Fung, Dover publications, New york
2. Theory of Elasticity by Sadhu singh, Khanna Publishers
3. Advanced Mechanics of solids by L.S.Srinath, Tata Mc-Graw Hill
4. Continuum Mechanics by P.N. ChandraMouli, Yes Dee Publishers

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

**STRUCTURAL DYNAMICS
(PC-2)**

Course Objectives: To impart knowledge on the fundamental of structural dynamics and their applications.

Course Outcomes: The learner will be able to understand the equation of motion, dynamics response of single, and multi degree-of freedom systems.

UNIT - I:

Theory of vibrations: Introduction - Elements of vibratory system - Degrees of Freedom - Continuous System - Lumped mass idealization - Oscillatory motion - Simple Harmonic motion - Vectorial representation of S.H.M. - Free vibrations of single degree of freedom system - undamped and damped vibrations - critical damping - Logarithmic decrement - Forced vibration of SDOF systems - Harmonic excitation -Dynamic magnification factor – Phase angle – Bandwidth

UNIT - II

Introduction to Structural Dynamics : Fundamental objectives of dynamic analysis -Types of prescribed loading - Methods of discretization - Formulation of equations of motion by different methods – Direct equilibration using Newton's law of motion / D'Alembert's principle, Principle of virtual work and Hamilton principle.

Single Degree of Freedom Systems: Formulation and solution of the equation of motion - Free vibration response - Response to Harmonic, Periodic, Impulsive and general dynamic loadings - Duhamel integral.

UNIT - III

Multi Degree of Freedom Systems : Selection of the degrees of Freedom - Evaluation of structural property matrices - Formulation of the MDOF equations of motion -Undamped free vibrations - Solutions of Eigen value problem for natural frequencies and mode shapes - Analysis of Dynamic response – Normal co-ordinates - Uncoupled equations of motion - Orthogonal properties of normal modes - Mode superposition procedure.

UNIT - IV

Practical Vibration Analysis: Introduction - Stodola method - Fundamental mode analysis - Analysis of second and higher modes - Holzer method - Basic procedure.

Continuous Systems: Introduction - Flexural vibrations of beams - Elementary case – Derivation of governing differential equation of motion - Analysis of undamped free vibrations of beams in flexure - Natural frequencies and mode-shapes of simple beams with different end conditions - Principles of application to continuous beams.

UNIT - V

Deterministic Earthquake Response of Systems – Rigid Foundation, Types of Earthquake Excitation – Response to Rigid – Soil Excitation, Lumped SDOF elastic systems – Lumped SDOF elastic system – Distributed Parameter Elastic Systems – SRSS, CQC combination of modal responses.

TEXT BOOKS:

1. Structural Dynamics by Mario Paz, C.B.S Publishers, New Delhi
2. Dynamics of Structures by Clough & Penzien, McGraw Hill, New York

REFERENCES:

1. Dynamics of Structures by Anil K. Chopra, Pearson Education (Singapore), Delhi.
2. Vibrations, Dynamics and Structural systems by Madhujit Mukhopadhyay, CRC press

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

**ADVANCED STRUCTURAL ANALYSIS
(PC-3)**

Course Objectives: To impart knowledge on the analysis of indeterminate structures like continuous beams, trusses and portal frames.

Course Outcomes: The learner will be able to analyse different indeterminate structures using Matrix methods.

UNIT - I

Introduction to matrix methods of analysis - static indeterminacy and kinematic indeterminacy - degree of freedom - coordinate system - structure idealization stiffness and flexibility matrices - suitability element stiffness equations - elements flexibility equations - mixed force - displacement equations - for truss element, beam element and tensional element.

Transformation of coordinates - element stiffness matrix - and load vector - local and global coordinates.

UNIT - II

Assembly of stiffness matrix from element stiffness matrix - direct stiffness method - general procedure - band matrix - semi bandwidth - computer algorithm for assembly by direct stiffness matrix method.

UNIT - III

Analysis of plane truss - continuous beam - plane frame and grids by flexibility methods.

UNIT - IV

Analysis of plane truss - continuous beam - plane frame and grids by stiffness methods.

UNIT - V. Special analysis procedures - static condensation and sub structuring - initial and thermal stresses.

Shear walls- Necessity - structural behaviour of large frames with and without shear walls - approximate methods of analysis of shear walls.

TEXT BOOKS:

1. Matrix Analysis of Frames structures by William Weaver J.R and James M. Gere, CBS publications.
2. Advanced Structural Analysis by Ashok. K. Jain, Nem Chand Brothers.

REFERENCES:

1. Basic Structural Analysis by C.S. Reddy, Tata Mc-Graw hill
2. Matrix Structural Analysis by Madhu B. Kanchi, John Willey publishers
3. Indeterminate Structural Analysis by K.U. Muthuet al., I.K. International Publishing House Pvt. Ltd.
4. Matrix Methods of Structural Analysis by J.L. Meek, Mc-Graw hill

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

**ADVANCED CONCRETE TECHNOLOGY
(PE-1)**

Course Objectives: To impart knowledge on ingredients of concrete, concrete mix design for proportioning and their testing.

Course Outcomes: The learner will be able to design concrete mixes of different grades and also use the special concretes.

UNIT – I

Ingredients of concrete: Cement-Bogue's compounds – Hydration Process– Types of cement – Setting times of cement - Aggregates – Shapes Testes on Aggregates - Gradation Charts – Combined aggregate-Alkali Silica Reaction -Admixtures – Chemical and Mineral admixtures.

UNIT – II

Fresh and Hardened Concrete: Fresh Concrete - workability tests on Concrete Setting times of Fresh Concrete - Segregation and bleeding.

Hardened Concrete: Abram's law- Gel space ratios, Maturity Concept – Stress Behaviour – Creep and Shrinkage – Durability tests on concrete - Non destructive testing of concrete.

UNIT - III

High Strength Concrete – Micro structure – Manufacturing and Properties- Design of HSC Using Erintryo Shaklok Method- Ultra High Strength Concrete.

High Performance Concrete- Requirements and properties of High Performance Concrete- Design Considerations.

UNIT –IV

Special Concrete: Self Compacting concrete – Polymer concrete – Fiber reinforced concrete – Reactive Powder concrete – Requirements and Guidelines – Advantages and Applications. Light weight concrete.

Concrete mix design: Quality Control - Quality assurance - Quality audit- Mix Design method - BIS method, ACI method, DOE method.

UNIT –V

Form work – materials – structural requirements – form work systems – connections – specifications – design of form work – shores – removal for forms – reshoring – failure of form work.

TEXT BOOKS:

1. Properties of Concrete by A.M. Neville, ELBS publications.
2. Concrete: Micro Structure, Properties and Materials by P.K. Mehta, Tata Mc-Graw Hill Publishing House Pvt. Ltd

REFERENCES:

1. Concrete Technology by M.S. Shetty, S. Chand & Co.
2. Concrete Technology by A. K. Santhakumar, Oxford University Press
3. Special Structural concretes by Rafat Siddique, Galgotia Publications.
4. IS: 10262: 2009 Code of practice for design of Concrete mix

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

TALL BUILDINGS
(PE-1)

Course Objective: To impart knowledge on analysis of tall buildings.

Course Outcomes : The learner will be able to analyse and chose a appropriate systems for tall buildings.

Unit-I

Introduction : Evolution of tall buildings - Classification of Buildings – Low-rise, medium-rise, high-rise — Ordinary framed buildings & Shear-wall buildings –Behaviour of buildings under lateral loads like Wind loads, Earthquake loads & Blast loads – Basic structural & functional design requirements – Strength, Stiffness & Stability.

Unit-II

Lateral load resisting elements : Frames, Shear walls & Tubes – Shear, Bending & combined modes of deformation – Structural behavior of Rigid frames – Simplified methods of analysis – Substitute frame method, Portal method, Cantilever method, Equivalent frame method –Structural behaviour of Shear walls – Approaches of analysis – Elastic continuum approach & Discrete approach -- Structural behavior of Tubes –Actions.

Unit-III

Choice of System for a Building: Frame building, Shear wall building, Shear walls acting with frames, Single framed tubes – Other structural forms – Staggered Wall-beam system, Tube-in-tube system, Base isolation technique for earthquake resistance. Load distribution in a tall building – Load resisted by different shear walls & frames – Determinate & Indeterminate problems – Equivalent Stiffness method.

Unit-IV

Methods of Analysis: Shear walls without Openings – Estimation of Stiffness by simple Cantilever theory & Deep beam theory – Shear walls with Openings – Equivalent frame for large openings – Muto's method for small openings –Elastic Continuum approach – Coull & Chowdhry's method – Design Charts – Limitations of Continuum approach. Shear wall- Frame Interaction: Sharing of loads between wall & frame - Different methods – comparison -- Khan & Sbrounis' method – Design charts - - Mac Leod's method - Advantages & limitations -- Cooperation of Floor slabs – Equivalent width.

Unit-V

Modern Methods: Analysis of Tall buildings by Stiffness method – Available Softwares for analysis of tall buildings.

TEXT BOOKS:

1. Design of Tall Buildings by Taranath B., McGraw Hill.

REFERENCES

1. Reinforced Concrete Design of Tall Buildings by Bungales. Taranath, CRC Press.
2. Analysis of Shear Walled Buildings by S. M. A. Kazimi& R. Chandra, Tor-steel Research Foundation, Calcutta, India.
3. Analysis of Framed Structures by Gere & Weaver
4. Design of Building Structures by Wolfgang Schuller, Prentice Hall

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

**ADVANCED FOUNDATION ENGINEERING
(PE-1)**

Course Objective: To determine the bearing capacity of shallow and deep foundations and to estimate settlements of structures subjected to external loads, leading to design of foundations resting on soils.

Course Outcome: Students should be in a position to design foundations for varieties of structures resting on soil deposits, and appreciate the importance of reliability based design in geotechnical engineering.

Unit-I

Soil Exploration: Exploration Methods; Planning the Exploration Program; Boring and Sampling; In Situ Tests: Standard & Cone Penetration Tests, Field Vane, Dilatometer, Pressure meter; Rock Sampling, Core Recovery, RQD; Geophysical Exploration; Preparation of Soil Report, Case Studies.

Unit-II

Shallow Foundations: Bearing Capacity:- Shear Failure; Effect of Water Table; Footings with Eccentric or Inclined Loads, Footings on Layered Soils, Slopes on finite layer with a Rigid Base at Shallow Depth, effect of compressibility of soil, on soils with strength increasing with depth, Plate Load tests, Presumptive bearing capacity.

Unit-III

Settlement: Components – Immediate, Primary and Secondary Settlements, Consolidation, Stresses and Displacements in Homogeneous, Layered and Anisotropic Soils; Bearing Pressure using SPT, CPT, Dilatometer and Pressure meter; Settlement of foundations on Sands-Schmertmann and Burland & Burbridge methods; Structure Tolerance to Settlement and Differential Settlements, Rotation, Codal Provisions.

Unit-IV

Deep Foundations: Single Pile: Vertically loaded piles, Static capacity- α , β and λ Methods, Dynamic formulae; Wave Equation Analyses; Point Bearing Resistance with SPT and CPT Results; Bearing Resistance of Piles on Rock; Settlement; Pile Load Test; Uplift Resistance; Laterally Loaded Piles -Ultimate Lateral Resistance; Negative Skin Friction; Batter Piles; Under Reamed Piles; Ultimate Capacity of Pile Groups in Compression, Pullout & Lateral Load; Efficiency; Settlements of Pile Groups; Interaction of Axially & Laterally Loaded Pile Groups, Codal Provisions.

Unit-V

Special Topics of Foundation Engineering

Foundations on Collapsible Soils: Origin and occurrence, Identification, Sampling and Testing, Preventive and Remedial measures.

Foundations on Expansive Soils: The nature, origin and occurrence, Identifying, testing and evaluating expansive soils, typical structural distress patterns and Preventive design & construction measures.

***Introduction to Reliability-Based Design:** Brief introduction of probability and statistics, LRFD for structural strength requirements, LRFD for geotechnical strength requirements, Serviceability requirements

TEXT BOOKS

1. Das, B. M. - Principles of Foundation Engineering 5th Edition Nelson Engineering (2004)
2. Donald P Coduto – Foundation Design Principles and Practices, 2nd edition, Pearson, Indian edition, 2012. Phi Learning (2008)

REFERENCE BOOKS

1. Bowles, J. E. - Foundation Analysis & Design 5th Edition McGraw-Hill Companies, Inc. (1996)
2. Poulos, H. G. & Davis, E. H. - Pile Foundation Analysis and Design john Wiley & sons inc(1980-08)
3. Tomlinson, M. J. - Foundation Design and Construction - Prentice Hall (2003).
4. Baecher, G.B. & Christian, J.T. – Reliability and Statistics in Geotechnical Engineering, Wiley Publications (2003)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED REINFORCED CONCRETE DESIGN
(PE-2)

Course Objectives: To impart knowledge on the behavior and design on various reinforced concrete structural elements.

Course Outcomes: The learner will be able to design the reinforced concrete elements like beams, slabs and compression members.

UNIT - I

Basic Design Concepts: Behaviour in flexure, Design of singly reinforced rectangular sections, Design of doubly reinforced rectangular sections, Design of flanged beams, Design of shear, Design for Torsion, Limit state of Serviceability: Deflections of Reinforced concrete beams and slabs, short term deflection and long term deflection, estimation of crack width in RCC members, calculation of crack widths.

UNIT - II

Limit Analysis of R.C. Structures: Rotation of a plastic hinge, Redistribution of moments, moment rotation characteristics of RC member, I.S. code provisions, and applications for fixed and continuous beam. Yield line analysis for slabs: Upper bound and lower bound theorems – yield line criterion – Virtual work and equilibrium methods of analysis for square and circular slabs with simple and continuous end conditions.

UNIT - III

Design of Ribbed slabs, Flat slabs: Analysis of the Slabs for Moment and Shears, Ultimate Moment of Resistance, Design for shear, Deflection, Arrangement of Reinforcements.

Flat slabs: Direct design method – Distribution of moments in column strips and middle strip-moment and shear transfer from slabs to columns – Shear in Flat slabs-Check for one way and two way shears - Introduction to Equivalent frame method. Limitations of Direct design method, Distribution of moments in column strips and middle strip.

UNIT - IV

Design of Reinforced Concrete Deep Beams & Corbels: Steps of Designing Deep Beams, Design by IS 456, Checking for Local Failures, Detailing of Deep Beams, Analysis of Forces in a Corbels, Design of Procedure of Corbels, Design of Nibs.

UNIT - V

Design of Elevated intz type of Water Tank **Design of Combined Footings-** Distribution of soil Pressure – Geometry of Two Column Combined Footing – Design Considerations in Combined Footing for Two – Columns.

TEXT BOOKS:

1. Reinforced concrete design by S. Unnikrishna Pillai & Menon, Tata Mc. Graw Hill, 2nd Edition, 2004
2. Advanced Reinforced Concrete Design – P.C. Varghese, Prentice Hall of India, 2008

REFERENCE BOOKS:

1. Reinforced concrete design by Kenneth Leet, Tata Mc. Graw-Hill International, editions, 2nd edition, 1991.
2. Design of Reinforced concrete structures by N.Subramanian, Oxford University Press
3. Reinforced Concrete Structures by Park and Paulay, John Wiley Publishers.
4. Design of concrete structures – Arthus H. Nilson, David Darwin, and Charles W. Dolar, Tata Mc. Graw-Hill, 3rd Edition, 2005.
5. Limit state theory and design of reinforced concrete by Dr. S.R. Karve and Dr. V.L. Shah, Standard Publishers, Pune, 3rd Edition, 1994.
6. IS : 456 : 2000, Code of Practice for Plane and Reinforced Cement Concrete,
7. SP 16, SP 34.
8. IS 3370 Part I to Part IV.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

**SOIL DYNAMICS AND FOUNDATIONS ENGINEERING
(PE-2)**

Course Objective: To understand the wave propagation in soils, determine dynamic properties of soil for analyzing and designing foundations subjected to vibratory loading.

Course Outcome: Able to understand the fundamentals of wave propagation in soil media, evaluate the dynamic properties of soil, and design foundations for centrifugal and reciprocating machines.

Unit-I

Fundamentals of Vibration: Definitions, Simple harmonic motion, Response of SDOF systems of Free and Forced vibrations with and without viscous damping, Frequency dependent excitation, Systems under transient loads, Rayleigh's method of fundamental frequency, Logarithmic decrement, Determination of viscous damping, Transmissibility, Systems with Two and Multiple degrees of freedom, Vibration measuring instruments.

Unit-II

Wave Propagation and Dynamic Soil Properties: Propagation of seismic waves in soil deposits - Attenuation of stress waves, Stress-strain behaviour of soils under cyclic loads, Strength of cyclically loaded soils, Dynamic soil properties - Laboratory and field testing techniques, Elastic constants of soils, Correlations for shear modulus and damping ratio in sand, gravels, clays and lightly cemented sand. Liquefaction of soils and its evaluation using simple methods.

Unit-III

Vibration Analyses: Types, General Requirements, Permissible amplitude, Allowable soil pressure, Modes of vibration of a rigid foundation block, Methods of analysis, Lumped Mass models, elastic half space method, elasto-dynamics, effect of footing shape on vibratory response, dynamic response of embedded block foundation, Vibration isolation.

Unit-IV

Design of Machine Foundations: Analysis and design of block foundations for reciprocating engines, Dynamic analysis and design procedure for a hammer foundation, IS code of practice design procedure for foundations of reciprocating and impact type machines. Vibration isolation and absorption techniques.

Unit-V

Machine Foundations on Piles: Introduction, Analysis of piles under vertical vibrations, Analysis of piles under translation and rocking, Analysis of piles under torsion, Design procedure for a pile supported machine foundation.

TEXT BOOKS:

1. Swami Saran - Soil Dynamics and Machine Foundation, Galgotia Publications Pvt. Ltd. (2010)
2. Prakash, S. - Soil Dynamics, McGraw Hill Book Company (1981)

REFERENCES:

1. Prakash, S. and Puri, V. K. - Foundation for Machines: Analysis and Design, John Wiley & Sons, 1998.

2. Kameswara Rao, N. S. V. - Vibration Analysis and Foundation Dynamics, Wheeler Publication Ltd., 1998.
3. Das, B. M. & Ramana, G.V. - Principles of Soil Dynamics, 2nd Edition, CL Engineering Publishers, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

**PLASTIC ANALYSIS AND DESIGN
(PE-2)**

Course Objectives: To impart knowledge on the analysis of steel structures like continuous beams, steel frames and connection, using Plastic Analysis.

Course Outcomes: The learner will be able to design continuous beams and steel frames

UNIT – I

Analysis of Structures for Ultimate Load: Fundamental Principles – statical method of Analysis – Mechanism method of analysis – Method of analysis, Moment check – Carry over factor – Moment Balancing Method.

UNIT – II

Design of Continuous Beams: Continuous Beams of uniform section throughout – Continuous Beams with different cross-sections.

UNIT – III

Secondary Design Problems: Introduction – Influence of Axial force on the plastic moment – influence of shear force – local buckling of flanges and webs – lateral buckling – column stability.

UNIT – IV

Design of Connections: Introduction – requirement for connections – straight corner connections – Haunched connection – Interior Beam-Column connections.

UNIT – V

Design of Steel Frames: Introduction – Single bay, single storey frames – simplified procedures for Single span frames – Design of Gable frames with Haunched Connection. Ultimate Deflections: Introduction – Deflection at ultimate load – Deflection at working load – Deflections of Beams and Single span frames.

REFERENCES:

1. Plastic Design of Steel Frames, L.S. Beedle. John Willey & Sons.
2. Plastic Analysis, B.G.Neal. SponPres
3. Design of Steel Structures by N. Subramanian, Oxford University Press

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED STRUCTURAL ENGINEERING LAB

Course Objective: to understand the test procedure and behavior of the concrete and RC beams

Course Outcome: the student will be able to design concrete mix which will satisfy the fresh and hardened concrete properties, and study the behavior of structural elements.

1. Workability of fresh concrete
 - (a) slump core
 - (b) Compaction Factor
 - (c) Vee Bee Test
2. Rapid chloride penetration test of concrete
3. Air Entrainment test on fresh concrete
4. Mash cone test and modulus of elasticity of concrete
5. Permeability of concrete
6. NDT tests on hardened concrete
 - a) Rebound hammer
 - b) UPV hammer
7. Accelerated curing of concrete
8. Design and Testing of self compacting concrete of standard grade (M30 or M40)
 - a) V- Funnel
 - b) L -Box
 - c) U -Box
 - d) J -Ring
9. Testing of RC Beams for pure bending
 - a) Under reinforced
 - b) Over reinforced
10. Testing of RC Beams for shear

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH IN VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN.
EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH**

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Digital System Design	25	75	4	0	0	4
PC-2	Device Modeling	25	75	4	0	0	4
PC-3	CMOS Analog Integrated Circuit Design	25	75	4	0	0	4
PE-1	VLSI Technology Hardware Software Co-Design CPLD and FPGA Architectures and Applications	25	75	3	0	0	3
PE-2	Algorithms for VLSI Design Automation Embedded System Design Advanced Computer Architecture	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Digital IC Design Lab	25	75	0	0	3	2
Seminar I	Seminar - I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Low Power VLSI Design	25	75	4	0	0	4
PC-5	Design for Testability	25	75	4	0	0	4
PC-6	CMOS Mixed Signal Circuit Design	25	75	4	0	0	4
PE-3	VLSI and DSP Architectures Full custom IC Design Verilog Hardware Description Language	25	75	3	0	0	3
PE4	RF IC Design System on Chip Architecture Scripting Languages	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Analog IC Design Lab	25	75	0	0	3	2
Seminar II	Seminar - II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

LOW POWER VLSI DESIGN (PC - 4)

UNIT – I

Fundamentals: Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects – Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.

UNIT – II

Low-Power Design Approaches: Low-Power Design through Voltage Scaling – VTCMOS circuits, MTCMOS circuits, Architectural Level Approach – Pipelining and Parallel Processing Approaches.

Switched Capacitance Minimization Approaches: System Level Measures, Circuit Level Measures, Mask level Measures.

UNIT – III

Low-Voltage Low-Power Adders: Introduction, Standard Adder Cells, CMOS Adder's Architectures – Ripple Carry Adders, Carry Look-Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques – Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

UNIT – IV

Low-Voltage Low-Power Multipliers: Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh-Wooley Multiplier, Booth Multiplier, Introduction to Wallace Tree Multiplier.

UNIT – V

Low-Voltage Low-Power Memories: Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Precharge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

TEXT BOOKS:

1. Sung-Mo Kang, Yusuf Leblebici, "CMOS Digital Integrated Circuits – Analysis and Design", TMH, 2011.
2. Kiat-Seng Yeo, Kaushik Roy, "Low-Voltage, Low-Power VLSI Subsystems", TMH Professional Engineering.

REFERENCE BOOKS:

1. Ming-BO Lin, "Introduction to VLSI Systems: A Logic, Circuit and System Perspective", CRC Press, 2011
2. Anantha Chandrakasan, "Low Power CMOS Design", IEEE Press/Wiley International, 1998.
3. Kaushik Roy, Sharat C. Prasad, "Low Power CMOS VLSI Circuit Design", John Wiley & Sons, 2000.
4. Gary K. Yeap, "Practical Low Power Digital VLSI Design", Kluwer Academic Press, 2002.
5. A. Bellamour, M. I. Elamasri, "Low Power CMOS VLSI Circuit Design", Kluwer Academic Press, 1995.
6. Siva G. Narendran, Anatha Chandrakasan, "Leakage in Nanometer CMOS Technologies" Springer, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

DESIGN FOR TESTABILITY (PC - 5)

UNIT - I

Introduction to Testing: Testing Philosophy, Role of Testing, Digital and Analog VLSI Testing, VLSI Technology Trends affecting Testing, Types of Testing, Fault Modeling: Defects, Errors and Faults, Functional Versus Structural Testing, Levels of Fault Models, Single Stuck-at Fault.

UNIT - II

Logic and Fault Simulation: Simulation for Design Verification and Test Evaluation, Modeling Circuits for Simulation, Algorithms for True-value Simulation, Algorithms for Fault Simulation, ATPG.

UNIT - III

Testability Measures: SCOAP Controllability and Observability, High Level Testability Measures, Digital DFT and Scan Design: Ad-Hoc DFT Methods, Scan Design, Partial-Scan Design, Variations of Scan.

UNIT - IV

Built-In Self-Test: The Economic Case for BIST, Random Logic BIST: Definitions, BIST Process, Pattern Generation, Response Compaction, Built-In Logic Block Observers, Test-Per-Clock, Test-Per-Scan BIST Systems, Circular Self Test Path System, Memory BIST, Delay Fault BIST.

UNIT - V

Boundary Scan Standard: Motivation, System Configuration with Boundary Scan: TAP Controller and Port, Boundary Scan Test Instructions, Pin Constraints of the Standard, Boundary Scan Description Language: BSDL Description Components, Pin Descriptions.

TEXT BOOK:

1. M.L. Bushnell, V. D. Agrawal, "Essentials of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits" Kluwer Academic Publishers.

REFERENCE BOOKS:

1. M. Abramovici, M. A. Breuer and A.D Friedman, "Digital Systems and Testable Design", Jaico Publishing House.
2. P.K. Lala, "Digital Circuits Testing and Testability", Academic Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

CMOS MIXED SIGNAL CIRCUIT DESIGN (PC - 6)

UNIT - I

Switched Capacitor Circuits: Introduction to Switched Capacitor circuits- basic building blocks, Operation and Analysis, Non-ideal effects in switched capacitor circuits, Switched capacitor integrators first order filters, Switch sharing, biquad filters.

UNIT - II

Phased Lock Loop (PLL): Basic PLL topology, Dynamics of simple PLL, Charge pump PLLs-Lock acquisition, Phase/Frequency detector and charge pump, Basic charge pump PLL, Non-ideal effects in PLLs-PFD/CP non-idealities, Jitter in PLLs, Delay locked loops, applications

UNIT - III

Data Converter Fundamentals: DC and dynamic specifications, Quantization noise, Nyquist rate D/A converters- Decoder based converters, Binary-Scaled converters, Thermometer-code converters, Hybrid converters

UNIT - IV

Nyquist Rate A/D Converters: Successive approximation converters, Flash converter, Two-step A/D converters, Interpolating A/D converters, Folding A/D converters, Pipelined A/D converters, Time-interleaved converters.

UNIT - V

Oversampling Converters: Noise shaping modulators, Decimating filters and interpolating filters, Higher order modulators, Delta sigma modulators with multibit quantizers, Delta sigma D/A

TEXT BOOKS:

1. Behzad Razavi, "Design of Analog CMOS Integrated Circuits", TMH Edition, 2002
2. Philip E. Allen and Douglas R. Holberg, "CMOS Analog Circuit Design", Oxford University Press, International Second Edition/Indian Edition, 2010.
3. David A. Johns, Ken Martin, "Analog Integrated Circuit Design", Wiley Student Edition, 2013

REFERENCE BOOKS:

1. Rudy Van De Plassche, "CMOS Integrated Analog-to- Digital and Digital-to-Analog converters", Kluwer Academic Publishers, 2003
2. Richard Schreier, "Understanding Delta-Sigma Data converters", Wiley Interscience, 2005.
3. R. Jacob Baker, "CMOS Mixed-Signal Circuit Design", Wiley Interscience, 2009.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

VLSI AND DSP ARCHITECTURES (PE - 3)

UNIT - I

Essential feature of Instruction set architectures of CISC, RISC and DSP processors and their implications for implementation as VLSI Chips, Micro programming approaches for implementation of control part of the processor. Assessing understanding performance, Introduction, CPU performance and its factors, evaluating performance, real stuff: Two spec bench marks and performance of recent INTEL processors, fallacies and pitfalls

UNIT - II

Data Path and Control: Introduction, logic design conventions, building a data path, a simple implementation scheme, a multi cycle implementation, exceptions, micro programming: simplifying control design, an introduction to digital design using hardware description language, fallacies and pitfalls

UNIT - III

Enhancing performance with pipeline: An overview of pipelining, a pipe lined data path. Pipe lined control, data hazards and forwarding, data hazards and stalls, branch hazards using a hard ware description language to describe and model a pipe line, exceptions, advanced pipelining: extracting more performance, fallacies and pitfalls

UNIT - IV

Computational Accuracy in DSP implementations: Introduction, number formats for signals and coefficients in DSP system, dynamic range and precision, sources of errors in DSP implementations, A/D conversion errors, DSP computational errors, D /A conversion errors

UNIT - V

Architectures for programmable digital signal processing devices: Introduction, basic architectural features, DSP Computational building blocks, bus architecture and memory, data addressing capabilities, address generation unit, programmability and program execution, speed issues, features for external interfacing.

TEXT BOOKS:

1. D. A, Patterson and J.L Hennessy, "Computer Organization and Design: Hardware/ Software Interface", 4th Ed., Elsevier, 2011
2. A. S Tannenbaum, "Structural Computer organization", 4th Ed., Prentice-Hall, 1999

REFERENCE BOOKS:

1. W. Wolf, "Modern VLSI Design: System on Silicon", 2nd Ed., Person Education, 1998
2. Keshab Parhi, "VLSI Digital Signal Processing system design and implementations", Wiley 1999
3. Avatar sign, Srinivasan S, "Digital Signal Processing implementations using DSP microprocessors with examples", Thomson 4th reprint, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

FULL CUSTOM IC DESIGN (PE - 3)

UNIT - I

Introduction: Schematic fundamentals, Layout design, Introduction to CMOS VLSI manufacturing processes, Layers and connectivity, Process design rules Significance of full custom IC design, layout design flows.

UNIT - II

Advanced techniques for specialized building blocks Standard cell libraries, Pad cells and Laser fuse cells, Advanced techniques for building blocks, Power grid Clock signals

UNIT - III

Interconnect routing. Interconnect layout design, Special electrical requirements, Layout design techniques to address electrical characteristics.

UNIT - IV

Layout considerations due to process constraints Large metal via implementations, Step coverage rules, Special design rules, Latch-up and Guard rings, Constructing the pad ring, Minimizing Stress effects.

UNIT - V

Proper layout CAD tools for layout, Planning tools, Layout generation tools, Support tools.

TEXT BOOKS:

1. Dan Clein, "CMOS IC Layout Concepts Methodologies and Tools", Newnes, 2000.
2. Ray Alan Hastings, "The Art of Analog Layout", 2nd Edition, Prentice Hall, 2006

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

VERILOG HARDWARE DESCRIPTION LANGUAGE (PE - 3)

UNIT - I

Introduction to Verilog HDL: Verilog as HDL, Levels of Design Description, Concurrency, Simulation and Synthesis, Function Verification, Systems tasks, programming language interface, Module, Simulation and Synthesis tools.

Language Constructs and Conventions: Introduction, Keywords, Identifiers, White Space Characters, Comments, Numbers, Strings, Logic values, Strengths, Data types, Scalars and Vectors, Parameters, Operators.

UNIT - II

Gate Level Modeling: Introduction, AND Gate Primitive, Module, Structure, Other Gate Primitives, Illustrative Examples, Tristate Gates, Array of Instances of Primitives, Design of Flip – Flops with Gate Primitives, Delays, Strengths and Construction Resolution, Net Types, Design of Basic Circuit.

Modeling at Dataflow Level: Introduction, Continuous Assignment Structure, Delays and Continuous Assignments Assignment to Vectors, Operators.

UNIT - III

Behavioral Modeling: Introduction, Operations and Assignments, Functional Bifurcation, Initial Construct, Always Construct, Assignments with Delays, Wait Construct, Multiple Always Block, Designs at Behavioral Level, Blocking and Non-Blocking Assignments, The Case Statement, Simulation Flow if an if-Else Constructs, Assign- De-Assign Construct, Repeat Construct, for Loop, the Disable Construct, While Loop, For Ever Loop, Parallel Blocks, Force-Release, Construct, Event.

UNIT - IV

Switch Level Modeling: Basic Transistor Switches, CMOS Switches, Bi Directional Gates, Time Delays with Switch Primitives, Instantiation with Strengths and Delays, Strength Contention with Trireg Nets.

System Tasks, Functions and Compiler Directives: Parameters, Path Delays, Module Parameters, System Tasks and Functions, File Based Tasks and Functions, Computer Directives, Hierarchical Access, User Defined Primitives.

UNIT - V

Sequential Circuit Description: Sequential Models – Feedback Model, Capacitive Model, Implicit Model, Basic Memory Components, Functional Register, Static Machine Coding, Sequential Synthesis.

Component Test and Verification: Test Bench-Combinational Circuit Testing, Sequential Circuit Testing, Test Bench Techniques, Design Verification, Assertion Verification

TEXT BOOKS:

1. T R Padmanabhan, B.Bala Tripura Sundari, Design Through Verilog HDL,2009, Wiley.
2. Zainalabdien Navabi, Verilog Digital System Design, TMH,2nd Edition,

REFERENCES:

1. Stephen Brown, Zvonkoc Vranesic, "Fundamentals of Digital Logic with Verilog Design", 2nd Edition, 2010, TMH
2. Sunggu Lee, " Digital Logic Design using Verilog, State Machine & Synthesis for FPGA," Cengage Learning 2009
3. Verilog HDL – Samir Palnitkar, 2nd Edition, Pearson Education, 2009.
4. Advanced Digital Design with verilog HDL – Michel D.Ciletti, PHI,2009

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

RF IC DESIGN (PE - 4)

UNIT – I: Introduction to RF and Wireless Technology: Complexity comparison, Design bottle necks, Applications, Analog and digital systems, Choice of Technology.

UNIT – II: Basic concepts in RF Design: Nonlinearity and time variance, ISI, Random process and noise, sensitivity and dynamic range, passive impedance transformation.

UNIT – III: Multiple Access: Techniques and wireless standards, mobile RF communication, FDMA, TDMA, CDMA, Wireless standards.
Transceiver Architectures: General considerations, receiver architecture, Transmitter Architecture, transceiver performance tests, case studies.

UNIT – IV: Amplifiers, Mixers and Oscillators: LNAs, down conversion mixers, Cascaded Stages, oscillators, Frequency synthesizers.

UNIT – V: Power Amplifiers: General considerations, linear and nonlinear PAs, classification, High Frequency power amplifier, large signal impedance matching, linearization techniques.

TEXT BOOKS:

1. Behzad Razavi, RF Microelectronics Prentice Hall of India, 2001.
2. Thomas H. Lee, The Design of CMOS Radio Integrated Circuits, Cambridge University Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

SYSTEM ON CHIP ARCHITECTURE (PE - 4)

UNIT – I

Introduction to the System Approach: System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory, and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT – II

Processors: Introduction , Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT – III

Memory Design for SOC: Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation , SOC Memory System, Models of Simple Processor – memory interaction.

UNIT - IV

Interconnect Customization and Configuration: Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses , Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance-Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT – V

Application Studies / Case Studies: SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

TEXT BOOKS:

1. Michael J. Flynn and Wayne Luk, “Computer System Design System-on-Chip”, Wiley India Pvt. Ltd.
2. Steve Furber, “ARM System on Chip Architecture”, 2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Ricardo Reis, “Design of System on a Chip: Devices and Components”, 1st Ed., 2004, Springer
2. Jason Andrews, “Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology)”, Newnes, BK and CDROM.
3. Prakash Rashinkar, Peter Paterson and Leena Singh L, “System on Chip Verification – Methodologies and Techniques”, 2001, Kluwer Academic Publishers.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

SCRIPTING LANGUAGES (PE - 4)

UNIT - I

Introduction to Scripts and Scripting: Characteristics and uses of scripting languages, Introduction to PERL, Names and values, Variables and assignment, Scalar expressions, Control structures, Built-in functions, Collections of Data, Working with arrays, Lists and hashes, Simple input and output, Strings, Patterns and regular expressions, Subroutines, Scripts with arguments.

UNIT - II

Advanced PERL: Finer points of Looping, Subroutines, Using Pack and Unpack, Working with files, Navigating the file system, Type globs, Eval, References, Data structures, Packages, Libraries and modules, Objects, Objects and modules in action, Tied variables, Interfacing to the operating systems, Security issues.

UNIT - III

TCL: The TCL phenomena, Philosophy, Structure, Syntax, Parser, Variables and data in TCL, Control flow, Data structures, Simple input/output, Procedures, Working with Strings, Patterns, Files and Pipes, Example code.

UNIT - IV

Advanced TCL: The eval, source, exec and up-level commands, Libraries and packages, Namespaces, Trapping errors, Event-driven programs, Making applications 'Internet-aware', 'Nuts-and-bolts' internet programming, Security issues, running untrusted code, The C interface.

UNIT - V

TK and JavaScript: Visual tool kits, Fundamental concepts of TK, TK by example, Events and bindings, Geometry managers, PERL-TK.

JavaScript – Object models, Design Philosophy, Versions of JavaScript, The Java Script core language, Basic concepts of Python.

Object Oriented Programming Concepts (Qualitative Concepts Only): Objects, Classes, Encapsulation, Data Hierarchy.

TEXT BOOKS:

1. David Barron, "The World of Scripting Languages", Wiley Student Edition, 2010.
2. Brent Welch, Ken Jones and Jeff Hobbs, "Practical Programming in Tcl and Tk", Fourth edition.
3. Herbert Schildt, "Java the Complete Reference", 7th Edition, TMH.

REFERENCE BOOKS:

1. Clif Flynt, "Tcl/Tk: A Developer's Guide", 2003, Morgan Kaufmann Series.
2. John Ousterhout, "Tcl and the Tk Toolkit", 2nd Edition, 2009, Kindel Edition.
3. Wojciech Kocjan and Piotr Beltowski, "Tcl 8.5 Network Programming book", Packt Publishing.
4. Bert Wheeler, "Tcl/Tk 8.5 Programming Cookbook", 2011, Packt Publishing Limited.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. – I YEAR – II SEMESTER VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

ANALOG IC DESIGN LAB

List of Experiments:

1. Lambda calculation for PMOS and NMOS
2. Transconductance plot
3. Ideal Current source PMOS & NMOS
4. NMOS saturated load
5. Single transistor amplifier
6. Cascade amplifier
7. Wilson current mirror
8. Cascade current mirror
9. Cascode current mirror
10. Regulated Cascade current mirror

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH IN VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN.
EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH**

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Advanced Digital System Design	25	75	4	0	0	4
PC-2	Device Modeling	25	75	4	0	0	4
PC-3	CMOS Analog Integrated Circuit Design	25	75	4	0	0	4
PE-1	VLSI Technology Hardware Software Co-Design CPLD and FPGA Architectures and Applications	25	75	3	0	0	3
PE-2	Algorithms for VLSI Design Automation Embedded System Design Advanced Computer Architecture	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Digital IC Design Lab	25	75	0	0	3	2
Seminar I	Seminar	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Low Power VLSI Design	25	75	4	0	0	4
PC-5	Design for Testability	25	75	4	0	0	4
PC-6	CMOS Mixed Signal Circuit Design	25	75	4	0	0	4
PE-3	VLSI and DSP Architectures Full custom IC Design Verilog Hardware Description Language	25	75	3	0	0	3
PE4	RF IC Design System On Chip Architecture Scripting Languages	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Analog IC Design Lab	25	75	0	0	3	2
Seminar II	Seminar	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

ADVANCED DIGITAL SYSTEM DESIGN (PC-1)

UNIT - I

Processor Arithmetic: Two's Complement Number System - Arithmetic Operations; Fixed point Number System; Floating Point Number system - IEEE 754 format, Basic binary codes.

UNIT - II

Combinational circuits: CMOS logic design, Static and dynamic analysis of Combinational circuits, timing hazards. Functional blocks - Decoders, Encoders, Three-state devices, Multiplexers, Parity circuits, Comparators, Adders, Subtractors, Carrylook-ahead adder – timing analysis. Combinational multiplier structures.

UNIT - III

Sequential Logic: Latches and Flip-Flops, Sequential logic circuits - timing analysis (Set up and hold times), State machines - Mealy & Moore machines, Analysis, FSM design using D Flip-Flops, FSM optimization and partitioning; Synchronizers and metastability. FSM Design examples: Vending machine, Traffic light controller, Washing machine.

UNIT - IV

Subsystem Design using Functional Blocks (1): Design (including Timing Analysis) of different logical blocks of varying complexities involving mostly combinational circuits:

- ALU
- 4-bit combinational multiplier
- Barrel shifter
- Simple fixed point to floating point encoder
- Dual Priority encoder
- Cascading comparators

UNIT - V

Subsystem Design using Functional Blocks (2): Design, (including Timing Analysis) of different logical blocks of different complexities involving mostly sequential circuits:

- Pattern (sequence) detector
- Programmable Up-down counter
- Round robin arbiter with 3 requesters
- Process Controller
- FIFO

TEXT BOOKS:

1. John F. Wakerly, "Digital Design", Prentice Hall, 3rd Edition, 2002

*Note1: VHDL and ABEL are not part of this course.

*Note2: SSI & MSI ICs listed in data books are not part of this course.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

DEVICE MODELING (PC-2)

UNIT - I

MOS Capacitor: Energy band diagram of Metal-Oxide-Semiconductor contacts, Mode of Operations: Accumulation, Depletion, Mid gap, and Inversion, 1D Electrostatics of MOS, Depletion Approximation, Accurate Solution of Poisson's Equation.

UNIT - II

MOS Capacitor Characteristics and Non idealities: CV characteristics of MOS, LFCV and HFCV, Non-idealities in MOS, oxide fixed charges, interfacial charges.

UNIT - III

The MOS transistor: Small signal modeling for low frequency and High frequency, Pao-Sah and Brews models; Short channel effects in MOS transistors.

UNIT - IV

The bipolar transistor: Eber's-Moll model; charge control model; small-signal models for low and high frequency and switching characteristics.

UNIT - V

FinFETs: I-V characteristics, device capacitances, parasitic effects of extension regions, performance of simple combinational gates and amplifiers, novel circuits using FinFETs and GAA devices.

TEXT BOOKS:

1. S. M. Sze, "Physics of Semiconductor Devices", 2nd Edition, Wiley Eastern, 1981.
2. Y. P. Tsividis, "Operation and Modelling of the MOS Transistor", McGraw-Hill, 1987.
3. E. Takeda, "Hot-carrier Effects in MOS Transistors", Academic Press, 1995.
4. P. Colinge, "FinFETs and Other Multi-Gate Transistors", Springer. 2009

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

CMOS ANALOG INTEGRATED CIRCUIT DESIGN (PC-3)

UNIT -I

MOS Devices and Modeling: The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

UNIT -II

Analog CMOS Sub-Circuits: MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

UNIT -III

CMOS Amplifiers: Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

UNIT -IV

CMOS Operational Amplifiers: Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.

UNIT -V

Comparators: Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

TEXT BOOKS:

1. Philip E. Allen and Douglas R. Holberg, "CMOS Analog Circuit Design", Oxford University Press, International 2nd Edition/Indian Edition, 2010.
2. Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, "Analysis and Design of Analog Integrated Circuits", Wiley India, 5th Edition, 2010.

REFERENCE BOOKS:

1. David A. Johns, Ken Martin, "Analog Integrated Circuit Design", Wiley Student Edition, 2013.
2. Behzad Razavi, "Design of Analog CMOS Integrated Circuits", TMH Edition.
3. Baker, Li and Boyce, "CMOS: Circuit Design, Layout and Simulation", PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

VLSI TECHNOLOGY (PE-1)

UNIT –I

Review of Microelectronics and Introduction to MOS Technologies: MOS, CMOS, BiCMOS Technology. Basic Electrical Properties of MOS, CMOS & BiCMOS Circuits: $I_{ds} - V_{ds}$ relationships, Threshold Voltage V_T , G_m , G_{ds} and ω_o , Pass Transistor, MOS, CMOS & Bi CMOS Inverters, Z_{pu}/Z_{pd} , MOS Transistor circuit model, Latch-up in CMOS circuits.

UNIT –II

Layout Design and Tools: Transistor structures, Wires and Vias, Scalable Design rules, Layout Design tools.

Logic Gates & Layouts: Static Complementary Gates, Switch Logic, Alternative Gate circuits, Low power gates, Resistive and Inductive interconnect delays.

UNIT –III

Overview of semiconductor industry, Stages of Manufacturing, Process and product trends, Crystal growth, Basic wafer fabrication operations, process yields, Semiconductor material preparation, Basic wafer fabrication operations, Yield measurement, Contamination sources, Clean room construction, Oxidation and Photolithography, Doping and Depositions, Metallization. Ten step patterning process, Photoresists, physical properties of photoresists, Storage and control of photoresists, photo masking process, Hard bake, develop inspect, Dry etching Wet etching, resist stripping

UNIT –IV

Doping and depositions: Diffusion process steps, deposition, Drive-in oxidation, Ion implantation-1, Ion implantation-2, CVD basics, CVD process steps, Low pressure CVD systems, Plasma enhanced CVD systems, Vapour phase epitaxy, molecular beam epitaxy.

UNIT –V

Design rules and Scaling, BiCMOS ICs: Choice of transistor types, pnp transistors, Resistors, capacitors,
Packaging: Chip characteristics, package functions, package operations

TEXT BOOKS:

1. Peter Van Zant, "Microchip fabrication", McGraw Hill, 1997.
2. C.Y. Chang and S.M. Sze, "ULSI technology", McGraw Hill, 2000

REFERENCE BOOKS:

1. Muhammad H Rashid, "Micro Electronics circuits Analysis and Design", 2nd Edition, CENAGE Learning, 2011.
2. Eugene D. Fabricius, "Introduction to VLSI design", McGraw Hill, 1999
3. Wani-Kai Chen (editor), "The VLSI Hand book", CRI/IEEE press, 2000
4. S.K. Gandhi, "VLSI Fabrication principles", John Wiley and Sons, NY, 1994

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

HARDWARE SOFTWARE CO-DESIGN (PE-1)

UNIT –I

Co- Design Issues: Co- Design Models, Architectures, Languages, A Generic Co-design Methodology.

Co- Synthesis Algorithms: Hardware software synthesis algorithms: hardware – software partitioning distributed system co-synthesis.

UNIT –II

Prototyping and Emulation: Prototyping and emulation techniques, prototyping and emulation environments, future developments in emulation and prototyping architecture specialization techniques, system communication infrastructure.

Target Architectures: Architecture Specialization techniques, System Communication infrastructure, Target Architecture and Application System classes, Architecture for control dominated systems (8051-Architectures for High performance control), Architecture for Data dominated systems (ADSP21060, TMS320C60), Mixed Systems.

UNIT –III

Compilation Techniques and Tools for Embedded Processor Architectures: Modern embedded architectures, embedded software development needs, compilation technologies, practical consideration in a compiler development environment.

UNIT –IV

Design Specification and Verification: Design, co-design, the co-design computational model, concurrency coordinating concurrent computations, interfacing components, design verification, implementation verification, verification tools, interface verification

UNIT –V

Languages for System – Level Specification and Design-I: System – level specification, design representation for system level synthesis, system level specification languages,

Languages for System – Level Specification and Design-II: Heterogeneous specifications and multi-language co-simulation, the cosyma system and lycos system.

TEXT BOOKS:

1. Jorgen Staunstrup, "Hardware / Software Co- Design Principles and Practice", Wayne Wolf – 2009, Springer.
2. Giovanni De Micheli, Mariagiovanna Sami, "Hardware / Software Co- Design", 2002, Kluwer Academic Publishers

REFERENCE BOOKS:

1. Patrick R. Schaumont, "A Practical Introduction to Hardware/Software Co-design", 2010, Springer

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

CPLD AND FPGA ARCHITECTURES AND APPLICATIONS (PE-1)

UNIT-I

Introduction to Programmable Logic Devices: Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation.

UNIT-II

Field Programmable Gate Arrays: Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, Applications of FPGAs.

UNIT -III

SRAM Programmable FPGAs: Introduction, Programming Technology, Device Architecture, The Xilinx XC2000, XC3000 and XC4000 Architectures.

UNIT -IV

Anti-Fuse Programmed FPGAs: Introduction, Programming Technology, Device Architecture, The Actel ACT1, ACT2 and ACT3 Architectures.

UNIT -V

Design Applications: General Design Issues, Counter Examples, A Fast Video Controller, A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.

TEXT BOOKS:

1. Stephen M. Trimberger, "Field Programmable Gate Array Technology", Springer International Edition.
2. Charles H. Roth Jr, Lizy Kurian John, "Digital Systems Design", Cengage Learning.

REFERENCE BOOKS:

1. John V. Oldfield, Richard C. Dorf, "Field Programmable Gate Arrays", Wiley India.
2. Pak K. Chan/Samiha Mourad, "Digital Design Using Field Programmable Gate Arrays", Pearson Low Price Edition.
3. Ian Grout, "Digital Systems Design with FPGAs and CPLDs", Elsevier, Newnes.
4. Wayne Wolf, "Modern Semiconductor Design Series", Prentice Hall.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

ALGORITHMS FOR VLSI DESIGN AUTOMATION (PE-2)

UNIT- I

PRELIMINARIES: Introduction to Design Methodologies, Design Automation tools, Algorithmic Graph Theory, Computational complexity, Tractable and Intractable problems.

UNIT -II

GENERAL PURPOSE METHODS FOR COMBINATIONAL OPTIMIZATION: Backtracking, Branch and Bound, Dynamic Programming, Integer Linear Programming, Local Search, Simulated Annealing, Tabu search, Genetic Algorithms.

UNIT- III

LAYOUT COMPACTION, PLACEMENT, FLOOR PLANNING AND ROUTING: Problems, Concepts and Algorithms.

MODELLING AND SIMULATION: Gate Level Modelling and Simulation, Switch level Modelling and Simulation.

UNIT -IV

LOGIC SYNTHESIS AND VERIFICATION: Basic issues and Terminology, Binary-Decision diagrams, Two-Level logic Synthesis.

HIGH-LEVEL SYNTHESIS: Hardware Models, Internal representation of the input Algorithm, Allocation, Assignment and Scheduling, Some Scheduling Algorithms, Some aspects of Assignment problem, High-level Transformations.

UNIT- V

PHYSICAL DESIGN AUTOMATION OF FPGAs: FPGA technologies, Physical Design cycle for FPGAs, partitioning and Routing for segmented and staggered Models.

PHYSICAL DESIGN AUTOMATION OF MCMs : MCM technologies, MCM physical design cycle, Partitioning, Placement - Chip Array based and Full Custom Approaches, Routing – Maze routing, Multiple stage routing, Topologic routing, Integrated Pin – Distribution and routing, Routing and Programmable MCMs.

TEXT BOOKS:

1. S.H. Gerez, "Algorithms for VLSI Design Automation", 1999, WILEY Student Edition, John Wiley & Sons (Asia) Pvt. Ltd.
2. Naveed Sherwani, "Algorithms for VLSI Physical Design Automation", 3rd Edition, 2005, Springer International Edition.

REFERENCE BOOKS:

1. Hill & Peterson, "Computer Aided Logical Design with Emphasis on VLSI", 1993, Wiley.
2. Wayne Wolf, "Modern VLSI Design: Systems on silicon", 2nd ed., 1998, Pearson Education Asia.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

EMBEDDED SYSTEM DESIGN (PE-2)

UNIT -I

Introduction to Embedded Systems: Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT -II

Typical Embedded System: Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT -III

Embedded Firmware: Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT -IV

RTOS Based Embedded System Design: Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT -V

Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOKS:

1. Shibu K.V, "Introduction to Embedded Systems", McGraw Hill.

REFERENCE BOOKS:

1. Raj Kamal, "Embedded Systems", TMH.
2. Frank Vahid, Tony Givargis, "Embedded System Design", John Wiley.
3. Lyla, "Embedded Systems", Pearson, 2013
4. David E. Simon, "An Embedded Software Primer", Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

ADVANCED COMPUTER ARCHITECTURE (PE-2)

UNIT- I

Fundamentals of Computer Design: Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, measuring and reporting performance, quantitative principles of computer design, Amdahl's law.

Instruction set principles and examples- Introduction, classifying instruction set- memory addressing-type and size of operands, operations in the instruction set.

UNIT – II

Pipelines: Introduction ,basic RISC instruction set ,Simple implementation of RISC instruction set, Classic five stage pipe line for RISC processor, Basic performance issues in pipelining , Pipeline hazards, Reducing pipeline branch penalties.

Memory Hierarchy Design: Introduction, review of ABC of cache, Cache performance , Reducing cache miss penalty, Virtual memory.

UNIT - III

Instruction Level Parallelism the Hardware Approach: Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, high performance instruction delivery- hardware based speculation.

ILP Software Approach: Basic compiler level techniques, static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues -Hardware verses Software.

UNIT – IV

Multi Processors and Thread Level Parallelism: Multi Processors and Thread level Parallelism-Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – memory architecture, Synchronization.

UNIT – V

Inter Connection and Networks: Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.

Intel Architecture: Intel IA- 64 ILP in embedded and mobile markets Fallacies and pit falls

TEXT BOOKS:

1. John L. Hennessy, David A. Patterson, "Computer Architecture: A Quantitative Approach", 3rd Edition, An Imprint of Elsevier.

REFERENCE BOOKS:

1. John P. Shen and Miikko H. Lipasti, "Modern Processor Design : Fundamentals of Super Scalar Processors",
2. Kai Hwang, Faye A.Brigs., "Computer Architecture and Parallel Processing", McGraw Hill.,
3. DezsoSima, Terence Fountain, Peter Kacsuk , "Advanced Computer Architecture - A Design Space Approach", Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. – I YEAR – I SEMESTER
VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN**

DIGITAL IC DESIGN LAB

Part –I

Programming can be done using any compiler. Down load the programs on FPGA/CPLD boards and performance testing may be done using pattern generator (32 channels) and logic analyzer apart from verification by simulation with any of the front end tools.

1. HDL code to realize all the logic gates
2. Design and Simulation of adder, Serial Binary Adder, Multi Precession Adder, Carry
3. Look Ahead Adder.
4. Design of 2-to-4 decoder
5. Design of 8-to-3 encoder (without and with parity)
6. Design of 8-to-1 multiplexer
7. Design of 4 bit binary to gray converter
8. Design of Multiplexer/ Demultiplexer, comparator
9. Design of Full adder using 3 modeling styles
10. Design of flip flops: SR, D, JK, T
11. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset) or any sequence counter
12. Design of a N- bit Register of Serial- in Serial –out, Serial in parallel out, Parallel in
13. Serial out and Parallel in Parallel Out.
14. Design of Sequence Detector (Finite State Machine- Mealy and Moore Machines).
15. Design of 4- Bit Multiplier, Divider.
16. Design of ALU to Perform – ADD, SUB, AND-OR, 1's and 2's Compliment,
17. Multiplication, and Division.
18. Design of Finite State Machine.
19. Implementing the above designs on Xilinx/Altera/Cypress/equivalent based FPGA/CPLD kits

Part –II

1. Static and Dynamic Characteristics of CMOS Inverter
2. Implementation of EX-OR gate using complementary CMOS, Psedo-NMOS, Dynamic and domino logic style
3. Implementation of Full Adder using Transmission Gates

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. IN WIRELESS AND MOBILE COMMUNICATIONS.
EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH**

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Wireless Communications & Networks	25	75	4	0	0	4
PC-2	Random Processes and Time Series Analysis	25	75	4	0	0	4
PC-3	Advanced Data Communications	25	75	4	0	0	4
PE-1	Detection and Estimation Theory Radio Navigational Aids Coding Theory and Techniques	25	75	3	0	0	3
PE-2	Voice over Internet Protocol Queueing Theory and Applications TCP/IP Internetworking	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Wireless Communications and Networks Lab	25	75	0	0	3	2
Seminar I	Seminar - I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Advanced Communication Systems	25	75	4	0	0	4
PC-5	Spread Spectrum Communications	25	75	4	0	0	4
PC-6	Adhoc Wireless Networks	25	75	4	0	0	4
PE-3	Optical Communications and Networks Wireless LANs and PANs Wireless Sensor Networks	25	75	3	0	0	3
PE4	Network Security and Cryptography Software Defined Radio 3G Networks	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Advanced Communications Lab	25	75	0	0	3	2
Seminar II	Seminar - II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

ADVANCED COMMUNICATION SYSTEMS (PC - 4)

UNIT - I

Spread Spectrum Communications: Spreading sequences- Properties of Spreading Sequences, Pseudo- noise sequence, Gold sequences, Kasami sequences, Walsh Sequences, Orthogonal Variable Spreading Factor Sequences, Barker Sequence, Complementary Codes
Direct sequence spread spectrum – DS-CDMA Model, Conventional receiver, Rake Receiver, Synchronization in CDMA, Power Control, Soft handoff, Multiuser detection – Optimum multiuser detector, Liner multiuser detection.

UNIT - II

Orthogonal Frequency Division Multiplexing: Basic Principles of Orthogonality, Single vs Multicarrier Systems, OFDM Block Diagram and Its Explanation, OFDM Signal Mathematical Representation, Selection parameter for Modulation, Pulse shaping in OFDM Signal and Spectral Efficiency, Window in OFDM Signal and Spectrum, Synchronization in OFDM, Pilot Insert in OFDM Transmission and Channel Estimation, Amplitude Limitations in OFDM, FFT Point Selection Constraints in OFDM, CDMA vs OFDM, Hybrid OFDM.

UNIT - III

MIMO Systems: Introduction, Space Diversity and System Based on Space Diversity, Smart Antenna system and MIMO, MIMO Based System Architecture, MIMO Exploits Multipath, Space – Time Processing, Antenna Consideration for MIMO, MIMO Channel Modelling, MIMO Channel Measurement, MIMO Channel Capacity, Cyclic Delay Diversity (CDD), Space Time Coding, Advantages and Applications of MIMO in Present Context, MIMO Applications in 3G Wireless System and Beyond, MIMO-OFDM

UNIT - IV

SONET/SDH: Architecture, SONET Layers, SONET Frames, STS Multiplexing, SONET Networks, Virtual Tributaries.

ATM: Overview, Virtual channels, Virtual paths, VP and VC switching, ATM cells, Header format, Generic flow control, Header error control, Transmission of ATM cells, Adaptation layer, AAL services and protocols.

UNIT - V

ATM Traffic and congestion Control: Requirements for ATM Traffic and Congestion Control, Cell-Delay Variation, ATM Service Categories, Traffic and Congestion Control Framework, Traffic Control, Congestion Control

TEXT BOOKS:

1. Gary J. Mullett, "Introduction to Wireless Telecommunications Systems and Networks", CENGAGE
2. Upena Dalal, "Wireless Communication", Oxford University Press, 2009
3. William Stallings, "ISDN and Broadband ISDN with Frame Relay and ATM" Prentice Hall, 4th edition

REFERENCE BOOKS:

1. Ke-Lin Du & M N S Swamy, "Wireless Communication System", Cambridge University Press, 2010
2. Behrouz A Forouzan, "Data Communications and Networking", 4th Edition, McGraw Hill.
3. Gottapu Sasibhusan Rao, "Mobile Cellular Communication", PEARSON

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

SPREAD SPECTRUM COMMUNICATIONS (PC - 5)

UNIT - I

Introduction to Spread Spectrum Systems: Fundamental Concepts of Spread Spectrum Systems, Pseudo Noise Sequences, Direct Sequence Spread Spectrum, Frequency Hop Spread Spectrum, Hybrid Direct Sequence Frequency Hop Spread Spectrum, Code Division Multiple Access.

Binary Shift Register Sequences for Spread Spectrum Systems: Introduction, Definitions, Mathematical Background and Sequence Generator Fundamentals, Maximal Length Sequences, Gold Codes.

UNIT - II

Code Tracking Loops: Introduction, Optimum Tracking of Wideband Signals, Base Band Delay-Lock Tracking Loop, Tau-Dither Non-Coherent Tracking Loop, Double Dither Non-Coherent Tracking Loop.

UNIT - III

Initial Synchronization of the Receiver Spreading Code: Introduction, Problem Definition and the Optimum Synchronizer, Serial Search Synchronization Techniques, Synchronization using a Matched Filter, Synchronization by Estimated the Received Spreading Code.

UNIT - IV

Cellular Code Division Multiple Access (CDMA) Principles: Introduction, Wide Band Mobile Channel, the Cellular CDMA System, Single User Receiver in a Multi User Channel, CDMA System Capacity.

Multi-User Detection in CDMA Cellular Radio: Optimal Multi-User Detection, Linear Suboptimal Detectors, Interference Combat Detection Schemes, Interference Cancellation Techniques.

UNIT - V

Performance of Spread Spectrum Systems in Jamming Environments: Spread Spectrum Communication System Model, Performance of Spread Spectrum Systems without Coding.

Performance of Spread Spectrum Systems with Forward Error Correction: Elementary Block Coding Concepts, Optimum Decoding Rule, Calculation of Error Probability, Elementary Convolution Coding Concepts, Viterbi Algorithm, Decoding and Bit-Error Rate.

TEXT BOOKS:

1. Rodger E Ziemer, Roger L. Peterson and David E Borth, "Introduction to Spread Spectrum Communication", Pearson, 1st Edition, 1995.
2. Mosa Ali Abu-Rgheff – "Introduction to CDMA Wireless Communications", Elsevier Publications, 2008.

REFERENCE BOOKS:

1. George R. Cooper, Clare D. Mc Gillem - "Modern Communication and Spread Spectrum," McGraw Hill, 1986.
2. Andrew j. Viterbi - "CDMA: Principles of spread spectrum communication," Pearson Education, 1st Edition, 1995.
3. Kamilo Feher, "Wireless Digital Communications", PHI, 2009.
4. Andrew Richardson, "WCDMA Design Handbook", Cambridge University Press, 2005.
5. Steve Lee, "Spread Spectrum CDMA", McGraw Hill, 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

ADHOC WIRELESS NETWORKS (PC - 6)

UNIT - I

Wireless Local Area Networks: Introduction, wireless LAN Topologies, Wireless LAN Requirements, Physical Layer- Infrared Physical Layer, Microwave based Physical Layer Alternatives, Medium Access Control Layer- HIPERLAN 1 Sublayer, IEEE 802.11 MAC Sublayer and Latest Developments-802.11a, 802.11b, 802.11g

Personal Area Networks: Introduction to PAN technology and Applications, Bluetooth - specifications, Radio Channel, Piconets and Scatternets, Inquiry, Paging and Link Establishment, Packet Format, Link Types, Power Management, Security, Home RF -Physical and MAC Layer

UNIT - II

MAC Protocols: Introduction, Issues in Designing a MAC protocol for Ad Hoc Wireless Networks, Design goals of a MAC Protocol for Ad Hoc Wireless Networks, Classifications of MAC Protocols, Contention - Based Protocols, Contention - Based Protocols with reservation Mechanisms, Contention – Based MAC Protocols with Scheduling Mechanisms, MAC Protocols that use Directional Antennas, Other MAC Protocols.

UNIT - III

Routing Protocols: Introduction, Issues in Designing a Routing Protocol for Ad Hoc Wireless Networks, Classification of Routing Protocols, Table –Driven Routing Protocols, On – Demand Routing Protocols, Hybrid Routing Protocols, Routing Protocols with Efficient Flooding Mechanisms, Hierarchical Routing Protocols, Power – Aware Routing Protocols.

UNIT – IV

Transport Layer Protocols: Introduction, Issues in Designing a Transport Layer Protocol for Ad Hoc Wireless Networks, Design Goals of a Transport Layer Protocol for Ad Hoc Wireless Networks, Classification of Transport Layer Solutions, TCP Over Ad Hoc Wireless Networks, Other Transport Layer Protocol for Ad Hoc Wireless Networks.

UNIT – V

Quality of Service in Ad Hoc Wireless Networks: Introduction, Real Time Traffic Support in Ad Hoc Wireless Networks, QoS Parameters in Ad Hoc Wireless Network, Issues and Challenges in providing QoS in Ad Hoc Wireless Networks, Classification of QoS Solutions: MAC Layer Solutions, Cluster TDMA, IEEE 802.11e, DBASE, Network Layer Solutions, QoS Routing Protocols, Ticket Based QoS Routing Protocol, Predictive Location Based QoS routing protocol, Trigger Based Distributed QoS Routing Protocol, QoS enabled AODV Routing Protocol, Bandwidth QoS Routing Protocol, On Demand QoS Routing Protocol, On Demand Link-State Multipath QoS Routing Protocol, Asynchronous Slot Allocation Strategies. QoS Frameworks for Ad Hoc Wireless Networks.

TEXT BOOKS:

1. C. Siva Ram Murthy and B.S.Manoj, "Ad Hoc Wireless Networks: Architectures and Protocols", 2004, PHI.
2. P Nicopolitidis and M S Obaidat, "Wireless Networks", Wiley India Edition 2003.

REFERENCE BOOKS:

1. Roy Blake, "Wireless Communication Technology", CENGAGE, 2012
2. Jagannathan Sarangapani, "Wireless Ad- hoc and Sensor Networks: Protocols, Performance and Control" CRC Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

OPTICAL COMMUNICATIONS AND NETWORKS (PE - 3)

UNIT - I

Optical Fibers: Structures, waveguiding and Fabrication: Nature of Light, Basic optical laws and definitions, Single mode fibers, Graded index fiber structure, Attenuation, Signal Dispersion in fibers.

Optical Sources- LEDs, Laser Diodes, Line Coding.

UNIT - II

Photodetectors: Photo detector Noise, Detector Response Time, Avalanche Multiplication Noise.

Optical Receiver Operation: Fundamental receiver operation, Digital receiver performance, Eye diagrams.

WDM Concepts and Components: Passive optical Couplers, Isolators and Circulators

UNIT - III

Digital Links: Point to point links, power penalties, error control, Coherent detection, Differential Quadrature Phase Shift Keying.

Analog Links: Carrier to noise ration, Multichannel Transmission Techniques, RF over Fiber, Radio over fiber links, Microwave Photonics.

UNIT - IV

Optical Networks: Network Concepts, Network Topologies, SONET/SDH, High speed lightwave links, Optical add/ Drop Multiplexing, Optical Switching, WDM Network, Passive Optical Networks, IP Over DWDM, Optical Ethernet, Mitigation of Transmission Impairments

UNIT - V

Performance Measurement and Monitoring: Measurement standards, Basic Test Equipment, Optical power measurement, Optical fiber characterization, Eye diagram tests, optical time domain reflectometer, optical performance monitoring, and optical fiber system performance measurements.

TEXTBOOK:

1. Gerd Keiser, "Optical Fiber Communications", 5th Edition, Mc Graw Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

WIRELESS LANS AND PANS (PE - 3)

UNIT – I

Wireless System & Random Access Protocols: Introduction, First and Second Generation Cellular Systems, Cellular Communications from 1G to 3G, Wireless 4G systems, The Wireless Spectrum; Random Access Methods: Pure ALOHA, Slotted ALOHA, Carrier Sense Multiple Access (CSMA), Carrier Sense Multiple Access with Collision Detection (CSMA/CD), Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA).

UNIT – II

Wireless LANs: Introduction, importance of Wireless LANs, WLAN Topologies, Transmission Techniques: Wired Networks, Wireless Networks, comparison of wired and Wireless LANs; WLAN Technologies: Infrared technology, UHF narrowband technology, Spread Spectrum technology

UNIT – III

The IEEE 802.11 Standard for Wireless LANs: Network Architecture, Physical layer, The Medium Access Control Layer; MAC Layer issues: Hidden Terminal Problem, Reliability, Collision avoidance, Congestion avoidance, Congestion control, Security, The IEEE 802.11e MAC protocol

UNIT – IV

Wireless PANS: Introduction, importance of Wireless PANS, The Bluetooth technology: history and applications, technical overview, the Bluetooth specifications, piconet synchronization and Bluetooth clocks, Master-Slave Switch; Bluetooth security; Enhancements to Bluetooth: Bluetooth interference issues, Intra and Inter Piconet scheduling, Bridge selection, Traffic Engineering, QoS and Dynamics Slot Assignment, Scatternet formation.

UNIT – V

The IEEE 802.15 working Group for WPANs: The IEEE 802.15.3, The IEEE 802.15.4, ZigBee Technology, ZigBee components and network topologies, The IEEE 802.15.4 LR-WPAN Device architecture: Physical Layer, Data Link Layer, The Network Layer, Applications; IEEE 802.15.3a Ultra wideband.

TEXT BOOKS:

1. Carlos de Morais Cordeiro and Dharma Prakash Agrawal, "Ad Hoc and Sensor Networks", World Scientific, 2011.
2. Vijay K.Garg, "Wireless Communications and Networking", Morgan Kaufmann Publishers, 2009.

REFERENCE BOOKS:

1. Kaveh Pahlaram, Prashant Krishnamurthy, "Wireless Networks", PHI, 2002.
2. Marks Ciampor, Jeorge Olenewa, "Wireless Communication", Cengage Learning, 2007.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

WIRELESS SENSOR NETWORKS (PE - 3)

UNIT - I

Introduction: Components of a wireless sensor node, Motivation for a Network of Wireless Sensor Nodes, Classification of sensor networks, Characteristics of wireless sensor networks, Challenges of wireless sensor networks, Comparison between wireless sensor networks and wireless mesh networks, Limitations in wireless sensor networks, Design challenges, Hardware architecture, Applications : Structural Health Monitoring, Traffic Control, Health Care, Pipeline Monitoring, Precision Agriculture, Active Volcano, Underground Mining

Node Architecture: The Sensing Subsystem, the Processor Subsystem, Communication Interfaces, Prototypes. **Operating Systems:** Functional Aspects, Nonfunctional Aspects, Prototypes, Evaluation

UNIT - II

Basic Architectural Framework: Physical Layer, Basic Components, Source Encoding, Channel Encoding, Modulation

Medium Access Control: Wireless MAC Protocols, Characteristics of MAC Protocols in Sensor Networks, Contention-Free MAC Protocols, Contention-Based MAC Protocols, Hybrid MAC Protocols

UNIT - III

Network Layer: Routing Metrics, Flooding and Gossiping, Data-Centric Routing, Proactive Routing, On-Demand Routing, Hierarchical Routing, Location-Based Routing, QoS-Based Routing Protocols

Node and Network Management: Power Management, Local Power Management aspects, Dynamic Power Management, Conceptual Architecture

UNIT - IV

Time Synchronization: Clocks and the Synchronization Problem, Time Synchronization in Wireless Sensor Networks, Basics of Time Synchronization, Time Synchronization Protocols

Localization: Ranging Techniques, Range-Based Localization, Range-Free Localization, Event-Driven Localization

UNIT - V

Security: Fundamentals of Network Security, Challenges of Security in Wireless Sensor Networks, Security Attacks in Sensor Networks, Protocols and Mechanisms for Security, IEEE 802.15.4 and Zig Bee Security

TEXT BOOKS:

1. Waltenegus Dargie, Christian Poellabauer, "Fundamentals of Wireless Sensor Networks: Theory and Practice", Wiley 2010
2. Mohammad S. Obaidat, Sudip Misra, "Principles of Wireless Sensor Networks", Cambridge, 2014

REFERENCE BOOKS:

1. Ian F. Akyildiz, Mehmet Can Vuran, "Wireless Sensor Networks", Wiley 2010
2. C S Raghavendra, K M Sivalingam, Taieb Zhati, "Wireless Sensor Networks", Springer, 2010
3. C. Sivarmurthy & B.S. Manoj, "Adhoc Wireless Networks", PHI-2004
4. FEI HU., XIAOJUN CAO, "Wireless Sensor Networks", CRC Press, 2013
5. Feng ZHAO, Leonidas GUIBAS, "Wireless Sensor Networks", ELSEVIER, 2004

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

NETWORK SECURITY AND CRYPTOGRAPHY (PE - 4)

UNIT - I

Introduction: Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security, Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Block Cipher Design Principles.

UNIT - II

Encryption Algorithms: Triple DES, International Data Encryption algorithm, Blowfish, RC5, Characteristics of Advanced Symmetric block ciphers.

Conventional Encryption: Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT - III

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography.

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT - IV

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD File, Message digest Algorithm, Secure Hash Algorithm.

Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT - V

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Key Management. Web Security: Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. William Stallings, "Cryptography and Network Security: Principles and Practice", Pearson Education.
2. William Stallings, "Network Security Essentials -Applications and Standards", Pearson Education.

REFERENCE BOOKS:

1. Eric Maiwald, " Fundamentals of Network Security", Dreamtech press
2. Charlie Kaufman, Radia Perlman and Mike Speciner, "Network Security - Private Communication in a Public World", Pearson/PHI.
3. Principles of Information Security, Whitman, Thomson.
4. Robert Bragg, Mark Rhodes, "Network Security: The complete reference", TMH
5. Buchmann, "Introduction to Cryptography", Springer.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

SOFTWARE DEFINED RADIO (PE - 4)

UNIT - I:

Introduction: The Need for Software Radios, What is Software Radio, Characteristics and benefits of software radio- Design Principles of Software Radio, RF Implementation issues- The Purpose of RF Front – End, Dynamic Range- The Principal Challenge of Receiver Design – RF Receiver Front- End Topologies- Enhanced Flexibility of the RF Chain with Software Radios- Importance of the Components to Overall Performance- Transmitter Architectures and Their Issues- Noise and Distortion in the RF Chain, ADC and DAC Distortion.

UNIT - II:

Profile and Radio Resource Management: Communication Profiles- Introduction, Communication Profiles, Terminal Profile, Service Profile , Network Profile, User Profile, Communication Profile Architecture, Profile Data Structure, XML Structure, Distribution of Profile Data, Access to Profile Data, Management of Communication Profiles, Communication Classmarks, Dynamic Classmarks for Reconfigurable Terminals, Compression and Coding, Meta Profile Data

UNIT - III:

Radio Resource Management in Heterogeneous Networks: Introduction, Definition of Radio Resource Management, Radio Resource Units over RRM Phases, RRM Challenges and Approaches, RRM Modelling and Investigation Approaches, Investigations of JRRM in Heterogeneous Networks, Measuring Gain in the Upper Bound Due to JRRM, Circuit-Switched System, Packet-Switched System, Functions and Principles of JRRM, General Architecture of JRRM, Detailed RRM Functions in Sub-Networks and Overall Systems

UNIT - IV:

Reconfiguration of the Network Elements: Introduction, Reconfiguration of Base Stations and Mobile Terminals, Abstract Modelling of Reconfigurable Devices, the Role of Local Intelligence in Reconfiguration, Performance Issues, Classification and Rating of Reconfigurable Hardware, Processing Elements, Connection Elements, Global Interconnect Networks, Hierarchical Interconnect Networks, Installing a New Configuration, Applying Reconfiguration Strategies, Reconfiguration Based on Comparison, Resource Recycling, Flexible Workload Management at the Physical Layer, Optimised Reconfiguration, Optimisation Parameters and Algorithms, Optimization Algorithms, Specific Reconfiguration Requirements, Reconfiguring Base Stations, Reconfiguring Mobile Terminals

UNIT - V:

Object – Oriented Representation of Radios and Network Resources: Networks- Object Oriented Programming- Object Brokers- Mobile Application Environments- Joint Tactical Radio System.

Case Studies in Software Radio Design: Introduction and Historical Perspective, SPEAK easy-JTRS, Wireless Information Transfer System, SDR-3000 Digital Transceiver Subsystem, Spectrum Ware, CHARIOT.

TEXT BOOKS:

1. Markus Dillinger, Kambiz Madani, "Software Defined Radio Architecture System and Functions", WILEY 2003
2. Walter Tuttle Bee, "Software Defined Radio: Enabling Technologies", 2002, Wiley Publications.

REFERENCE BOOKS:

1. Jeffrey H. Reed, "Software Radio: A Modern Approach to Radio Engineering", 2002, PEA Publication.
2. Paul Burns, "Software Defined Radio for 3G", 2002, Artech House.
3. Markus Dillinger, Kambiz Madani, Nancy Alonistioti, "Software Defined Radio: Architectures, Systems and Functions", 2003, Wiley.
4. Joseph Mitola, "Software Radio Architecture: Object Oriented Approaches to wireless System Engineering", 2000, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

3G NETWORKS (PE - 4)

UNIT - I

GSM Fundamentals: General Architecture, Mobility Management, GSM Air Interface, Timing Advance, Initial Connection Procedure, Protocols and Signaling, GSM and Signaling Systems.

General Packet Radio Service: General Architecture, GPRS Network Elements, Network Interfaces, GPRS Air Interface, GPRS Protocols.

UNIT - II

Introduction: Second Generation Mobile Networks, 2.5 Generation Mobile Networks, International Mobile, Third Generation Partnership Program (3GPP)

Principles of WCDMA: Requirements for 3rd Generation Air Interface, Schemes for Radio Access, WCDMA Overview, Spreading and De-Spreading, Scrambling, Rake Receiver

UNIT - III

UMTS Network Architecture: Basic Structure of UMTS Network, Access Stratum and Non-access Stratum, Hierarchical Network Organization, Addresses and Identifiers, Service Aspects, Service Classification, Quality of Service (QoS) Architecture, UMTS QoS Classes.

User Equipment: Components of User Equipment, Interfaces of User Equipment, UE Functions, UE Protocols, Classification of UE.

Access Network: Access Network Entities, Network Interfaces, Radio Interface Protocol Architecture, UTRAN Protocol Architecture, Functions.

UNIT - IV

Core Network: Entities Common to CS and PS Domain, Entities Specific to CS Domain, Entities Specific to the PS Domain, Service Specific Entities of the Core Network, CS Domain Protocol Architecture, PS Domain Protocol Architecture, Core Network Functions.

UNIT - V

IP Applications for GPRS/UMTS : IP Protocol Suite Overview, IP Routing, TCP and Congestion Control, TCP Optimization for the Air, IP for GPRS and UMTS R99, IP based QoS for UMTS Networks.

TEXT BOOKS:

1. Jeffrey Bannister, Paul Mather, Sebastian Coope, "Convergence Technologies for 3G Networks- IP, UMTS, EGPRS and ATM", John Wiley & Sons.
2. Procedures Sumith Kaseara, Nishit Narang, "3G Networks Architecture, Protocols", Tata McGraw Hill

REFERENCE BOOKS:

1. Clint Smith, Daniel Collins, "3G wireless Networks", McGraw-Hill-2002.
2. Zheng, "Wireless Networking", complete by ISBN 9789351071563, First Indian reprint 2014., Elsevier publication
3. Hendrik Berndt, "Towards 4G Technologies: Services with Initiative", John Wiley & Sons
4. Savo G. Glisic, "Advanced Wireless Networks: 4G Technologies", John Wiley & Sons

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR II SEMESTER WIRELESS AND MOBILE COMMUNICATIONS

ADVANCED COMMUNICATIONS LAB

Note:

- A. Minimum of 10 Experiments have to be conducted
- B. All Experiments may be Simulated using MATLAB and to be verified using related training kits.

1. Determination of output of convolutional Encoder for a given sequence
2. Determination of output of convolutional Decoder for a given sequence
3. Efficiency of DS Spread- Spectrum Technique
4. Simulation of Frequency Hopping (FH) Spread- Spectrum
5. Implementation of Matched Filters.
6. Optimum receiver for the AWGN channel.
7. Measurement of effect of Inter Symbol Interference..
8. Simulation of PSK system with $M=4$
9. Simulation of DPSK system with $M=4$
10. Design of FSK system
11. BPSK Modulation and Demodulation techniques
12. QPSK Modulation and Demodulation techniques
13. DQPSK Modulation and Demodulation techniques
14. 8-QAM Modulation and Demodulation techniques
15. OFDM Transceiver design
16. Performance evaluation of simulated CDMA system

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR II SEMESTER

List of Open Electives Offered by Various Departments, Effective from AY 2017 - 18

S. No	Name of the Department	Open Elective (S) Offered for Other Departments
1	Civil Engineering (Open Elective – II)	1. Finite Element Method 2. Optimization Techniques
2	Electronics and Communication Engineering (Open Elective – II)	1. Industrial Instrumentation 2. Principles of Computer Communications and Networks
3	Electrical and Electronics Engineering (Open Elective – II)	1. Energy From Waste 2. Distributed Generation and Microgrid 3. Reliability Engineering
4	Mechanical Engineering (Open Elective – II)	1. Engineering Research Methodology
5	Computer Science and Engineering (Open Elective – II)	1. Machine Learning

***Open Elective subject must be chosen from the list of open electives offered by **OTHER** departments.**

Ex: A M.Tech ECE student cannot take Open Elective – II offered by ECE Dept, but can select from open electives offered by **OTHER departments.**

CIVIL ENGINEERING

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

FINITE ELEMENT METHOD (Open Elective – II)

Course Objectives: To impart knowledge about various finite element techniques and development of finite element code.

Course Outcome: The learner will be able to solve continuum problems using finite element analysis.

UNIT - I

Introduction: Concepts of FEM - steps involved - merits and demerits - energy principles –

Discretization - Raleigh - Ritz method of functional approximation.

Principles of Elasticity: Stress equations - strain displacement relationships in matrix form plane stress, plane strain and axi-symmetric bodies of revolution with axi-symmetric loading.

UNIT - II

One dimensional FEM: Stiffness matrix for beam and bar elements - shape functions for 1-D elements.

Two dimensional FEM: Different types of elements for plane stress and plane strain analysis - displacement models - generalized coordinates - shape functions - convergent and compatibility requirements - geometric invariance - natural coordinate system - area and volume coordinates - generation of element stiffness and nodal load matrices

UNIT - III

Isoparametric formulation: Concept - different isoparametric elements for 2D analysis - formulation of 4-noded and 8-noded isoparametric quadrilateral elements - Lagrange elements - serendipity elements.

Axi Symmetric Analysis: bodies of revolution - axi symmetric modeling - strain displacement relationship - formulation of axi symmetric elements.

Three dimensional FEM: Different 3-D elements - strain-displacement relationship – formulation of hexahedral and isoparametric solid element.

UNIT - IV

Introduction to Finite Element Analysis of Plates: Basic theory of plate bending - thin plate theory - stress resultants - Mindlin's approximations - formulation of 4-noded isoperimetric quadrilateral plate element – Shell Element.

UNIT - V

Introduction to non – linear finite analysis – basic methods – application to Special structures.

TEXT BOOKS:

1. A First Course in a Finite Element by Daryl L .Logan, CL Engineers.
2. Concepts and Applications of Finite Element Analysis by Robert D.Cook, DavidS. Malkus and Michael E. Plesha, John Wiley & Sons.

REFERENCES:

1. Introduction to Finite element Method by Tirupathi Chandra Patla and Belugunudu
2. Finite element Methods by OC Zienkiewicz
3. Finite element analysis, theory and programming by GS Krishna Murthy.
4. Introduction to Finite element Method by JN Reddy.

CIVIL ENGINEERING

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

OPTIMIZATION TECHNIQUES (Open Elective – II)

Course Objectives: To understand the theory of optimization methods and algorithms developed for solving various types of optimization problems

Course Outcomes: The student will be able to understand the basic principles of optimization, and in a position to formulate optimization models for a wide range of civil engineering problems and able to solve them.

Unit-I

Linear Programming: Introduction and need for optimization in engineering design, formulating linear programs, graphical solution of linear programs, special cases of linear programming.

UNIT - II

The Simplex Method: Converting a problem to standard form, the theory of the simplex method, the simplex algorithm, special situations in the simplex algorithm, obtaining initial feasible solution.

UNIT - III

Duality and Sensitivity Analysis: Sensitivity analysis, shadow prices, dual of a normal linear program, duality theorems, dual simplex method. Integer Programming: Formulating integer programming problems, the branch-and-bound algorithm for pure integer programs, the branch-and-bound algorithm for mixed integer programs.

UNIT - IV

Non-linear Programming: Introduction to non-linear programming (NLP), Convex and concave functions, NLP with one variable, Line search algorithms, Multivariable unconstrained problems, constrained problems, Lagrange Multiplier, The Karush-Kuhn-Tucker (KKT) conditions, the method of steepest ascent, convex combination method, penalty function, Quadratic programming,

UNIT - V

Dynamic programming: Evolutionary algorithms: Genetic Algorithm, concepts of multiobjective optimization, Markov Process, Queuing Models.

TEXT BOOK:

1. S.S. Rao, Engineering Optimization: Theory and Practice, Wiley & Sons, New Jersey, 2009.

REFERENCES:

1. F.H. Hiller and G.J. Liberman, Introduction to Operations Research, Tata-McGraw-Hill, 2010.
2. W.L. Winston, Operations Research: Applications and Algorithm, 4th Edition, Cengage Learning, 1994.
3. K. Deb, Optimization for Engineering Design, Prentice Hall, 2013.
4. M.C. Joshi and K.M. Moudgalay, Optimization: Theory and Practice, Narosa, 2004.
5. K. Deb, Multi-Objective Optimization using evolutionary algorithms, John Wiley and Sons, 2009.

ELECTRONICS AND COMMUNICATION ENGINEERING
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
INDUSTRIAL INSTRUMENTATION (Open Elective – II)

UNIT - I

METROLOGY, VELOCITY AND ACCELERATION MEASUREMENT: Measurement of length - Gauge blocks - Plainness - Area using Simpson's rule, Plain meter - Diameter - Roughness - Angle using Bevel protractor, sine bars and Clinometer - Mechanical, Electrical, Optical and Pneumatic Comparators. Optical Methods for length and distance measurements using Optical flats and Michelson Interferometer.

Relative velocity - Translational and Rotational velocity measurements - Revolution counters and Timers - Magnetic and Photoelectric pulse counting stroboscopic methods. Accelerometers-different types, Gyroscopes-applications.

UNIT - II

FORCE AND PRESSURE MEASUREMENT: Force measurement - Different methods - Gyroscopic Force Measurement - Vibrating wire Force transducer. Basics of Pressure measurement - Manometer types - Force-Balance and Vibrating Cylinder Transducers - High and Low Pressure measurement - McLeod Gauge, Knudsen Gauge, Momentum Transfer Gauge, Thermal Conductivity Gauge, Ionization Gauge, Dual Gauge Techniques, Deadweight Gauges, Hydrostatic Pressure Measurement

UNIT - III

FLOW MEASUREMENT AND LEVEL MEASUREMENT: Flow Meters- Head type, Area type (Rota meter), electromagnetic type, Positive displacement type, mass flow meter, ultrasonic type, vortex shedding type, Hotwire anemometer type, Laser Doppler Velocity-meter. Basic Level measurements - Direct, Indirect, Pressure, Buoyancy, Weight, Capacitive Probe methods

UNIT - IV

DENSITY, VISCOSITY AND OTHER MEASUREMENTS: Density measurements - Strain Gauge load cell method - Buoyancy method - Air pressure balance method - Gamma ray method - Vibrating probe method. Units of Viscosity, specific gravity scales used in Petroleum Industries, Different Methods of measuring consistency and Viscosity - Two float viscorator - Industrial consistency meter. Sound-Level Meters, Microphones, Humidity Measurement

UNIT - V

CALIBRATION AND INTERFACING: Calibration using Master Sensors, Interfacing of Force, Pressure, Velocity, Acceleration, Flow, Density and Viscosity Sensors, Variable Frequency Drive

TEXT BOOKS:

1. Doebelin E.O., "Measurement Systems - Applications and Design", 4th Edition, McGraw Hill International, 1990.
2. Patranabis D, "Principles of Industrial Instrumentation", TMH. End edition 1997

REFERENCES:

1. Considine D. M., "Process Instruments and Control Handbook", 4th Edition, McGraw Hill International, 1993
2. Jain R.K., "Mechanical and Industrial Measurements", Khanna Publications.

ELECTRONICS AND COMMUNICATION ENGINEERING

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

PRINCIPLES OF COMPUTER COMMUNICATIONS AND NETWORKS (Open Elective – II)

Prerequisite: Nil

Course Objectives:

- To understand the concept of computer communication.
- To learn about the networking concept, layered protocols.
- To understand various communications concepts.
- To get the knowledge of various networking equipment.

Course Outcomes: The student:

- Can get the knowledge of networking of computers, data transmission between computers.
- Will have the exposure about the various communication concepts.
- Will get awareness about the structure and equipment of computer network structures.

UNIT - I

Overview of Computer Communications and Networking: Introduction to Computer Communications and Networking, Introduction to Computer Network, Types of Computer Networks, Network Addressing, Routing, Reliability, Interoperability and Security, Network Standards, The Telephone System and Data Communications.

UNIT - II

Essential Terms and Concepts: Computer Applications and application protocols, Computer Communications and Networking models, Communication Service Methods and data transmission modes, analog and Digital Communications, Speed and capacity of a Communication Channel, Multiplexing and switching, Network architecture and the OSI reference model.

UNIT - III

Analog and Digital Communication Concepts: Representing data as analog signals, representing data as digital signals, data rate and bandwidth reduction, Digital Carrier Systems.

UNIT - IV

Physical and data link layer Concepts: The Physical and Electrical Characteristics of wire, Copper media, fiber optic media, wireless Communications. Introduction to data link Layer, the logical link control and medium access control sub-layers.

UNIT - V

Network Hardware Components: Introduction to Connectors, Transreceivers and media convertors, repeaters, network interface cards and PC cards, bridges, switches, switches Vs Routers.

TEXT BOOKS:

1. Computer Communications and Networking Technologies, Michel A. Gallo and William H. Hancock, Thomson Brooks / Cole.

REFERENCE BOOKS:

1. Principles of Computer Networks and Communications, M. Barry Dumas, Morris Schwartz, Pearson.

ELECTRICAL AND ELECTRONICS ENGINEERING
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

ENERGY FROM WASTE (Open Elective – II)

Prerequisite: Renewable Energy Sources, Physics, Environmental Studies

Course Objectives:

- To classify solid waste sources
- To identify methods of solid waste disposal
- To study various energy generation methods
- To analyse biogas production methods and recycling of e-waste

Course Outcomes: Upon the completion of the subject, the student will be able to

- Understand technologies for generation of energy from solid waste
- Compare methods of solid waste disposal
- Identify sources of energy from bio-chemical conversion
- Analyze methods for management of e-waste

UNIT- I

Solid Waste Sources Solid Waste Sources, types, composition, Properties, Global warming, Municipal Solid Waste: Physical, chemical and biological properties , Waste Collection and, Transfer stations, Waste minimization and recycling of municipal waste, Segregation of waste, Size Reduction , Managing Waste. Status of technologies for generation of Energy from Waste Treatment and Disposal Aerobic composting, incineration, Furnace type and design, Medical waste /Pharmaceutical waste treatment Technologies, incineration, Environmental impacts, Measures to mitigate environmental effects due to incineration .

UNIT - II

Land Fill method of Solid waste disposal Land fill classification, Types, methods and Siting consideration, Layout and preliminary design of landfills: Composition, characteristics, generation, Movement and control of landfill leachate and gases, Environmental monitoring system for land fill gases.

UNIT - III

Energy Generation from Waste Bio-chemical Conversion: Sources of energy generation, anaerobic digestion of sewage and municipal wastes, direct combustion of MSW-refuse derived solid fuel, Industrial waste, agro residues, Anaerobic Digestion.

UNIT - IV

Biogas production, Land fill gas generation and utilization, Thermo-chemical conversion: Sources of energy generation, Gasification of waste using Gasifiers, Briquetting, Utilization and advantages of briquetting, Environmental benefits of Bio-chemical and Thermo- chemical conversion.

UNIT - V

E-waste: e-waste in the global context – Growth of Electrical and Electronics Industry in India – Environmental concerns and health hazards – Recycling e-waste: a thriving economy of the unorganized sector – Global trade in hazardous waste – impact of hazardous e-waste in India. Management of e-waste: e-waste legislation, Government regulations on e-waste management – International experience – need for stringent health safeguards and environmental protection laws of India.

TEXT BOOKS:

1. Nicholas P. Cheremisinoff. Handbook of Solid Waste Management and Waste Minimization Technologies. An Imprint of Elsevier, New Delhi (2003).
2. P. Aarne Vesilind, William A. Worrell and Debra R. Reinhart. Solid Waste Engineering. Thomson Asia Pte Ltd. Singapore (2002)
3. M. Dutta , B. P. Parida, B. K. Guha and T. R. Surkrishnan. Industrial Solid Waste Management and Landfilling practice. Narosa Publishing House, New Delhi (1999).

4. "E-waste in India: Research unit, Rajya Sabha Secretariat, New Delhi, June 2011"
5. Amalendu Bagchi. Design, construction and Monitoring of Landfills. John Wiley and Sons. New York. (1994)
6. M. L. Davis and D. A. Cornwell. Introduction to environmental engineering. Mc Graw Hill International Edition, Singapore (2008)
7. C. S. Rao. Environmental Pollution Control Engineering. Wiley Eastern Ltd. New Delhi (1995)
8. S. K. Agarwal. Industrial Environment Assessment and Strategy. APH Publishing Corporation. New Delhi (1996)
9. Sofer, Samir S. (ed.), Zaborsky, R. (ed.), "Biomass Conversion Processes for Energy and Fuels", New York, Plenum Press, 1981
10. Hagerty, D. Joseph; Pavoni, Joseph L; Heer, John E., "Solid Waste Management", New York, Van Nostrand, 1973
11. George Tchobanoglous, Hilary Theisen and Samuel Vigil Prsl: Tchobanoglous, George Theisen, Hilary Vigil, Samuel, "Integrated Solid Waste management: Engineering Principles and Management issues", New York, McGraw Hill, 1993.

REFERENCES:

1. C Parker and T Roberts (Ed), Energy from Waste - An Evaluation of Conversion Technologies, Elsevier Applied Science, London, 1985
2. KL Shah, Basics of Solid and Hazardous Waste Management Technology, Prentice Hall, 2000
3. M Datta, Waste Disposal in Engineered Landfills, Narosa Publishing House, 1997
3. G Rich et.al, Hazardous Waste Management Technology, Podvan Publishers, 1987
4. AD Bhide, BB Sundaresan, Solid Waste Management in Developing Countries, INSDOC, New Delhi, 1983
4. FUEL CELL AND
5. **Google books:**
 - (i) e-waste Management: From waste to Resource Klaus Hieronymi, Ramzy Kahnat, Eric williams Tech. & Engg.-2013(Publisher: Earthscan 2013).
 - (ii) What is the impact of E-waste: Tamara Thompson
 - (iii) E-waste poses a Health Hazard: Sairudeen Pattazhy
6. **Weblinks :**
 - www.unep.org
 - www.routledge.com
 - www.amazon.com
 - www.bookdepository.com
 - www.ecoactiv.com

ELECTRICAL AND ELECTRONICS ENGINEERING
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
DISTRIBUTED GENERATION AND MICROGRID (Open Elective – II)

Course Objectives

- To illustrate the concept of distributed generation
- To analyze the impact of grid integration.
- To study concept of Micro grid and its configuration
- To find optimal size, placement and control aspects of DGs

Course Outcomes: Upon the Completion of the course student will be able to

- Find the size and optimal placement DG
- Analyze the impact of grid integration and control aspects of DGs
- Model and analyze a micro grid taking into consideration the planning and operational issues of the DGs to be connected in the system
- Describe the technical impacts of DGs in power systems

UNIT - I

Need for distributed generation - Renewable sources in distributed generation - Current scenario in distributed generation - Planning of DGs – Siting and sizing of DGs – Optimal placement of DG sources in distribution systems.

UNIT - II

Grid integration of DGs – Different types of interfaces - Inverter based DGs and rotating machine based interfaces - Aggregation of multiple DG units - Energy storage elements - Batteries, ultra-capacitors, flywheels.

UNIT - III

Technical impacts of DGs – Transmission systems, Distribution systems, De-regulation – Impact of DGs upon protective relaying – Impact of DGs upon transient and dynamic stability of existing distribution systems.

UNIT-IV

Economic and control aspects of DGs – Market facts, issues and challenges - Limitations of DGs - Voltage control techniques, Reactive power control, Harmonics, Power quality issues - Reliability of DG based systems – Steady state and Dynamic analysis.

UNIT - V

Introduction to micro-grids – Types of micro-grids – Autonomous and non-autonomous grids – Sizing of micro-grids - Modeling & analysis - Micro-grids with multiple DGs – Micro-grids with power electronic interfacing units - Transients in micro-grids - Protection of micro-grids – Case studies.

TEXT BOOKS:

1. H. Lee Willis, Walter G. Scott , 'Distributed Power Generation – Planning and Evaluation', Marcel Decker Press, 2000.
2. M.Godoy Simoes, Felix A.Farret, 'Renewable Energy Systems – Design and Analysis with Induction Generators', CRC press.
3. Robert Lasseter, Paolo Piagi, ' Micro-grid: A Conceptual Solution', PESG 2004, June 2004.
4. F. Katiraei, M.R. Iravani, 'Transients of a Micro-Grid System with Multiple Distributed Energy Resources', International Conference on Power Systems Transients (IPST'05) in Montreal, Canada on June 19-23, 2005.
5. Z. Ye, R. Walling, N. Miller, P. Du, K. Nelson, 'Facility Microgrids', General Electric Global Research Center, Niskayuna, New York, Subcontract report, May 2005.

ELECTRICAL AND ELECTRONICS ENGINEERING
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
RELIABILITY ENGINEERING (Open Elective – II)

Course Objectives:

- To comprehend the concept of Reliability and Unreliability
- Derive the expressions for probability of failure, Expected value and standard deviation of Binominal distribution, Poisson distribution, normal distribution and weibull distributions.
- Formulating expressions for Reliability analysis of series-parallel and Non-series parallel systems
- Deriving expressions for Time dependent and Limiting State Probabilities using Markov models.

Course Outcomes: Upon the completion of this course, the student will be able to

- Apply fundamental knowledge of Reliability to modeling and analysis of series-parallel and Non-series parallel systems.
- Solve some practical problems related with Generation, Transmission and Utilization of Electrical Energy.
- Understand or become aware of various failures, causes of failures and remedies for failures in practical systems.

UNIT – I

Rules for combining probabilities of events, Definition of Reliability. Significance of the terms appearing in the definition. Probability distributions: Random variables, probability density and distribution functions. Mathematical expectation, Binominal distribution, Poisson distribution, normal distribution, weibull distribution.

UNIT - II

Hazard rate, derivation of the reliability function in terms of the hazard rate. Failures: Causes of failures, types of failures (early failures, chance failures and wear-out failures). Bath tub curve. Preventive and corrective maintenance. Modes of failure. Measures of reliability: mean time to failure and mean time between failures.

UNIT - III

Classification of engineering systems: series, parallel and series-parallel systems- Expressions for the reliability of the basic configurations.

Reliability evaluation of Non-series-parallel configurations: Decomposition, Path based and cutset based methods, Deduction of the Paths and cutsets from Event tree.

UNIT - IV

Discrete Markov Chains: General modeling concepts, stochastic transitional probability matrix, time dependent probability evaluation and limiting state probability evaluation of one component repairable model. Absorbing states.

Continuous Markov Processes: Modeling concepts, State space diagrams, Stochastic Transitional Probability Matrix, Evaluating time dependent and limiting state Probabilities of one component repairable model. Evaluation of Limiting state probabilities of two component repairable model.

UNIT - V

Approximate system Reliability analysis of Series systems, parallel systems with two and more than two components, Network reduction techniques. Minimal cutset/failure mode approach.

TEXT BOOKS:

1. "Reliability evaluation of Engineering systems", Roy Billinton and Ronald N Allan, BS Publications.
2. "Reliability Engineering", Elsayed A. Elsayed, Prentice Hall Publications.

REFERENCES:

1. "Reliability Engineering: Theory and Practice", By Alessandro Birolini, Springer Publications.
2. "An Introduction to Reliability and Maintainability Engineering", Charles Ebeling, TMH Publications.
3. "Reliability Engineering", E. Balaguruswamy, TMH Publications.

MECHANICAL ENGINEERING

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

ENGINEERING RESEARCH METHODOLOGY (Open Elective – II)

UNIT - I

Research Methodology: Objectives and Motivation of Research, Types of Research, Research Approaches, Significance of Research, Research Methods verses Methodology, Research and Scientific Method, Important of Research Methodology, Research Process, Criteria of Good Research, Problems Encountered by Researchers in India, Benefits to the society in general.

Defining the Research Problem: Definition of Research Problem, Problem Formulation, Necessity of Defining the Problem, Technique involved in Defining a Problem.

UNIT - II

Literature Survey: Importance of Literature Survey, Sources of Information, Assessment of Quality of Journals and Articles, Information through Internet. **Literature Review:** Need of Review, Guidelines for Review, Record of Research Review.

UNIT - III

Research Design: Meaning of Research Design, Need of Research Design, Feature of a Good Design Important Concepts Related to Research Design, Different Research Designs, Basic Principles of Experimental Design, Developing a Research Plan, Design of Experimental Set-up, Use of Standards and Codes.

UNIT - IV

Data Collection: Collection of primary data, Secondary data, Data organization, Methods of data grouping, Diagrammatic representation of data, Graphic representation of data. Sample Design, Need for sampling, some important sampling definitions, Estimation of population, Role of Statistics for Data Analysis, Parametric V/s Non Parametric methods, Descriptive Statistics, Measures of central tendency and Dispersion, Hypothesis testing, Use of Statistical software.

Data Analysis: Deterministic and random data, Uncertainty analysis, Tests for significance: Chi-square, student's t-test, Regression modeling, Direct and Interaction effects, ANOVA, F-test, Time Series analysis, Autocorrelation and Autoregressive modeling.

UNIT - V

Research Report Writing: Format of the Research report, Synopsis, Dissertation, Thesis its Differentiation, References/Bibliography/Webliography, Technical paper writing/Journal report writing, making presentation, Use of visual aids. **Research Proposal Preparation:** Writing a Research Proposal and Research Report, Writing Research Grant Proposal.

REFERENCES:

1. C.R Kothari, Research Methodology, Methods & Technique; New Age International Publishers, 2004
2. R. Ganesan, Research Methodology for Engineers, MJP Publishers, 2011
3. Ratan Khananabis and Suvasis Saha, Research Methodology, Universities Press, Hyderabad, 2015.
4. Y. P. Agarwal, Statistical Methods: Concepts, Application and Computation, Sterling Publs., Pvt., Ltd., New Delhi, 2004
5. Vijay Upagade and Aravind Shende, Research Methodology, S. Chand & Company Ltd., New Delhi, 2009
6. G. Nageswara Rao, Research Methodology and Quantitative methods, BS Publications, Hyderabad, 2012.
7. Naval Bajjai "Business Research Methods" Pearson 2011.
8. Prahalad Mishra " Business Research Methods " Oxford 2016

COMPUTER SCIENCE AND ENGINEERING
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
MACHINE LEARNING (Open Elective - II)

Prerequisites:

- Data Structures
- Knowledge on statistical methods

Course Objectives:

- This course explains machine learning techniques such as decision tree learning, Bayesian learning etc.
- To understand computational learning theory.
- To study the pattern comparison techniques.

Course Outcomes:

- Understand the concepts of computational intelligence like machine learning
- Ability to get the skill to apply machine learning techniques to address the real time problems in different areas
- Understand the Neural Networks and its usage in machine learning application.

UNIT - I

Introduction - Well-posed learning problems, designing a learning system Perspectives and issues in machine learning

Concept learning and the general to specific ordering – Introduction, A concept learning task, concept learning as search, Find-S: Finding a Maximally Specific Hypothesis, Version Spaces and the Candidate Elimination algorithm, Remarks on Version Spaces and Candidate Elimination, Inductive Bias.

Decision Tree Learning – Introduction, Decision Tree Representation, Appropriate Problems for Decision Tree Learning, The Basic Decision Tree Learning Algorithm Hypothesis Space Search in Decision Tree Learning, Inductive Bias in Decision Tree Learning, Issues in Decision Tree Learning.

UNIT - II

Artificial Neural Networks Introduction, Neural Network Representation, Appropriate Problems for Neural Network Learning, Perceptions, Multilayer Networks and the Back propagation Algorithm.

Discussion on the Back Propagation Algorithm, An illustrative Example: Face Recognition

Evaluation Hypotheses – Motivation, Estimation Hypothesis Accuracy, Basics of Sampling Theory, A General Approach for Deriving Confidence Intervals, Difference in Error of Two Hypotheses, Comparing Learning Algorithms.

UNIT - III

Bayesian learning - Introduction, Bayes Theorem, Bayes Theorem and Concept Learning Maximum Likelihood and Least Squared Error Hypotheses, Maximum Likelihood Hypotheses for Predicting Probabilities, Minimum Description Length Principle, Bayes Optimal Classifier, Gibbs Algorithm, Naïve Bayes Classifier, An Example: Learning to Classify Text, Bayesian Belief Networks, EM Algorithm.

Computational Learning Theory – Introduction, Probably Learning an Approximately Correct Hypothesis, Sample Complexity for Finite Hypothesis Space, Sample Complexity for Infinite Hypothesis Spaces, The Mistake Bound Model of Learning.

Instance-Based Learning – Introduction, k-Nearest Neighbor Learning, Locally Weighted Regression, Radial Basis Functions, Case-Based Reasoning, Remarks on Lazy and Eager Learning.

UNIT - IV

Pattern Comparison Techniques, Temporal patterns, Dynamic Time Warping Methods, Clustering, Codebook Generation, Vector Quantization

Pattern Classification: Introduction to HMMs, Training and Testing of Discrete Hidden Markov Models and Continuous Hidden Markov Models, Viterbi Algorithm, Different Case Studies in Speech recognition and Image Processing

UNIT - V

Analytical Learning – Introduction, Learning with Perfect Domain Theories : PROLOG-EBG Remarks on Explanation-Based Learning, Explanation-Based Learning of Search Control Knowledge, Using Prior Knowledge to Alter the Search Objective, Using Prior Knowledge to Augment Search Operations.

Combining Inductive and Analytical Learning – Motivation, Inductive-Analytical Approaches to Learning, Using Prior Knowledge to Initialize the Hypothesis.

TEXT BOOKS:

1. Machine Learning – Tom M. Mitchell,- MGH
2. Fundamentals of Speech Recognition By Lawrence Rabiner and Biing – Hwang Juang.

REFERENCE BOOK:

1. Machine Learning : An Algorithmic Perspective, Stephen Marsland, Taylor & Francis



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH (POWER ELECTRONICS/
POWER AND INDUSTRIAL DRIVES/POWER ELECTRONICS AND ELECTRIC DRIVES)
COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Machine Modeling and Analysis	25	75	4	--	4
Core Course II	Modern Control Theory	25	75	4	--	4
Core Course III	Power Electronic Devices and Circuits	25	75	4	--	4
Core Elective I	1. Special Machines 2. HVDC Transmission 3. Programmable Logic Controllers and their Applications	25	75	4	--	4
Core Elective II	1. Microcontrollers and Applications 2. Embedded Systems 3. Digital Control Systems	25	75	4	--	4
Open Elective I	1. Optimization Techniques 2. Digital control systems 3. Renewable energy systems 4. HVDC Transmission 5. Analysis of power converters 6. Embedded Systems	25	75	4	--	4
Laboratory I	Power Converters Simulation Lab	25	75	--	4	2
Seminar I	Seminar-I	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Power Electronic Converters	25	75	4	--	4
Core Course V	Power Electronic Control of DC Drives	25	75	4	--	4
Core Course VI	Power Electronic Control of AC Drives	25	75	4	--	4
Core Elective III	1. Power Quality 2. Advanced Digital Signal Processing 3. Switched Mode Power Supplies (SMPS)	25	75	4	--	4
Core Elective IV	1. Flexible AC Transmission Systems 2. High-Frequency Magnetic Components 3. Dynamics of Electrical Machines	25	75	4	--	4
Open Elective II	1. Instrumentation & Control 2. Intelligent Control 3. Smart grid technologies 4. AI Techniques in Electrical Engineering 5. Reliability Engineering 6. Energy Auditing, Conservation & Management	25	75	4	--	4
Laboratory II	Power Converters and Drives Lab	25	75	--	4	2
Seminar II	Seminar-II	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C
Comprehensive Viva-Voce	--	100	--	--	4
Project work Review I	50	--	--	24	12
Total Credits			--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C
Project work Review II	50	--	--	8	4
Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits			--	24	16



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

MACHINE MODELLING AND ANALYSIS

Course Objectives:

- Identifying the methods and assumptions in modeling of machines.
- Recognize the different frames for modeling of AC machines.
- To write voltage and torque equations in state space form for different machines.

Learning Outcomes:

- Develop the mathematical models of various machines like machines like dc machine, induction motor and Synchronous machines using modeling equations.
- Models have to be used for analysis using simulation study.

UNIT-I:

Basic Two-pole DC machine - primitive 2-axis machine – Voltage and Current relationship – Torque equation.

UNIT-II:

Mathematical model of separately excited DC motor and DC Series motor in state variable form – Transfer function of the motor - Numerical problems.

Mathematical model of D.C. shunt motor D.C. Compound motor in state variable form – Transfer function of the motor - Numerical Problems

UNIT-III:

Liner transformation – Phase transformation (α, β, γ to a, b, c) – Active transformation (α, β, γ to d, q).

Circuit model of a 3 phase Induction motor – Linear transformation - Phase Transformation – Transformation to a Reference frame – Two axis models for induction motor.

dq model based DOL starting of Induction Motors

UNIT-IV:

Voltage and current Equations in stator reference frame – equation in Rotor reference frame – equations in a synchronously rotating frame – Torque equation - Equations in state – space form.

UNIT-V:

Circuits model of a 3ph Synchronous motor – Two axis representation of Syn. Motor.

Voltage and current Equations in state – space variable form – Torque equation.

dq model based short circuit fault analysis- emphasis on voltage, frequency and recovery time.

TEXT BOOKS:

1. Generalized Machine theory - P.S. Bimbhra, Khanna Publishers
2. Analysis of electric machinery and Drives systems - Paul C. Krause, Oleg wasynezuk, Scott D. Sudhoff.
3. Thyristor control of Electric Drives - Vedom Subranmanyam, Add Publisher.
4. Power System Stability and Control – Prabha Kundur, EPRI.

REFERECES:

1. Performance optimization of induction motors during Voltage-controlled soft starting, Article In IEEE Transactions On Energy Conversion, July 2004.
2. A Novel Method for Starting of Induction Motor with Improved Transient Torque Pulsations, Nithin K.S, Dr. Bos Mathew Jos, Muhammed Rafeek, Dr. Babu Paul. International Journal of Engineering and Innovative Technology (IJEIT) Volume 2, Issue 8, February 2013.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

MODERN CONTROL THEORY

UNIT-I:

Mathematical Preliminaries: Fields, Vectors and Vector Spaces – Linear combinations and Bases – Linear Transformations and Matrices – Scalar Product and Norms – Eigen-values, Eigen Vectors and a Canonical form representation of Linear operators – The concept of state – State Equations for Dynamic systems – Time invariance and Linearity – Non-uniqueness of state model – State diagrams for Continuous-Time State models.

UNIT-II:

State Variable Analysis: Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and its properties. General concept of controllability – General concept of Observability – Controllability tests for Continuous-Time Invariant Systems – Observability tests for Continuous-Time Invariant Systems – Controllability and Observability of State Model in Jordan Canonical form – Controllability and Observability Canonical forms of State model.

UNIT-III:

Non Linear Systems: Introduction – Non Linear Systems - Types of Non-Linearities – Saturation – Dead-Zone - Backlash – Jump Phenomenon etc;– Singular Points – Introduction to Linearization of nonlinear systems, Properties of Non-Linear systems – Describing function–describing function analysis of nonlinear systems – Stability analysis of Non-Linear systems through describing functions. Introduction to phase-plane analysis, Method of Isoclines for Constructing Trajectories, singular points, phase-plane analysis of nonlinear control systems.

UNIT-IV:

Stability Analysis: Stability in the sense of Lyapunov, Lyapunov's stability and Lyapunov's instability theorems - Stability Analysis of the Linear continuous time invariant systems by Lyapunov second method – Generation of Lyapunov functions – Variable gradient method – Krasovskii's method. State feedback controller design through Pole Assignment – State observers: Full order and Reduced order.

UNIT-V:

Optimal Control: Introduction to optimal control - Formulation of optimal control problems – calculus of variations – fundamental concepts, functional, variation of functional – fundamental theorem of theorem of Calculus of variations – boundary conditions – constrained minimization – formulation using Hamiltonian method – Linear Quadratic regulator.

TEXT BOOKS:

1. Modern Control System Theory by M.Gopal – New Age International -1984
2. Modern Control Engineering by Ogata.K – Prentice Hall - 1997

REFERENCES:

Optimal control by Kirck



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

POWER ELECTRONIC DEVICES AND CIRCUITS

Course Objectives

- To understand the characteristics and principle of operation of modern power semi conductor devices.
- To comprehend the c o n c e p t s o f different power converters and their applications
- Students will be able to analyze and design switched mode regulator for various industrial applications.

Learning Outcomes

- Students will be able to choose appropriate device for a particular converter topology.
- Students will be able to use power electronic simulation packages for analyzing and designing power converters

UNIT-I:

Modern Power Semiconductor Devices: Modern power semiconductor devices – MOS turn Off Thyristor (MTO) – Emitter Turn off Thyristor (ETO) – Intergrated Gate-Commutated thyristor (IGCTs) – MOS-controlled thyristors (MCTs) – Insulated Gate Bipolar Transistor (IGBT) – MOSFET – comparison of their features.

UNIT-II:

Driver Circuits, Snubber Circuits and Heat Sinks: Introduction, MOSFET and IGBT Drive Circuits, Bipolar Transistor Drive Circuits, Thyristor Drive Circuits, Transistor Snubber Circuits, Energy Recovery Snubber Circuits, Thyristor Snubber Circuits, Heat Sinks and Thermal Management

UNIT-III:

AC Voltage Controllers & Cyclo-Converters: Single phase AC voltage controllers: with Resistive, Resistive –inductive and Resistive –inductive-induced EMF loads – AC voltage controllers with PWM Control – Effects of source and load inductances – Synchronous tap changers – Applications.

Single phase and Three phase cyclo-converters – analysis of midpoint and bridge Configurations – Limitations – Advantages – Applications.

UNIT-IV:

Single-Phase and Three-Phase Converters: Single-phase converters: Half controlled and fully controlled converters – Evaluation of input power factor and harmonic factor – continuous and Discontinuous load current – single phase dual converters –

Power factor Improvements – Extinction angle control – symmetrical angle control – PWM – single phase sinusoidal PWM – single phase series converters – Applications.

Three-Phase Converters: Half controlled and fully controlled converters – Evaluation of input power factor and harmonic factor – continuous load current – three phase dual converters – three-phase PWM – Twelve phase converters – Applications.

UNIT-V:

D.C. to D.C. Converters: Analysis of step – down and step-up dc to dc converters with resistive and Resistive –inductive loads – Switched mode regulators – Analysis of Buck Regulators – Boost regulators – buck and boost regulators – Cuk regulators – Condition for Continuous inductor current and capacitor voltage – comparison of regulators – Multi-output boost converters – Advantages - Applications.

TEXT BOOKS:

1. Power Electronics – Mohammed H. Rashid – Pearson Education Third Edition – First Indian reprint 2004.
2. Power Electronics – Daniel W. Hart, McGraw Hill Publications.
3. Power Electronics Devices, Circuits and Industrial applications, V. R. Moorthi, Oxford University Press.



4. Power Electronics – Ned Mohan, Tore M. Undeland and William P. Robbins – John Wiley and Sons – Second Edition.

REFERENCE BOOKS:

1. Power Electronics, Dr. P. S. Bimbhra, Khanna Publishers.
2. Elements of Power Electronics, Philip T. Krein, Oxford University Press.
3. Power Electronics, M. S. Jamil Asghar, PHI Private Limited.
4. Principles of Power Electronics, John G. Kassakian, Martin F. Schlect, Geroge C. Verghese, Pearson Education.
5. Fundamentals of Power Electronics, Robert W. Erickson, Dragan and Maksimovic, Springer.

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

SPECIAL MACHINES
(Elective-I)

Course Objectives: The course will enable the students to:

- (i) Learn the constructional features, principle of operation, methods of control and applications of stepper motors.
- (ii) Understand the constructional features, principle of operation, methods of control and applications of Switched reluctance motors.
- (iii) Have an insight into the constructional features, principle of operation, methods of control and applications of PMLDC motors.
- (iv) Have a clear picture of the types, the constructional features, principle of operation, methods of control and applications of PMSM.
- (v) Gain knowledge in the types, the constructional features, principle of operation, methods of control and applications of SyRM.

Learning Outcomes: After completion of the course, the students are expected to

- (i) Realize the need for stepper motors and the various applications in industries.
- (ii) Get a clear picture of the operational characteristics and the applications of SRM.
- (iii) Know the various types of PMLDC motors, rotor position sensors, methods of control and their applications.
- (iv) Get a clear idea of the features, control and the applications of PMSM.
- (v) Get a clear picture of the operational characteristics and the applications of SyRM.

UNIT-I:

Stepper Motors: Introduction-synchronous inductor (or hybrid stepper motor), Hybrid stepping motor, construction, principles of operation, energization with two phase at a time- essential conditions for the satisfactory operation of a 2-phase hybrid step motor - very slow - speed synchronous motor for servo control-different configurations for switching the phase windings-control circuits for stepping motors-an open-loop controller for a 2-phase stepping motor.

UNIT-II:

Variable Reluctance Stepping Motors: Variable reluctance (VR) Stepping motors, single-stack VR step motors, Multiple stack VR motors-Open-loop control of 3-phase VR step motor-closed-Loop control of step motor, discriminator (or rotor position sensor) transilator, major loop-characteristics of step motor in open-loop drive – comparison between open-loop position control with step motor and a position control servo using a conventional (dc or ac) servo motor- Suitability and areas of application of stepping motors-5- phase hybrid stepping motor - single phase - stepping motor, the construction, operating principle torque developed in the motor.

Switched Reluctance Motor: Introduction – improvements in the design of conventional reluctance motors- Some distinctive differences between SR and conventional reluctance motors-principle of operation of SRM- Some design aspects of stator and rotor pole arcs, design of stator and rotor and pole arcs in SR motor-determination of $L(\theta)$ - θ profile - power converter for SR motor-A numerical example –Rotor sensing mechanism and logic control, drive and power circuits, position sensing of rotor with Hall problems-derivation of torque expression, general linear case.

UNIT-III:

Permanent Magnet Materials and PM DC Machines: Introduction, Hysteresis loops and recoil line-stator frames (pole and yoke - part) of conventional PM dc Motors, Equivalent circuit of PM Generator and Motor-Development of Electronically commutated dc motor from conventional dc motor.

Brushless DC Motor: Types of construction – principle of operation of BLDM- sensing and switching logic scheme, sensing logic controller, lockout pulses –drive and power circuits, Base drive circuits, power converter circuit-Theoretical analysis and performance prediction, modeling and magnet circuit d-q analysis of BLDM -transient analysis formulation in terms of flux linkages as state variables- Approximate solution for current and torque under steady state –Theory of BLDM as variable speed synchronous motor (assuming sinusoidal flux distribution)- Methods of reducing Torque Pulsations, 180 degrees pole arc and 120 degree current sheet.



UNIT-IV:

Linear Induction Motor: Development of a double sided LIM from rotary type IM- A schematic of LIM drive for electric traction development of one sided LIM with back iron-field analysis of a DSLIM fundamental assumptions.

UNIT-V:

Permanent Magnet Axial Flux (Pmaf) Machines: Construction, Armature windings – Toroidal Stator and Trapezoidal Stator Windings, Torque and EMF equations, Phasor diagram and output equation.

TEXT BOOKS:

1. Special electrical machines, K. Venkataratnam, - University press.
2. Special electrical machines, E. G. Janardanan, - PHI.
3. R. K. Rajput, "Electrical machines"-5th edition.
4. V. V. Athani, "Stepper motor : Fundamentals , Applications and Design"- New age International pub.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

HVDC TRANSMISSION
(Elective-I)

UNIT-I:

Introduction: General consideration, Power Handling Capabilities of HVDC Lines Basic Conversion principles, static converter configuration.

UNIT-II:

Static Power Converters: 3-pulse, 6-pulse, and 12-pulse converters, converter station and Terminal equipment, commutation process, Rectifier and inverter operation, equivalent circuit for converter – special features of converter transformers. Harmonics in HVDC Systems, Harmonic elimination, AC and DC filters. VSC based HVDC and Hybrid HVDC systems. Back to back thyristor converter system.

UNIT-III:

Control of HVDC Converters and Systems: Constant current, constant extinction angle and constant ignition angle control Individual phase control and equidistant firing angle control DC power flow control. Interaction between HV AC and DC systems – Voltage interaction Harmonic instability problems and DC power modulation.

UNIT-IV:

MTDC Systems & Over Voltages: Series parallel and series parallel systems their operation and control. Over voltages due to disturbances on DC side, over voltages due to DC and AC side line faults.

UNIT-V:

Converter Faults & Protection: Converter faults, over current protection – valve group, and DC line protection over voltage protection of converters, surge arresters.

REFERENCE BOOKS:

1. E.W. Kimbark: Direct current Transmission, Wiley Inter Science – New York.
2. J. Arillaga HVDC Transmission Peter Peregrinus Ltd. London UK 1983
3. KR Padiyar : High Voltage Direct current Transmission Wiley Eastern Ltd New Delhi – 1992.
4. E. Uhlman : Power Transmission by Direct Current , Springer Verlag, Berlin Helberg. 1985.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

PROGRAMMABLE LOGIC CONTROLLERS AND THEIR APPLICATIONS
(Elective-I)

Course Objectives:

- To understand the generic architecture and constituent components of a Programmable Logic Controller.
- To develop a software program using modern engineering tools and technique for PLC.
- To apply knowledge gained about PLCs to identify few real life industrial applications

Learning Outcomes: Students will be able to

- Develop and explain the working of PLC with the help of a block diagram.
- Execute, debug and test the programs developed for digital and analog operations.
- Reproduce block diagram representation on industrial applications using PLC.

UNIT-I:

PLC Basics PLC system, I/O modules and interfacing CPU processor programming equipment programming formats, construction of PLC ladder diagrams, devices connected to I/O modules.

UNIT-II:

PLC Programming input instructions, outputs, operational procedures, programming examples using contacts and coils. Drill-press operation.

Digital logic gates programming in the Boolean algebra system, conversion examples Ladder diagrams for process control Ladder diagrams and sequence listings, ladder diagram construction and flow chart for spray process system.

UNIT-III:

PLC Registers: Characteristics of Registers module addressing holding registers input registers, output registers. PLC Functions Timer functions and industrial applications counters counter function industrial applications, Architecture functions, Number comparison functions, number conversion functions.

UNIT-IV:

Data handling functions: SKIP, Master control Relay Jump Move FIFO, FAL, ONS, CLR and Sweep functions and their applications. Bit Pattern and changing a bit shift register, sequence functions and applications, controlling of two axes and three axis Robots with PLC, Matrix functions.

UNIT-V:

Analog PLC operation: Analog modules and systems Analog signal processing multi bit data processing , analog output application examples, PID principles position indicator with PID control, PID modules, PID tuning, PID functions

REFERENCE BOOKS:

1. Programmable Logic Controllers – Principle and Applications by John W Webb and Ronald A Reiss Fifth edition, PHI
2. Programmable Logic Controllers – Programming Method and Applications by JR Hackworth and F.D Hackworth – Jr- Pearson, 2004.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

MICROCONTROLLERS AND APPLICATIONS
(Elective-II)

Course Objectives:

- The aim of this course is to introduce Microcontroller Intel 8051, Controller 68HC11, PIC Microcontrollers and their applications,
- To study the architecture of 8051, 68HC11, 16C74, their addressing modes and Instruction sets,
- To introduce the need and use of Interrupt structure, timers and to be acquainted with the applications.

Learning Outcomes: At the end of the course, the student is expected to possess knowledge and achieve skills on the following.

- A solid understanding of the fundamental hardware layout of a microprocessor and a microcontroller.
- Working knowledge in ports and interrupts.
- A comfort level in assembly language and C programming for microcontrollers.

UNIT-I:

Overview of Architecture & Microcontroller Resources: Architecture of a microcontroller – Microcontroller resources – Resources in advanced and next generation microcontrollers – 8051 microcontroller – Internal and External memories – Counters and Timers – Synchronous serial-cum asynchronous serial communication - Interrupts.

UNIT-II:

8051- Microcontrollers Instruction Set : Basic assembly language programming – Data transfer instructions – Data and Bit-manipulation instructions – Arithmetic instructions – Instructions for Logical operations on the test among the Registers, Internal RAM, and SFRs – Program flow control instructions – Interrupt control flow.

UNIT-III:

Real Time Control: Interrupts: Interrupt handling structure of an MCU – Interrupt Latency and Interrupt deadline – Multiple sources of the interrupts – Non-maskable interrupt sources – Enabling or disabling of the sources – Polling to determine the interrupt source and assignment of the priorities among them – Interrupt structure in Intel 8051.

Timers: Programmable Timers in the MCU's – Free running counter and real time control – Interrupt interval and density constraints.

UNIT-IV:

Systems Design: Digital And Analog Interfacing Methods: Switch, Keypad and Keyboard interfacing – LED and Array of LEDs – Keyboard-cum-Display controller (8279) – Alphanumeric Devices – Display Systems and its interfaces – Printer interfaces – Programmable instruments interface using IEEE 488 Bus – Interfacing with the Flash Memory – Interfaces – Interfacing to High Power Devices – Analog input interfacing – Analog output interfacing – Optical motor shaft encoders – Industrial control – Industrial process control system – Prototype MCU based Measuring instruments – Robotics and Embedded control – Digital Signal Processing and digital filters.

UNIT-V:

Real Time Operating System For Microcontrollers: Real Time operating system – RTOS of Keil (RTX51) – Use of RTOS in Design – Software development tools for Microcontrollers.

16-Bit Microcontrollers: Hardware – Memory map in Intel 80196 family MCU system – IO ports – Programmable Timers and High-speed outputs and input captures – Interrupts – instructions. ARM 32 Bit MCUs: Introduction to 16/32 Bit processors – ARM architecture and organization – ARM / Thumb programming model – ARM / Thumb instruction set –Development-tools.

TEXT BOOKS:

1. Raj Kamal, "Microcontrollers Architecture, Programming, Interfacing and System Design" – Pearson Education, 2005.



2. Mazidi and Mazidi, "The 8051 Microcontroller and Embedded Systems" – PHI, 2000.

REFERENCE BOOKS:

1. A.V. Deshmuk, "Microcontrollers (Theory & Applications)" – WTMH, 2005.

2. John B. Peatman, "Design with PIC Microcontrollers" – Pearson Education, 2005.

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

EMBEDDED SYSTEMS
(Elective-II)

UNIT- I:

Overview of Embedded System: Embedded System, types of Embedded System, Requirements of Embedded System, and Issues in Embedded software development, Applications.

UNIT-II:

Processor & Memory Organization: Structural units in a processor, Processor selection, Memory devices, Memory selection, Memory Allocation & Map, Interfacing.

UNIT-III:

Devices, Device Drivers & Buses For Device Networks: I/O devices, Timer & Counter devices, Serial Communication, Communication between devices using different buses. Device drives, Parallel and serial port device drives in a system, Interrupt servicing mechanism, context and periods for context switching, Deadline and Interrupt Latency.

UNIT-IV:

Programming & Modeling Concepts : Program elements, Modeling Processes for Software Analysis, Programming Models, Modeling of Multiprocessor Systems, Software algorithm Concepts, design, implementation, testing, validating, debugging, Management and maintenance, Necessity of RTOS.

UNIT-V:

Hardware and Software Co-Design: Embedded system design and co design issues in software development, design cycle in development phase for Embedded System, Use of ICE & Software tools for development of **ES**, **Issues in embedded system design.**

REFERENCE BOOKS:

1. Embedded Systems: Architecture, Programming and Design – Rajkamal, TMH 2003.
2. Programming for Embedded System: DreamTech Software Team-John Wiley -2002



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

DIGITAL CONTROL SYSTEMS
(Elective-II)

UNIT – I:

Introduction: Block Diagram of typical control system- advantages of sampling in control systems – examples of discrete data and digital systems – data conversion and quantization – sample and hold devices – D/A and A/D conversion – sampling theorem – reconstruction of sampled signals –ZOH.

Z-transform: Definition and evaluation of Z-transforms – mapping between s-plane and z-plane – inverse z-plane transform – theorems of the Z-transforms –limitations of z-transforms –pulse transfer function –pulse transfer function of ZOH –relation between $G(s)$ and $G(z)$ – signal flow graph method applied to digital systems.

UNIT- II:

State Space Analysis: State space modeling of digital systems with sample and hold – state transition equation of digital time in variant systems – solution of time in variant discrete state equations by the Z-Transformation – transfer function from the state model – Eigen values – Eigen vector and diagonalisation of the A-matrix – Jordan canonical form. Computation of state transition matrix-Transformation to phase to variable canonical form-The state diagram – decomposition of digital system – Response of sample data system between sampling instants using state approach.

Stability: Definition of stability – stability tests – The second method of Liapunov.

UNIT- III:

Time Domain Analysis : Comparison of time response of continuous data and digital control systems- correlation between time response and root locus in the s-plane and z-plane – effect of pole-zero configuration in the z-plane upon the maximum overshoot and peak time of transient response – Root loci for digital control systems – steady state error analysis of digital control systems – Nyquits plot – Bode plot-G.M and P.M.

UNIT- IV:

Design: The digital control design with digital controller with bilinear transformation – Digital PID controller-Design with deadbeat response-Pole placement through state feedback-Design of full order state observer-Discrete Euler Lagrange Equation – Discrete maximum principle.

UNIT-V:

Digital State Observer: Design of - Full order and reduced order observers. Design by max.principle: Discrete Euler language equation-discrete maximum principle.

TEXT BOOKS:

1. Discrete-Time Control systems - K. Ogata, Pearson Education/PHI, 2nd Edition.
2. Digital Control and State Variable Methods by M.Gopal, TMH.

REFERENCE BOOKS:

1. Digital Control Systems, Kuo, Oxford University Press, 2nd Edition, 2003.
2. Digital Control Engineering, M.Gopal



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

OPTIMIZATION TECHNIQUES
(Open Elective - I)

Course Objectives:

- To understand the theory of optimization methods and algorithms developed for solving various types of optimization problems.
- To develop an interest in applying optimization techniques in problems of Engineering and Technology
- To apply the mathematical results and numerical techniques of optimization theory to concrete Engineering problems.

Course Outcomes: Upon the completion of this course, the student will be able to

- Know basic theoretical principles in optimization
- formulate optimization models and obtain solutions for optimization;
- apply methods of sensitivity analysis and analyze post processing of results

UNIT – I

Introduction and Classical Optimization Techniques: Statement of an Optimization problem – design vector – design constraints – constraint surface – objective function – objective function surfaces – classification of Optimization problems.

Classical Optimization Techniques: Single variable Optimization – multi variable Optimization without constraints – necessary and sufficient conditions for minimum/maximum – multivariable Optimization with equality constraints.

Solution by method of Lagrange multipliers – multivariable Optimization with inequality constraints – Kuhn – Tucker conditions.

UNIT – II

Linear Programming: Standard form of a linear programming problem – geometry of linear programming problems – definitions and theorems – solution of a system of linear simultaneous equations – pivotal reduction of a general system of equations – motivation to the simplex method – simplex algorithm.

UNIT – III

Transportation Problem: Finding initial basic feasible solution by north – west corner rule, least cost method and Vogel's approximation method – testing for optimality of balanced transportation problems.

Unconstrained Nonlinear Programming: One – dimensional minimization methods: Classification, Fibonacci method and Quadratic interpolation method

UNIT – IV

Unconstrained Optimization Techniques: Univariate method, Powell's method and steepest descent method.

Constrained Nonlinear Programming: Characteristics of a constrained problem, Classification, Basic approach of Penalty Function method; Basic approach of Penalty Function method; Basic approaches of Interior and Exterior penalty function methods. Introduction to convex Programming Problem.

UNIT – V

Dynamic Programming: Dynamic programming multistage decision processes – types – concept of sub optimization and the principle of optimality – computational procedure in dynamic programming – examples illustrating the calculus method of solution - examples illustrating the tabular method of solution.

TEXT BOOKS:

1. "Engineering optimization: Theory and practice"-by S. S.Rao, New Age International (P) Limited, 3rd edition, 1998.
2. "Introductory Operations Research" by H.S. Kasene & K.D. Kumar, Springer(India), Pvt .LTd.



REFERENCES:

1. "Optimization Methods in Operations Research and systems Analysis" – by K.V. Mital and C. Mohan, New Age International (P) Limited, Publishers, 3rd edition, 1996.
2. Operations Research – by Dr. S.D.Sharma.
3. "Operations Research: An Introduction" by H.A. Taha, PHI Pvt. Ltd., 6th edition
4. Linear Programming by G. Hadley

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

DIGITAL CONTROL SYSTEMS
(Open Elective - I)

Course Objectives:

- To explain basic and digital control system for the real time analysis and design of control systems.
- To apply the knowledge state variable analysis in the design of discrete systems.
- To explain the concept of stability analysis and design of discrete time systems.

Course Outcomes: Upon the completion of this course, the student will be able to

- Apply the concepts of Digital control systems.
- Analyze and design of discrete systems in state variable analysis.
- To relate the concepts of stability analysis and design of discrete time systems.

UNIT – I:

Concept & Representation of Discrete time Systems: Block Diagram of typical control system- advantages of sampling in control systems – examples of discrete data and digital systems – data conversion and quantization – sample and hold devices – D/A and A/D conversion – sampling theorem – reconstruction of sampled signals.

Z-transform: Definition of Z-transforms – mapping between s-plane and z-plane – inverse z- transform – properties of z-transforms - ROC of z-transforms –pulse transfer function –relation between $G(s)$ and $G(z)$ – signal flow graph method applied to digital control systems.

UNIT- II:

State Space Analysis: State space modeling of discrete time systems – state transition equation of discrete time invariant systems – solution of time invariant discrete state equations: recursive method and the Z-Transformation method – conversion of pulse transfer function to the state model & vice-versa – Eigen values – Eigen vectors of discrete time system-matrix (A) – Realization of pulse transformation in state space form, discretization of continuous time systems, Computation of state transition matrix and its properties. Response of sample data system between sampling instants.

UNIT – III:

Controllability, Observability & Stability Tests: Concept of controllability, stabilizability, observability and reachability - Controllability and observability tests, Transformation of discrete time systems into controllable and observable forms.

Stability: Definition of stability – stability tests – The second method of Liapunov.

UNIT- IV:

Design of discrete time Controllers and observers: Design of discrete time controller with bilinear transformation – Realization of digital PID controller-Design of deadbeat controller; Pole placement through state feedback.

UNIT-V:

State Observers: Design of - Full order and reduced order observers. Study of observer based control design

TEXT BOOKS:

1. K. Ogata , Discrete-Time Control systems, Pearson Education/PHI, 2nd Edition.
2. V. I. George, C. P. Kurian, Digital Control Systems, Cengage Learning.
3. M.Gopal, Digital Control Engineering, New Age Int. Pvt. Ltd., 2014

REFERENCES:

1. Kuo, Digital Control Systems, Oxford University Press, 2nd Edition, 2003.
2. M.Gopal , Digital Control and State Variable Methods, TMH.
3. M. Sami Fadali Antonio Visioli, Digital Control Engineering Analysis and Design, Academic Press



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

RENEWABLE ENERGY SYSTEMS
(Open Elective - I)

Course Objectives:

- To recognize the awareness of energy conservation in students
- To identify the use of renewable energy sources for electrical power generation
- To collect different energy storage methods
- To detect about environmental effects of energy conversion

Course Outcomes: Upon the completion of this course, the student will be able to

- find different renewable energy sources to produce electrical power
- estimate the use of conventional energy sources to produce electrical energy
- role-play the fact that the conventional energy resources are depleted
- arrange Store energy and to avoid the environmental pollution

UNIT-I:

Photo voltaic power generation ,spectral distribution of energy in solar radiation, solar cell configurations, voltage developed by solar cell, photo current and load current, practical solar cell performance, commercial photo voltaic systems, test specifications for PV systems, applications of super conducting materials in electrical equipment systems.

UNIT-II:

Principles of MHD power generation, ideal MHD generator performance, practical MHD generator, MHD technology.

Wind Energy conversion: Power from wind, properties of air and wind, types of wind Turbines, operating characteristics.

UNIT-III:

Tides and tidal power stations, modes of operation, tidal project examples, turbines and generators for tidal power generation.

Wave energy conversion: properties of waves and power content, vertex motion of Waves, device applications. Types of ocean thermal energy conversion systems Application of OTEC systems examples,

UNIT-IV:

Miscellaneous energy conversion systems: coal gasification and liquefaction, biomass conversion, geothermal energy, thermo electric energy conversion, principles of EMF generation, description of fuel cells, Co-generation and energy storage, combined cycle co-generation, energy storage.

Global energy position and environmental effects: energy units, global energy position.

UNIT-V:

Types of fuel cells, H₂-O₂ Fuel cells, Application of fuel cells – Batteries, Description of batteries, Battery application for large power. Environmental effects of energy conversion systems, pollution from coal and preventive measures steam stations and pollution, pollution free energy systems.

TEXT BOOKS:

1. "Energy conversion systems" by Rakosh das Begamudre, New age International publishers, New Delhi - 2000.
2. "Renewable Energy Resources" by John Twidell and Tony Weir, 2nd Edition, Fspan & Co.

REFERENCES:

1. "Understanding Renewable Energy Systems" , by Volker Quaschnig, 2005, UK.
2. "Renewable Energy Systems-Advanced Conversion, Technologies & Applications" by Faner Lin Luo Honer Ye, CRC press, Taylor & Francis group.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

HVDC TRANSMISSION
(Open Elective - I)

Prerequisite: Power Electronics and Power Systems

Course Objectives:

- To Comprehend the conversion principles of HVDC Transmission
- Analysis of 3, 6, 12 pulse converters, rectifier and inverter operations of HVDC converters
- To identify the different types of Harmonics and reduction by using Filters
- To comprehend interaction between HVAC and DC systems in various aspects
- To appreciate the reliable MTDC systems and protection of HVDC system

Course Outcomes: Upon the completion of this course, the student will be able to

- To find the applications of HVDC transmission in the power system with the acquired knowledge.
- To analyze different converter topologies viz. 3, 6 and 12 Pulse converters and understand it's control aspects.
- To understand the filter configuration for Harmonics in HVDC systems.
- To appreciate the reliable Multi terminal HVDC system.
- To have knowledge on the Protection of HVDC systems against Transient over voltages and over currents.

UNIT-I:

Introduction: General consideration, Power Handling Capabilities of HVDC Lines Basic Conversion principles, static converter configuration.

UNIT-II:

Static Power Converters: 3-pulse, 6-pulse, and 12-pulse converters, converter station and Terminal equipment, commutation process, Rectifier and inverter operation, equivalent circuit for converter – special features of converter transformers. Harmonics in HVDC Systems, Harmonic elimination, AC and DC filters.

UNIT-III:

Control of HVDC Converters and Systems: Constant current, constant extinction angle and constant ignition angle control Individual phase control and equidistant firing angle control DC power flow control. Interaction between HV AC and DC systems – Voltage interaction Harmonic instability problems and DC power modulation.

UNIT-IV:

MTDC Systems & Over Voltages: Series parallel and series parallel systems their operation and control.

Over voltages due to disturbances on DC side, over voltages due to DC and AC side line faults.

UNIT-V:

Converter Faults & Protection: Converter faults, over current protection – valve group, and DC line protection over voltage protection of converters, surge arresters.

TEXT BOOKS:

1. E.W. Kimbark: Direct current Transmission, Wiley Inter Science – New York
2. KR Padiyar : High Voltage Direct current Transmission Wiley Esatern Ltd New Delhi – 1992.

REFERENCES:

1. J. Arillaga HVDC Transmission Peter Peregrinus Ltd. London UK 1983
2. E. Uhlman : Power Transmission by Direct Current , Springer Verlag, Berlin Helberg. 1985.
3. S. Rao "EHVAC and HVDC Transmission Engg. Practice" Khanna publishers.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

ANALYSIS OF POWER ELECTRONIC CONVERTERS
(Open Elective - I)

Prerequisite: Power Electronics

Course Objectives:

- To comprehend the concepts of power converters and their applications.
- To describe the importance of AC voltage controllers and cyclo converters for various industrial applications
- To analyze and design switch mode power electronic converters for various applications including microprocessor power supplies, renewable energy systems, and motor drives.
- To analyze pulse width modulated inverters which are used in variable speed drives

Learning Outcomes: Upon the completion of this course, the student will be able to

- To understand of the basic principles of switch mode power conversion.
- To understand the operating principles and models of different types of power electronic converters including dc-dc converters, PWM rectifiers and inverters
- To choose appropriate power converter topologies and design the power stage and feedback controllers for various applications

Unit I:

Single Phase AC Voltage Controllers: Single phase AC voltage controllers with Resistive, Resistive-inductive and Resistive-inductive-induced e.m.f. loads - ac voltage controllers with PW Control - Effects of source and load inductances - Synchronous tap changers-Applications - numerical problems.

Unit II

Three Phase AC Voltage Controllers: Three phase AC voltage controllers - Analysis of controllers with star and delta Connected Resistive, Resistive-inductive loads - Effects of source and load Inductances - applications - numerical problems.

Cycloconverters: Single phase to single phase cycloconverters - analysis of midpoint and bridge Configurations - Three phase to three phase cycloconverters - analysis of Midpoint and bridge configurations - Limitations - Advantages - Applications- numerical problems.

Unit III

Single Phase Converters: Single phase converters - Half controlled and Fully controlled converters -Evaluation of input power factor and harmonic factor - continuous and Discontinuous load current - single phase dual converters - power factor Improvements - Extinction angle control - symmetrical angle control - PWM -single phase sinusoidal PWM - single phase series converters - Applications -Numerical problems.

Three Phase Converters: Three phase converters - Half controlled and fully controlled converters - Evaluation of input power factor and harmonic factor - continuous and Discontinuous load current - three phase dual converters - power factor Improvements - three phase PWM - twelve pulse converters - applications -Numerical problems.

Unit VI

D.C. to D.C. Converters: Analysis of step-down and step-up dc to dc converters with resistive and Resistive-inductive loads - Switched mode regulators - Analysis of Buck Regulators - Boost regulators - buck and boost regulators - Cuk regulators - Condition for continuous inductor current and capacitor voltage - comparison of regulators -Multioutput boost converters - advantages - applications - Numerical problems.

Unit V

Pulse Width Modulated Inverters (single phase): Principle of operation - performance parameters - single phase bridge inverter -evaluation of output voltage and current with resistive, inductive and Capacitive loads - Voltage control of single phase inverters - single PWM - Multiple PWM - sinusoidal PWM - modified PWM - phase displacement Control - Advanced modulation techniques for improved



performance - Trapezoidal, staircase, stepped, harmonic injection and delta modulation - Advantage - application - numerical problems.

Pulse Width Modulated Inverters (three phase): Three phase inverters - analysis of 180 degree condition for output voltage And current with resistive, inductive loads - analysis of 120 degree Conduction - voltage control of three phase inverters - sinusoidal PWM - Third Harmonic PWM – 60 degree PWM - space vector modulation -Comparison of PWM techniques - harmonic reductions - Current Source Inverter - variable d.c. link inverter - boost inverter - buck and boost inverter - inverter circuit design - advantages -applications - numerical problems.

TEXT BOOKS:

1. Power Electronics - Mohammed H. Rashid - Pearson Education -Third Edition - First Indian reprint 2004.
2. Power Electronics - Ned Mohan, Tore M. Undeland and William P. Robbins – John Wiley and Sons - Second Edition.

REFERENCES:

1. Power Electronics Daniel W. Hart
2. Fundamentals of Power Electronics, 2nd Edition. R.W. Erickson
3. The power electronics Hand Book Timothy, L. Skvarenina, Purdue University



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

EMBEDDED SYSTEMS
(Open Elective - I)

Prerequisite: Microprocessors and Interfacing Devices

Course Objectives:

- To emphasize the general embedded system concepts, design of embedded hardware and software development tools
- To explain the basics of real time operating and embedded systems
- To describe key issues such as CPU scheduling, memory management, task synchronization, and file system in the context of real-time embedded systems.

Course Outcomes: Upon the completion of this course, the student will be able to

- To analyze and design embedded systems and real-time systems
- Define the unique design problems and challenges of real-time systems
- Identify the unique characteristics of real-time operating systems and evaluate the need for real-time operating system
- Explain the general structure of a real-time system and Understand and use RTOS to build an embedded real-time system
- Gain knowledge and skills necessary to design and develop embedded applications based on real-time operating systems.

UNIT- I:

Overview Of Embedded System: Embedded System, types of Embedded System, Requirements of Embedded System, and Issues in Embedded software development, Applications.

UNIT-II:

Processor & Memory Organization: Structural units in a processor, Processor selection, Memory devices, Memory selection, Memory Allocation & Map, Interfacing.

UNIT-III:

Devices, Device Drivers & Buses for Device Networks: I/O devices, Timer & Counter devices, Serial Communication, Communication between devices using different buses. Device drives, Parallel and serial port device drives in a system, Interrupt servicing mechanism, context and periods for context switching, Deadline and Interrupt Latency.

UNIT-IV:

Programming & Modeling Concepts : Program elements, Modeling Processes for Software Analysis, Programming Models, Modeling of Multiprocessor Systems, Software algorithm Concepts, design, implementation, testing, validating, debugging, Management and maintenance, Necessity of RTOS.

UNIT-V:

Hardware and Software Co-Design: Embedded system design and co-design issues in software development, design cycle in development phase for Embedded System, Use of ICE & Software tools for development of ES, Issues in embedded system design.

TEXTBOOKS:

1. Embedded systems: Architecture, programming and design by Rajkamal, TMH
2. Embedded system design by Arnold S Burger, CMP

REFERENCES:

1. An embedded software primer by David Simon, PEA
2. Embedded systems design:Real world design be Steve Heath; Butterworth Heinenann, Newton mass USA 2002
3. Data communication by Hayt.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

POWER CONVERTERS SIMULATION LAB

PART A:

1. Single phase full converter using RL and E loads.
2. Three phase full converter using RL and E loads.
3. Single phase AC Voltage controller using RL load.
4. Three-phase inverter with PWM controller.
5. DC-DC Converters.
6. Modeling of Separately Excited DC Motor.
7. Modeling of Three Phase Induction Motor.

PART B:

8. Write program and simulate dynamical system of following models:
 - i. I/O Model
 - ii. State variable modelAlso identify time domain specifications of each.
9. Obtain frequency response of a given system by using various methods:
 - i. General method of finding the frequency domain specifications.
 - ii. Polar plot
 - iii. Bode plot
 - iv. Also obtain the Gain margin and Phase margin.
10. Determine stability of a given dynamical system using following methods.
 - i. Root locus
 - ii. Bode plot
 - iii. Nyquist plot
 - iv. Liapunov stability criteria
11. Transform a given dynamical system from I/O model to state variable model and vice versa.
12. Design a compensator for a given systems for required specifications.
13. Design a PID controller based on Bode plot.
14. Develop a program to solve Swing Equation.

Notes: Use the suitable software for each simulation.

Any ten experiments, Six from PART A and Four from PART B, can be selected from the above list.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH (POWER ELECTRONICS/
POWER AND INDUSTRIAL DRIVES/POWER ELECTRONICS AND ELECTRIC DRIVES)
COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Power Electronic Converters	25	75	4	--	4
Core Course V	Power Electronic Control of DC Drives	25	75	4	--	4
Core Course VI	Power Electronic Control of AC Drives	25	75	4	--	4
Core Elective III	1. Power Quality 2. Advanced Digital Signal Processing 3. Switched Mode Power Supplies (SMPS)	25	75	4	--	4
Core Elective IV	1. Flexible AC Transmission Systems 2. High-Frequency Magnetic Components 3. Dynamics of Electrical Machines	25	75	4	--	4
Open Elective II	1. Instrumentation & Control 2. Intelligent Control 3. Smart grid technologies 4. AI Techniques in Electrical Engineering 5. Reliability Engineering 6. Energy Auditing, Conservation & Management	25	75	4	--	4
Laboratory II	Power Converters and Drives Lab	25	75	--	4	2
Seminar II	Seminar-II	50	--	--	4	2
Total Credits				24	8	28



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER ELECTRONIC CONVERTERS

Course Objectives

- To understand various PWM techniques for inverters and corresponding T.H.D
- To describe the operation of multi level inverters with switching strategies for high power applications
- To appreciate the design of resonant converters and switch mode power supplies.

Outcomes:

- Students will be able to develop various converter topologies and analyze it and can identify the corresponding T.H.D.
- AC or DC switched mode power supplies can be designed.

UNIT-I:

PWM Inverters (Single-Phase & Three-Phase): Principle of operation – performance parameters – single phase bridge inverter – evaluation of output voltage and current with resistive, inductive and Capacitive loads – Voltage control of single phase inverters – single PWM – Multiple PWM – sinusoidal PWM – modified PWM – phase displacement Control – Advanced modulation techniques for improved performance – Trapezoidal, staircase, stepped, harmonic injection and delta modulations – Advantage – application.

Three phase inverters – analysis of 180 degree condition for output voltage And current with resistive, inductive loads – analysis of 120 degree Conduction – voltage control of three phase inverters – sinusoidal PWM – Third Harmonic PWM – 60 degree PWM – space vector modulation – Comparison of PWM techniques – harmonic reductions – Current Source Inverter – variable DC link inverter – buck and boost inverter – inverter circuit design – advantage applications.

UNIT-II:

Resonant Pulse Inverters: Resonant pulse inverters – series resonant inverters – series resonant inverters with unidirectional switches – series resonant inverters with bidirectional Switches – analysis of half bridge resonant inverter - evaluation of currents and Voltages of a simple resonant inverter – analysis of half bridge and full bridge resonant inverter with bidirectional switches – Frequency response of series resonant inverters – for series loaded inverter – for parallel loaded inverter – For series and parallel loaded inverters – parallel resonant inverters – Voltage control of resonant inverters – class E inverter and Class E rectifier.

Resonant converters: Resonant converters – Zero current switching resonant converters – L type ZCS resonant converter – M type ZCS resonant converter – zero voltage Switching resonant converters – comparison between ZCS and ZVS resonant Converters – Two quadrant ZVS resonant converters – resonant de-link Inverters – evaluation of L and C for a zero current switching inverter.

UNIT-III:

Multilevel Inverters: Multilevel concept – Classification of multilevel inverters – Diode clamped multilevel inverter – principle of operation – main features – improved diode Clamped inverter – principle of operation – Flying capacitors multilevel inverter – principle of operation – main features. Cascaded multilevel inverter – principle of operation – main features – Multilevel inverter applications – reactive power compensation – back to back intertie system – adjustable drives – Switching device currents – de link capacitor voltage balancing – features of Multilevel inverters – comparisons of multilevel converters.

UNIT-IV:

DC Power Supplies: DC power supplies – classification – switched mode dc power supplies – fly back Converter – forward converter – push-pull converter – half bridge converter – Full bridge converter – Resonant dc power supplies – bidirectional power supplies – Applications.

UNIT-V:

AC Power Supplies: AC power supplies – classification – switched mode ac power supplies – Resonant AC power supplies – bidirectional ac power supplies – multistage conversions – control circuits – applications. Introduction – power line disturbances – power conditioners – uninterruptible Power supplies – applications.



TEXT BOOKS:

1. Power Electronics – Mohammed H. Rashid – Pearson Education – Third Edition.
2. Power Electronics – Ned Mohan, Tore M. Undeland and William P. Robbins – John Wiley and Sons – Second Edition.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER ELECTRONIC CONTROL OF DC DRIVES

Course Objectives

- Introduction of drive system and characteristics of drive ,operating modes of drive
- Comprehend the principle o p e r a t i o n of phase control and Chopper controlled of dc drives
- Design a current and speed controllers to achieve closed loop operation of dc drive

Outcomes

- Students will be able to perform simulations of phase or chopper controlled dc drive both for open loop and closed loop operations.
- Student can choose proper gain values for speed and current controllers.
- To Comprehend the difference between pwm controller and hysteresis controller

UNIT-I:

Single-Phase Rectifiers Controlled DC Motor: Separately excited DC motors with rectified single – phase supply – single-phase semi converter and single phase full converter for continuous and discontinuous modes of operation – power and power factor.

UNIT-II:

Three-Phase Rectifiers Controlled DC Motor: Three-phase semi-converter and Three phase full converter for continuous and discontinuous modes of operations – power and power factor - Addition of Free wheeling diode – Three phase double converter.

Three phase controlled bridge rectifier with passive load impedance, resistive load and ideal supply – Highly inductive load and ideal supply for load side and supply side quantities, shunt capacitor compensation, three phase controlled bridge rectifier inverter.

UNIT-III:

Phase, Current & Speed Controlled DC Drive: Three-phase controlled converter, control circuit, control modeling of three phase converter – Steady state analysis of three phase converter control DC motor drive – Two quadrant, Three phase converter controlled DC motor drive – DC motor and load, converter.

Current and speed controllers - Current and speed feedback – Design of controllers – Current and speed controllers – Motor equations – filter in the speed feedback loop speed controller – current reference generator – current controller and flow chart for simulation – Harmonics and associated problems – sixth harmonics torque.

UNIT-IV:

Chopper Controlled DC Motor Drives: Principle of operation of the chopper – Chopper with other power devices – model of the chopper – input to the chopper – steady state analysis of chopper controlled DC motor drives – rating of the devices – Pulsating torque.

Closed loop operation: Speed controlled drive system – current control loop – pulse width modulated current controller – hysteresis current controller – modeling of current controller – design of current controller.

UNIT-V: FOUR QUADRANT OPERATION OF DC DRIVES

Introduction to Four quadrant operation – Motoring operations, Electric Braking – Plugging, Dynamic and Regenerative Braking operations. Four quadrant operation of D.C motors by single phase, three phase dual converters and Choppers – Closed loop operation of DC motor.

REFERENCES:

1. Power Electronics and motor control – Shepherd, Hulley, Liang – II Edition Cambridge University Press.
2. Electronic motor drives modeling Analysis and control – R. Krishnan – I Edition Prentice Hall India.
3. Power Electronics circuits, Devices and Applications – MH Rashid – PHI – 1 Edition 1995.
4. Fundamentals of Electric Drives – GK Dubey, Narosa Publishers 1995
5. Power Semiconductor drives – SB Dewan and A Straughen -1975.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER ELECTRONIC CONTROL OF AC DRIVES

Objectives

- To understand principle operation of scalar control of ac motor and corresponding speed-torque-slip characteristics
- To comprehend the vector control for ac motor drive (IM and SM)
- To explain the static resistance control and Slip power recovery drive
- To explain synchronous motor drive characteristics and its control strategies
- To Comprehend the brushless dc motor principle of operation.

Outcomes

- Students will be able to develop induction motor for variable speed operations using scalar and vector control techniques.
- To identify the difference between the rotor resistance control and static rotor resistance control method and significance of slip power recovery drives
- Controllers for synchronous motor and variable reluctance motor can be developed

UNIT-I:

Introduction : Introduction to motor drives – Torque production – Equivalent circuit analysis – Speed – Torque Characteristics with variable voltage operation Variable frequency operation constant v/t operation – Variable stator current operation – Induction motor characteristics in constant torque and field weakening regions.

UNIT-II:

Stator Side Control of Induction Drives: Scalar control – Voltage fed inverter control – Open loop volts/Hz control – speed control slip regulation – speed control with torque and flux control – current controlled voltage fed inverter drive – current – fed inverter control – Independent current and frequency control – Speed and flux control in Current –Fed inverter drive – Volts/Hz control of Current –fed inverter drive – Efficiency optimization control by flux program.

UNIT-III:

Rotor Side Control of Induction Drives : Slip power recovery drives – Static Kramer Drive – Phasor diagram – Torque expression – speed control of Kramer Drive – Static Scheribus Drive – modes of operation.

Vector control of Induction Motor Drives: Principles of Vector control – Vector control methods – Direct methods of vector control – Indirect methods of vector control – Adaptive control principles – Self tuning regulator Model referencing control.

UNIT-IV:

Control of Synchronous Motor Drives: Synchronous motor and its characteristics – Control strategies – Constant torque angle control – Unity power factor control – Constant mutual flux linkage control.

Controllers: Flux weakening operation – Maximum speed – Direct flux weakening algorithm – Constant Torque mode controller – Flux Weakening controller – indirect flux weakening – Maximum permissible torque – speed control scheme – Implementation strategy speed controller design.

UNIT-V:

Variable Reluctance Motor Drive: Variable Reluctance motor drive – Torque production in the variable reluctance motor Drive characteristics and control principles – Current control variable reluctance motor service drive.

Brushless DC Motor Drives: Three phase full wave Brushless dc motor – Sinusoidal type of Brushless dc motor- current controlled Brushless dc motor Servo drive.

REFERENCES:

1. Electric Motor Drives Pearson Modeling, Analysis and control – R. Krishnan – Publications – 1st edition – 2002.
2. Modern Power Electronics and AC Drives B K Bose – Pearson Publications 1st edition



3. Power Electronics and Control of AC Motors – MD Murthy and FG Turn Bull Pergman Press 1st edition
4. Power Electronics and AC Drives – BK Bose – Prentice Hall Eagle wood diffs New Jersey - 1st edition
5. Power Electronic circuits Deices and Applications – M H Rashid – PHI – 1995.
6. Fundamentals of Electrical Drives – G. K. Dubey – Narora publications – 1995.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER QUALITY
(Core Elective – III)

Course Objectives: The course should enable the students to

- Study the basics of power quality , power quality problems and power quality standards,
- Study about the characteristics of non-linear loads ,
- Study Voltage, Current, Power and Energy measurements and analysis methods of Laplace's, Fourier and Hartley and Wavelet Transforms ,
- Study the analysis and conventional mitigation methods,
- Study about various devices used to enhance power quality.

Learning Outcomes: At the end of the course the student should be able to:

- Know the different characteristics of electric power quality in power systems,
- One can learn about the applications of non-linear loads ,
- Know the applications of Hartley and Wavelet Transforms ,
- One can learn to mitigate the power quality problems
- One can learn about the application of FACTS device on DG side.

UNIT-I:

Introduction : Introduction of the Power Quality (PQ) problem, Terms used in PQ: Voltage, Sag, Swell, Surges, Harmonics, over voltages, spikes, Voltage fluctuations, Transients, Interruption, overview of power quality phenomenon, Remedies to improve power quality, power quality monitoring.

UNIT-II:

Long & Short Interruptions: Interruptions – Definition – Difference between failures, outage, Interruptions – causes of Long Interruptions – Origin of Interruptions – Limits for the Interruption frequency – Limits for the interruption duration – costs of Interruption – Overview of Reliability evaluation to power quality, comparison of observations and reliability evaluation.

Short interruptions: definition, origin of short interruptions, basic principle, fuse saving, voltage magnitude events due to re-closing, voltage during the interruption, monitoring of short interruptions, difference between medium and low voltage systems. Multiple events, single phase tripping – voltage and current during fault period, voltage and current at post fault period, stochastic prediction of short interruptions.

UNIT III:

Single and Three-Phase Voltage Sag Characterization : Voltage sag – definition, causes of voltage sag, voltage sag magnitude, and monitoring, theoretical calculation of voltage sag magnitude, voltage sag calculation in non-radial systems, meshed systems, and voltage sag duration.

Three phase faults, phase angle jumps, magnitude and phase angle jumps for three phase unbalanced sags, load influence on voltage sags.

UNIT-IV:

Power Quality Considerations in Industrial Power Systems: Voltage sag – equipment behavior of Power electronic loads, induction motors, synchronous motors, computers, consumer electronics, adjustable speed AC drives and its operation. Mitigation of AC Drives, adjustable speed DC drives and its operation, mitigation methods of DC drives.

UNIT-V:

Mitigation of Interruptions & Voltage Sags: Overview of mitigation methods – from fault to trip, reducing the number of faults, reducing the fault clearing time changing the power system, installing mitigation equipment, improving equipment immunity, different events and mitigation methods. System equipment interface – voltage source converter, series voltage controller, shunt controller, combined shunt and series controller.

Power Quality and EMC Standards: Introduction to standardization, IEC Electromagnetic compatibility standards, European voltage characteristics standards, PQ surveys.



REFERENCE BOOK:

1. "Understanding Power Quality Problems" by Math H J Bollen. IEEE Press.
2. Power Quality VAR Compensation in Power Systems, R. SastryVedam Mulukutla S. Sarma,CRC Press.
3. Power Quality, C. Sankaran, CRC Presss.
4. Electrical Power Systems Quality, Roger C. Dugan , Mark F. Mc Granaghan, Surya Santoso, H. Wayne Beaty, Tata McGraw Hill Education Private Ltd.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

ADVANCED DIGITAL SIGNAL PROCESSING
(Core Elective–III)

Course Objectives: The course will enable the students to:

- (i) Know the basics of discrete random processes
- (ii) Know the basics of various Spectrum estimation methods
- (iii) Know the basics of linear estimators & predictors
- (iv) Know the basics of various adaptive filters along with their applications
- (v) Know the fundamentals of multirate digital signal processing

Learning Outcomes: At the end of the course the students should be able to

- (i) Understand the various theorems & processing that are done on discrete random processes
- (ii) Understand the different parametric & non-parametric spectrum estimation methods
- (iii) Understand the linear predictors & Wiener filters
- (iv) Understand the adaptive filters & their various applications
- (v) Understand the importance of multirate digital signal processing

UNIT-I:

Digital Filter Structures: Block diagram representation – Equivalent Structures – FIR and IIR digital filter Structures All pass Filters-tunable IIR Digital Sine-cosine generator- Computational complexity of digital filter structures.

UNIT-II:

Digital Filter Design : Preliminary considerations- Bilinear transformation method of IIR filter design – design of Low pass high-pass – Band-pass, and Band stop- IIR digital filters – Spectral transformations of IIR filters – FIR filter design –based on Windowed Fourier series – design of FIR digital filters with least – mean square-error – constrained Least –square design of FIR digital filters.

UNIT-III:

DSP Algorithm Implementation : Computation of the discrete Fourier transform- Number representation – Arithmetic operations – handling of overflow – Tunable digital filters – function approximation.

UNIT-IV:

Analysis of Finite Word Length Effects: The Quantization process and errors-Quantization of fixed –point and floating –point Numbers – Analysis of coefficient Quantization effects – Analysis of Arithmetic Round-off errors- Dynamic range scaling – signal –to- noise in Low –order IIR filters- Low – Sensitivity Digital filter – Reduction of Product round-off errors feedback – Limit cycles in IIR digital filter – Round – off errors in FFT Algorithms.

UNIT-V:

Power Spectrum Estimation : Estimation of spectra from Finite Duration Observations signals- Non-parametric methods for power spectrum Estimation- parametric method for power spectrum Estimation- Estimation of spectral form-Finite duration observation of signals- Non-parametric methods for power spectrum estimation – Walsh methods – Blackman and torchy method.

REFERENCE BOOKS:

1. Digital signal processing –sanjit K. Mitra – TMH second edition
2. Discrete Time Signal Processing – Alan V. Oppenheim, Ronald W, Shafer – PHI 1996 1ST Edition reprint
3. Digital Signal Processing principles – algorithms and Applications- john G. Proakis – PHI – 3RD edition 2002.
4. Digital Signal Processing – S Salivahanan. A. Vallavaraj C. Gnanapriya – TMH – 2nd reprint 2001.
5. Theory and Applications of Digital Signal Processing –Lourens R Rebinarand Bernold.
6. Digital Filter Analysis and Design Auntoniam – TMH.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

SWITCHED MODE POWER SUPPLIES (SMPS)
(Core Elective–III)

Course Objectives:

- To apply the basic concepts of power electronics for designing converters.
- Design and implement practical circuits for UPS, SMPS etc.

Learning Outcomes: At the end of the course, the student is expected to possess knowledge and achieve skills on the following:

- Ability to design converter system for electrical applications
- Ability to understand and design SMPS

UNIT – I

Basic Converter Circuits:

Buck Regulator, Buck- Boost Regulator, Boost Regulator, Cuk Converters and Resonant Converters. Choice of switching frequency.

UNIT – II

Isolated SMPS:

Fly back Converter, Forward Converter, Half-Bridge and Full Bridge Converters, Push-Pull Converter and SMPS with multiple outputs. Choice of switching frequency.

UNIT – III

Control Aspects

PWM Controllers, Isolation in feedback loop, Power Supplies with multiple output. Stability analysis using Bode Diagrams.

UNIT – IV

Design Considerations

Selection of output filter capacitor, Selection of energy storage inductor, Design of High Frequency Inductor and High frequency Transformer, Selection of switches. Snubber circuit design, Design of driver circuits.

UNIT – V

Electro Magnetic Interference (EMI)

EMI Filter Components, Conducted EMI suppression, Radiated EMI suppression, Measurement.

Protection

Over current protection, Over voltage protection, Inrush current protection.

Thermal Model

Thermal Resistance, Cooling Considerations, Selection of Heat sinks, Simple Heat sink calculations.

TEXT BOOKS:

- 1) Switched Mode Power Supplies, Design and Construction, H. W. Whittington, B. W. Flynn and D. E. MacPherson, Universities Press, 2009 Edition.
- 2) Mohan N. Undeland . T & Robbins W., Power Electronics Converters, Application and Design. John Wiley, 3rd edition, 2002
- 3) Umanand L., Bhat S.R., Design of magnetic components for switched Mode Power Converters. , Wiley Eastern Ltd.,1992
- 4) Robert. W. Erickson, D. Maksimovic .Fundamentals of Power Electronics., Springer International Edition, 2005
- 5) Course Material on Switched Mode Power Conversion, V. Ramanarayanan.

REFERENCE BOOKS:

- 1) Krein P.T .Elements of Power Electronics., Oxford University Press
- 2) M.H.Rashid, Power Electronics. Prentice-Hall of India



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

FLEXIBLE AC TRANSMISSION SYSTEMS
(Core Elective-IV)

Course Objectives: The course will enable the students

- (i) To get introduced to basic concepts of FACTS controllers.
- (ii) To familiar the students with the working of series compensation .
- (iii) To familiar the students with the working of Unified Power Flow Controller.
- (iv) To expose the students to the designing of FACTS controllers.
- (v) To familiarize the students with static VAR compensators

Learning Outcomes: After completion of the course the students are expected to be able to

- (i) Explain the basic compensators used in power systems.
- (ii) Explain how a series compensation is done in power system
- (iii) Explain the working of Unified Power Flow Controller.
- (iv) Design variable structure of FACTS controllers for power system
- (v) Explain the working of static VAR compensators and their applications in power system

UNIT-I:

Facts Concepts: Transmission interconnections power flow in an AC system, loading capability limits, Dynamic stability considerations, importance of controllable parameters basic types of FACTS controllers, benefits from FACTS controllers.

UNIT-II:

Voltage Source Converters : Single phase three phase full wave bridge converters transformer connections for 12 pulse 24 and 48 pulse operation. Three level voltage source converter, pulse width modulation converter, basic concept of current source Converters, and comparison of current source converters with voltage source converters.

UNIT-III:

Static Shunt Compensation: Objectives of shunt compensation, mid-point voltage regulation voltage instability prevention, improvement of transient stability, Power oscillation damping, Methods of controllable VAR generation, variable impedance type static VAR generators switching converter type VAR generators hybrid VAR generators.

UNIT-IV:

SVC and STATCOM: The regulation and slope transfer function and dynamic performance, transient stability enhancement and power oscillation damping operating point control and summary of compensator control.

UNIT-V:

Static Series Compensators: Concept of series capacitive compensation, improvement of transient stability, power oscillation damping, and functional requirements of GTO thyristor controlled series capacitor (GSC), thyristor switched series capacitor (TSSC), and thyristor controlled series capacitor (TCSC)
Control schemes for GSC TSSC and TCSC.

TEXT BOOKS:

1. "Understanding FACTS Devices" N.G. Hingorani and L. Guygi. IEEE Press Publications 2000.
2. Song, Y.H. and Allan T. Johns, 'Flexible AC Transmission Systems (FACTS)', Institution of Electrical Engineers Press, London, 1999.
3. Mohan Mathur R. and Rajiv K.Varma , 'Thyristor - based FACTS controllers for Electrical transmission systems', IEEE press, Wiley Inter science , 2002.
4. Padiyar K.R., 'FACTS controllers for Transmission and Distribution systems' New Age International Publishers, 1 st Edition, 2007.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

HIGH-FREQUENCY MAGNETIC COMPONENTS
(Core Elective-IV)

UNIT-I:

Fundamentals of Magnetic Devices: Introduction, Magnetic Relationships, Magnetic Circuits, Magnetic Laws, Eddy Currents, Core Saturation, Volt-Second Balance, Inductance, Inductance Factor, Magnetic Energy, Self-Resonant Frequency, Classification of Power Losses in Magnetic Components, Non-inductive Coils.

Magnetic Cores: Introduction, Properties of Core Materials, Magnetic Dipoles, Magnetic Domains, Curie Temperature, Magnetization, Magnetic Materials, Hysteresis, Core Permeability, Core Geometries, Iron Alloy Cores, Amorphous Alloy Cores, Nickel-Iron and Cobalt-Iron Cores, Ferrite Cores, Powder Cores, Nano-crystalline Cores, Superconductors, Hysteresis Core Loss, Eddy-Current Core Loss, Total Core Loss, Complex Permeability.

UNIT-II:

Skin Effect & Proximity Effect: Introduction, Magnet Wire, Wire Insulation, Skin Depth, Ratio of AC-to-DC Winding Resistance, Skin Effect in Long Single Round Conductor, Current Density in Single Round Conductor, Impedance of Round Conductor, Magnetic Field Intensity for Round Wire, Other Methods of Determining the Round Wire Inductance, Power Density in Round Conductor, Skin Effect on Single Rectangular Plate. Proximity and Skin Effects in Two Parallel Plates, Anti-proximity and Skin Effects in Two Parallel Plates, Proximity Effect in Multiple-Layer Inductor, Appendix: Derivation of Proximity Power Loss.

Winding Resistance at High Frequencies: Introduction, Winding Resistance, Square and Round Conductors, Winding Resistance of Rectangular Conductor, Winding Resistance of Square Wire, Winding Resistance of Round Wire, Leakage Inductance, Solution for Round Conductor Winding in Cylindrical Coordinates, Litz Wire, Winding Power Loss for Inductor Current with Harmonics, Effective Winding Resistance for Non-sinusoidal Inductor Current, Thermal Model of Inductors.

UNIT-III:

Transformers: Introduction, Neumann's Formula for Mutual Inductance, Mutual Inductance, Energy Stored in Coupled Inductors, Magnetizing Inductance, Leakage Inductance, Measurement of Transformer Inductances, Stray Capacitance, High-Frequency Transformer Model, Non-interleaved Windings, Interleaved Windings, AC Current Transformers, Winding Power Losses with Harmonics, Thermal Model of Transformers.

Design of Transformers: Introduction, Area Product Method, Optimum Flux Density, Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM, Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM.

UNIT-IV:

Integrated Inductors: Introduction, Resistance of Rectangular Trace, Inductance of Straight Rectangular Trace, Construction of Integrated Inductors, Meander Inductors, Inductance of Straight Round Conductor, Inductance of Circular Round Wire Loop, Inductance of Two-Parallel Wire Loop, Inductance of Rectangle of Round Wire, Inductance of Polygon Round Wire Loop, Bond-wire Inductors, Single-Turn Planar Inductor, Inductance of Planar Square Loop, Planar Spiral Inductors, Multi-metal Spiral Inductors, Planar Transformers, MEMS Inductors, Inductance of Coaxial Cable, Inductance of Two-Wire Transmission Line, Eddy Currents in Integrated Inductors, Model of RF Integrated Inductors, PCB Inductors.

Design of Inductors: Introduction, Restrictions on Inductors, Window Utilization Factor, Temperature Rise of Inductors, Mean Turn Length of Inductors, Area Product Method, AC Inductor Design, Inductor Design for Buck Converter in CCM, Inductor Design for Buck Converter in DCM method.

UNIT-V:

Self-Capacitance: Introduction, High-Frequency Inductor Model, Self-Capacitance Components, Capacitance of Parallel-Plate Capacitor, Self-Capacitance of Foil Winding Inductors, Capacitance of Two Parallel Round Conductors, Capacitance of Round Conductor and Conducting Plane, Self-



Capacitance of Single-Layer Inductors, Self-Capacitance of Multi-layer Inductors, Capacitance of Coaxial Cable.

TEXT BOOK:

1. Design of Magnetic Components for Switched Mode Power Converters, Umanand L., Bhat, S.R., ISBN:978-81-224-0339-8, Wiley Eastern Publication, 1992.

REFERENCES:

1. High-Frequency Magnetic Components, Marian K. Kazimierczuk, ISBN: 978-0-470-71453-9 John Wiley & Sons, Inc.
2. G.C. Chrysis, High frequency switching power supplies, McGraw Hill, 1989 (2nd Edn.)
3. Eric Lowdon, Practical Transformer Design Handbook, Howard W. Sams & Co., Inc., 1980
4. "Thompson --- Electrodynamic Magnetic Suspension.pdf"
5. Witulski --- "Introduction to modeling of transformers and coupled inductors" Beattie --- "Inductance 101.pdf"
6. P. L. Dowell, "Effects of eddy currents in transformer windings.pdf"
7. Dixon--- "Eddy current losses in transformer windings.pdf"
8. J J Ding, J S Buckkridge, "Design Considerations For A Sustainable Hybrid Energy System" IPENZ Transactions, 2000, Vol. 27, No. 1/EMCh.
9. Texas Instruments --- "Windings.pdf"
10. Texas Instruments --- "Magnetic core characteristics.pdf"
11. Ferroxcube --- "3f3 ferrite datasheet.pdf"
12. Ferroxcube --- "Ferrite selection guide.pdf"
13. Magnetics, Inc., Ferrite Cores (www.mag-inc.com).



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

DYNAMICS OF ELECTRICAL MACHINES
(Core Elective-IV)

Course Objective: This course deals with generalized modeling and analysis of different electrical machines used for industrial drive applications.

Learning Outcomes: Students will be able to

- Basic mathematical analysis of electrical machines and its characteristics.
- Behavior of electrical machines under steady state and transient state.
- Dynamic modeling of electrical machines.

UNIT-I:

Basic Machine Theory:

Electromechanical Analogy – Magnetic Saturation – Rotating field theory – Operation of Inductor motor – equivalent circuit – Steady state equations of DC machines – operations of synchronous motor – Power angle characteristics

UNIT-II:

Electrodynamical Equation & Their Solutions

Spring and Plunger system - Rotational motion – mutually coupled coils – Lagrange's equation – Application of Lagrange's equation solution of Electro dynamical equations.

UNIT-III:

Dynamics of DC Machines

Separately excited d. c. generators – steady state analysis – transient analysis – Separately excited d. c. motors – steady state analysis – transient analysis – interconnection of machines – Ward Leonard system of speed control.

UNIT-IV:

Induction Machine Dynamics

Induction machine dynamics during starting and braking – accelerating time – induction machine dynamic during normal operation – Equation for dynamical response of the induction motor.

UNIT-V:

Synchronous Machine Dynamics

Electromechanical equation – motor operation – generator operation – small oscillations – general equations for small oscillations – representation of the oscillation equations in state variable form.

REFERENCE BOOKS:

1. Sen Gupta D.P. and J.W “ Electrical Machine Dynamics “Macmillan Press Ltd 1980.
2. Bimbhra P.S. “Generalized Theory of Electrical Machines “ Khanna Publishers 2002.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

INSTRUMENTATION AND CONTROL
(Open Elective – II)

Course Objectives:

- To provide knowledge of Instrumentation systems and their applications.
- To provide knowledge of advanced control theory and its applications to engineering problems.
- To have a comprehensive idea about P,PI,PD,PID controllers

Course Outcomes: Upon the completion of this course, the student will be able to

- to understand and analyze instrumentation systems and their applications to various industries.
- to apply advanced control theory to practical engineering problems.

Unit-I

Instrumentation: Introduction to mechanical systems and their structure and function, Performance Characteristics – Static and Dynamic, Fundamentals of signals acquisition, conditioning and processing,

Unit-II

Measurement of temperature, pressure, flow, position, velocity, acceleration, force, torque etc.

Unit-III

Control: Introduction to control systems, mathematical model of physical systems in transfer function and state space forms, response of dynamic systems, concept of pole & zero of a system,

Unit-IV

Realization of transfer functions, stability analysis. Introduction of discrete time system. Controllers: P, PI, PD, PID, Feed forward etc., tuning of controller parameters, disturbance rejection, implementation of controller using digital computer.

Unit-V

Control components: Actuator (ac & dc servomotors, valve), AC, DC tacho-generators, servo amplifier.

TEXT BOOKS:

1. John P Bently, "Principles of Measurement Systems" 3rd. Edition, Pearson
2. Alok Barua, "Fundamentals of Industrial Instrumentation" Wiley India, 2011
3. William Bolton, "Instrumentation and Control Systems", Elsevier, 2015

REFERENCES:

1. William Bolton, "Industrial Control and Instrumentation" University Press, 1991
2. Norman A Anderson, "Instrumentation for Process Measurement and Control" CRC, 1997
3. A. K. Ghosh, "Introduction to Instrumentation and Control" Prentice Hall of India, 2005



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

INTELLIGENT CONTROL
(Open Elective-II)

Course Objectives:

- Gaining an understanding of the functional operation of a variety of intelligent control techniques and their bio-foundations
- the study of control-theoretic foundations
- learning analytical approaches to study properties

Course Outcomes: Upon the completion of this course, the student will be able to

- develop Neural Networks, Fuzzy Logic and Genetic algorithms.
- implement soft computing to solve real-world problems mainly pertaining to control system applications

Unit-I

Introduction and motivation. Approaches to intelligent control. Architecture for intelligent control. Symbolic reasoning system, rule-based systems, the AI approach. Knowledge representation. Expert systems.

Unit-II

Concept of Artificial Neural Networks and its basic mathematical model, McCulloch-Pitts neuron model, simple perceptron, Adaline and Madaline, Feedforward Multilayer Perceptron. Learning and Training the neural network. Data Processing: Scaling, Fourier transformation, principal-component analysis.

Unit-III

Networks: Hopfield network, Self-organizing network and Recurrent network. Neural Network based controller Case studies: Identification and control of linear and nonlinear dynamic systems using Matlab-Neural Network toolbox. Stability analysis of Neural-Network interconnection systems.

Unit-IV

Genetic Algorithm: Basic concept of Genetic algorithm and detail algorithmic steps, adjustment of free parameters. Solution of typical control problems using genetic algorithm. Concept on some other search techniques like tabu search and ant-colony search techniques for solving optimization problems.

Unit-V

Introduction to crisp sets and fuzzy sets, basic fuzzy set operation and approximate reasoning. Introduction to fuzzy logic modeling and control. Fuzzification, inferencing and defuzzification. Fuzzy knowledge and rule bases. Fuzzy modeling and control schemes for nonlinear systems. Fuzzy logic control for nonlinear time-delay system. Implementation of fuzzy logic controller using Matlab fuzzy-logic toolbox. Stability analysis of fuzzy control systems.

TEXT BOOKS:

- a. Simon Haykins, Neural Networks: A comprehensive Foundation, Pearson Edition, 2003.
- b. T.J.Ross, Fuzzy logic with Fuzzy Applications, Mc Graw Hill Inc, 1997.
- c. David E Goldberg, "Genetic Algorithms".
- d. John Yen and Reza Langari, Fuzzy logic Intelligence, Control, and Information, Pearson Education, Indian Edition, 2003.

REFERENCES:

- a. M.T.Hagan, H. B. Demuth and M. Beale, Neural Network Design, Indian reprint, 2008.
- b. Fredric M.Ham and Ivica Kostanic, Principles of Neurocomputing for science and Engineering, McGraw Hill, 2001.
- c. N.K. Bose and P.Liang, Neural Network Fundamentals with Graphs, Algorithms and Applications, Mc - Graw Hill, Inc. 1996.
- d. Yung C. Shin and Chengying Xu, Intelligent System - Modeling, Optimization and Control, CRC Press, 2009.
- e. N.K.Sinha and Madan M Gupta, Soft computing & Intelligent Systems - Theory & Applications, Indian Edition, Elsevier, 2007.
- f. Witold Pedrycz, Fuzzy Control and Fuzzy Systms, Overseas Press, Indian Edition, 2008.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

SMART GRID TECHNOLOGIES
(Open Elective-II)

Prerequisite: Electrical and Electronic Instrumentation

Course Objectives:

- To group various aspects of the smart grid,
- To defend smart grid design to meet the needs of a utility
- To select issues and challenges that remain to be solved
- To analyze basics of electricity, electricity generation, economics of supply and demand, and the various aspects of electricity market operations in both regulated and deregulated environment.

Course Outcomes: Upon the completion of this course, the student will be able to

- analyze the structure of an electricity market in either regulated or deregulated market conditions.
- know the advantages of DC distribution and developing technologies in distribution
- Discriminate the trade-off between economics and reliability of an electric power system.
- Differentiate various investment options (e.g. generation capacities, transmission, renewable, demand-side resources, etc) in electricity markets.
- Analyze the development of smart and intelligent domestic systems.

UNIT-I:

Introduction: Introduction to smart grid - Electricity network - Local energy networks- Electric transportation - Low carbon central generation - Attributes of the smart grid - Alternate views of a smart grid.

Smart Grid to Evolve A Perfect Power System: Introduction- Overview of the perfect power system configurations- Device level power system- Building integrated power systems- Distributed power systems- Fully integrated power system-Nodes of innovation.

UNIT-II:

Dc Distribution and Smart Grid: AC Vs DC sources-Benefits of and drives of DC power delivery systems - Powering equipment and appliances with DC-Data centers and information technology loads - Future neighborhood-Potential future work and research.

Intelligrid Architecture for the Smartgrid: Introduction- Launching intelligrid -Intelligrid today - Smart grid vision based on the intelligrid architecture-Barriers and enabling technologies.

UNIT-III:

Dynamic Energy Systems Concept: Smart energy efficient end use devices-Smart distributed energy resources - Advanced whole building control systems- Integrated communications architecture - Energy management-Role of technology in demand response- Current limitations to dynamic energy management-Distributed energy resources-Overview of a dynamic energy management-Key characteristics of smart devices- Key characteristics of advanced whole building control systems-Key characteristics of dynamic energy management system.

UNIT-IV:

Energy Port as Part of the Smart Grid: Concept of energy - Port, generic features of the energy port.

Policies and Programs to Encourage End – Use Energy Efficiency: Policies and programs in action - multinational - national-state-city and corporate levels.

Market Implementation: Framework-factors influencing customer acceptance and response - program planning - monitoring and evaluation.

UNIT-V:

Efficient Electric End – Use Technology Alternatives: Existing technologies – lighting - Space conditioning - Indoor air quality - Domestic water heating - hyper efficient appliances - Ductless residential heat pumps and air conditioners - Variable refrigerant flow air conditioning-Heat pump water heating - Hyper efficient residential appliances - Data center energy efficiency- LED street and



area lighting - Industrial motors and drives - Equipment retrofit and replacement - Process heating - Cogeneration, Thermal energy storage - Industrial energy management programs - Manufacturing process-Electro-technologies, Residential, Commercial and industrial sectors.

TEXT BOOKS:

1. Clark W Gellings, "The Smart Grid, Enabling Energy Efficiency and Demand Side Response"- CRC Press, 2009.
2. Jean Claude Sabonnadière, NouredineHadjsaïd, "Smart Grids", Wiley-ISTE, IEEE Press, May 2012

REFERENCES:

1. JanakaEkanayake, KithsiriLiyanage, Jianzhong.Wu, Akihiko Yokoyama, Nick Jenkins, "Smart Grid: Technology and Applications"- Wiley, 2012.
2. James Momoh, "Smart Grid: Fundamentals of Design and Analysis" - Wiley, IEEE Press, 2012.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

AI TECHNIQUES IN ELECTRICAL ENGINEERING
(Open Elective-II)

Course Objectives:

- To locate soft commanding methodologies, such as artificial neural networks, Fuzzy logic and genetic Algorithms.
- To observe the concepts of feed forward neural networks and about feedback neural networks.
- To practice the concept of fuzziness involved in various systems and comprehensive knowledge of fuzzy logic control and to design the fuzzy control
- To analyze genetic algorithm, genetic operations and genetic mutations.

Course Outcomes: Upon the completion of this course, the student will be able to

- understand feed forward neural networks, feedback neural networks and learning techniques.
- understand fuzziness involved in various systems and fuzzy set theory.
- develop fuzzy logic control for applications in electrical engineering
- develop genetic algorithm for applications in electrical engineering.

UNIT – I:

Artificial Neural Networks: Introduction-Models of Neural Network - Architectures – Knowledge representation – Artificial Intelligence and Neural networks – Learning process – Error correction learning – Hebbian learning – Competitive learning – Boltzman learning – Supervised learning – Unsupervised learning – Reinforcement learning - learning tasks.

UNIT- II:

ANN Paradigms : Multi – layer perceptron using Back propagation Algorithm-Self – organizing Map – Radial Basis Function Network – Functional link, network – Hopfield Network.

UNIT – III:

Fuzzy Logic: Introduction – Fuzzy versus crisp – Fuzzy sets - Membership function – Basic Fuzzy set operations – Properties of Fuzzy sets – Fuzzy cartesian Product – Operations on Fuzzy relations – Fuzzy logic – Fuzzy Quantifiers - Fuzzy Inference - Fuzzy Rule based system - Defuzzification methods.

UNIT – IV:

Genetic Algorithms: Introduction-Encoding – Fitness Function-Reproduction operators - Genetic Modeling – Genetic operators - Crossover - Single-site crossover – Two-point crossover – Multi point crossover-Uniform crossover – Matrix crossover - Crossover Rate - Inversion & Deletion – Mutation operator –Mutation – Mutation Rate-Bit-wise operators - Generational cycle-convergence of Genetic Algorithm.

UNIT-V:

Applications of AI Techniques: Load forecasting – Load flow studies – Economic load dispatch – Load frequency control – Single area system and two area system – Small Signal Stability (Dynamic stability) Reactive power control – speed control of DC and AC Motors.

TEXT BOOK:

1. S. Rajasekaran and G.A.V.Pai, "Neural Networks, Fuzzy Logic & Genetic Algorithms"- PHI, New Delhi, 2003.

REFERENCES:

1. P.D.Wasserman, Van Nostrand Reinhold, "Neural Computing Theory & Practice" - New York, 1989.
2. Bart Kosko, "Neural Network & Fuzzy System" Prentice Hall, 1992.
3. G.J.Klir and T.A.Folger, "Fuzzy sets, Uncertainty and Information"-PHI, Pvt.Ltd, 1994.
4. D.E.Goldberg, " Genetic Algorithms"- Addison Wesley 1999.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

RELIABILITY ENGINEERING
(Open Elective –II)

Course Objectives:

- To comprehend the concept of Reliability and Unreliability
- Derive the expressions for probability of failure, Expected value and standard deviation of Binominal distribution, Poisson distribution, normal distribution and weibull distributions.
- Formulating expressions for Reliability analysis of series-parallel and Non-series parallel systems
- Deriving expressions for Time dependent and Limiting State Probabilities using Markov models.

Learning Outcomes: Upon the completion of this course, the student will be able to

- Apply fundamental knowledge of Reliability to modeling and analysis of series-parallel and Non-series parallel systems.
- Solve some practical problems related with Generation, Transmission and Utilization of Electrical Energy.
- Understand or become aware of various failures, causes of failures and remedies for failures in practical systems.

Unit I:

Rules for combining probabilities of events, Definition of Reliability. Significance of the terms appearing in the definition. Probability distributions: Random variables, probability density and distribution functions. Mathematical expectation, Binominal distribution, Poisson distribution, normal distribution, weibull distribution.

Unit II:

Hazard rate, derivation of the reliability function in terms of the hazard rate. Failures: Causes of failures, types of failures (early failures, chance failures and wear-out failures). Bath tub curve. Preventive and corrective maintenance. Modes of failure. Measures of reliability: mean time to failure and mean time between failures.

Unit III:

Classification of engineering systems: series, parallel and series-parallel systems- Expressions for the reliability of the basic configurations.
Reliability evaluation of Non-series-parallel configurations: Decomposition, Path based and cutset based methods, Deduction of the Paths and cutsets from Event tree.

Unit IV:

Discrete Markov Chains: General modeling concepts, stochastic transitional probability matrix, time dependent probability evaluation and limiting state probability evaluation of one component repairable model. Absorbing states.

Continuous Markov Processes: Modeling concepts, State space diagrams, Stochastic Transitional Probability Matrix, Evaluating time dependent and limiting state Probabilities of one component repairable model. Evaluation of Limiting state probabilities of two component repairable model.

UNIT-V:

Approximate system Reliability analysis of Series systems, parallel systems with two and more than two components, Network reduction techniques. Minimal cutset/failure mode approach.

TEXT BOOKS:

1. "Reliability evaluation of Engineering systems", Roy Billinton and Ronald N Allan, BS Publications.
2. "Reliability Engineering", Elsayed A. Elsayed, Prentice Hall Publications.

REFERENCES:

1. "Reliability Engineering: Theory and Practice", By Alessandro Birolini, Springer Publications.
2. "An Introduction to Reliability and Maintainability Engineering", Charles Ebeling, TMH Publications.
3. "Reliability Engineering", E. Balaguruswamy, TMH Publications.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

ENERGY AUDITING, CONSERVATION AND MANAGEMENT
(Open Elective –II)

Course Objectives:

- To know the necessity of conservation of energy
- To generalize the methods of energy management
- To illustrate the factors to increase the efficiency of electrical equipment
- To detect the benefits of carrying out energy audits.

Course Outcomes: Upon the completion of this course, the student will be able to

- Tell energy audit of industries
- Predict management of energy systems
- Sequence the methods of improving efficiency of electric motor
- Analyze the power factor and to design a good illumination system
- Determine pay back periods for energy saving equipment

UNIT-I:

Basic Principles of Energy Audit: Energy audit- definitions, concept , types of audit, energy index, cost index ,pie charts, Sankey diagrams, load profiles, Energy conservation schemes- Energy audit of industries- energy saving potential, energy audit of process industry, thermal power station, building energy audit.

UNIT-II:

Energy Management: Principles of energy management, organizing energy management program, initiating, planning, controlling, promoting, monitoring, reporting- Energy manger, Qualities and functions, language, Questionnaire – check list for top management.

UNIT-III:

Energy Efficient Motors: Energy efficient motors , factors affecting efficiency, loss distribution , constructional details , characteristics - variable speed , variable duty cycle systems, RMS hp- voltage variation-voltage unbalance- over motoring- motor energy audit

UNIT-IV:

Power Factor Improvement, Lighting and Energy Instruments: Power factor – methods of improvement, location of capacitors, pf with non linear loads, effect of harmonics on power factor, power factor motor controllers - Good lighting system design and practice, lighting control, lighting energy audit - Energy Instruments- wattmeter, data loggers, thermocouples, pyrometers, lux meters, tongue testers ,application of PLC's.

UNIT-V:

Economic Aspects and Analysis: Economics Analysis-Depreciation Methods, time value of money, rate of return , present worth method , replacement analysis, life cycle costing analysis- Energy efficient motors- calculation of simple payback method, net present worth method- Power factor correction, lighting - Applications of life cycle costing analysis, return on investment .

TEXT BOOKS:

1. Energy management by W.R. Murphy AND G. McKay Butter worth, Heinemann publications.
2. Energy management by Paul o' Callaghan, Mc-graw Hill Book company-1st edition, 1998

REFERENCES:

1. Energy efficient electric motors by John .C. Andreas, Marcel Dekker Inc Ltd-2nd edition, 1995-
2. Energy management hand book by W.C.Turner, John wiley and sons
3. Energy management and good lighting practice : fuel efficiency- booklet12-EEO



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER CONVERTERS AND DRIVES LAB

1. Speed Measurement and closed loop control using PMDC motor.
2. Thyristorised drive for PMDC Motor with speed measurement and closed Loop control.
3. IGBT used single 4 quadrant chopper drive for PMDC motor with speed measurement and closed loop control.
4. Thyristorised drive for 1Hp DC motor with closed loop control.
5. 3-Phase input, thyristorised drive, 3 Hp DC motor with closed loop
6. 3-Phase input IGBT, 4 quadrant chopper drive for DC motor with closed Loop control equipment.
7. Cyclo-converter based AC Induction motor control equipment.
8. Speed control of 3 phase wound rotor Induction motor.
9. Single-phase fully controlled converter with inductive load.
10. Single phase half wave controlled converter with inductive load.
11. Isolated Gate Drive circuits for MOSFET / IGBT based circuits.
12. Characteristics of solar PV Systems.
13. Maximum Power Point Tracking Charge Controllers.
14. Inverter control for Solar PV based systems.

Note: Any ten experiments can be conducted.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech. (CAD/CAM)
COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Advanced CAD	25	75	4	--	4
Core Course II	Computer Aided Manufacturing	25	75	4	--	4
Core Course III	Advanced FEM	25	75	4	--	4
Core Elective I	Mechanical Behaviour of Materials Stress Analysis and Vibration Rapid Prototyping Technologies	25	75	4	--	4
Core Elective II	Automation in Manufacturing Computer Aided Process Planning Performance Modeling and Analysis of Manufacturing Systems	25	75	4	--	4
Open Elective I	Numerical Methods for Partial Differential Equations Production and Operations Management	25	75	4	--	4
Laboratory I	Laboratory	25	75	--	4	2
Seminar I	Seminar	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Design for Manufacturing And Assembly	25	75	4	--	4
Core Course V	Flexible Manufacturing Systems	25	75	4	--	4
Core Course VI	Industrial Robotics	25	75	4	--	4
Core Elective III	Intelligent Manufacturing Systems Special Manufacturing Process Design Optimization	25	75	4	--	4
Core Elective IV	Advanced Mechatronics Design and Manufacturing of MEMS and Micro Systems Fuzzy Logic and Neural Networks	25	75	4	--	4
Open Elective II	Engineering Research and Methodology Quality Engineering in Manufacturing	25	75	4	--	4
Laboratory II	Manufacturing Simulation & Precision Engineering Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C
Comprehensive Viva-Voce	--	100	--	--	4
Project work Review I	50	--	--	24	12
Total Credits			--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C
Project work Review II	50	--	--	8	4
Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits			--	24	16



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED CAD

UNIT- I:

CAD Tools: Definition of CAD Tools, Graphics standards, Graphics software: requirements of graphics software, Functional areas of CAD, Efficient use of CAD software.

Basics of Geometric Modelling: Requirement of geometric modelling, Geometric models, Geometric construction methods, Modelling facilities desired.

UNIT- II:

Eometricmodelling: Classification of wireframe entities, Curve representation methods, Parametric representation of analytic curves: line, circle, arc, conics, Parametric representation of synthetic curves: Hermite cubic curve, Bezier curve, B-Spleen curve wire , NURBS, Curve manipulations.

UNIT- III:

Surface Modeling : Classification of surface entities, Surface representation methods, Parametric representation of analytic surfaces: plane surface, ruled surface, surface of revolution, tabulated cylinder, Parametric representation of synthetic curves: Hermite cubic surface, Bezier surface, B-Spleen surface , Blending surface, Surface manipulations.

UNIT- IV:

Solid Modelling: Geometry and topology, Boundary representation, The Euler-Poincare formula, Euler operators, Constructive solid geometry: CSG primitives, Boolean operators, CSG expressions, Interior, Exterior, closure, Sweeping: linear and non-linear, Solid manipulations.

UNIT- V:

Transformations: @-D and 3-D transformations: translation, scaling, rotation, reflection, concatenation, homogeneous coordinates, Perspective projection, orthotropic projection, isometric projection, Hidden surface removal, shading, rendering.

Evaluation Criteria: Evaluation criteria of CAD software, Data exchange formats: GKS, IGES, PHIGS, CGM, STEP

Dimensioning and tolerances: Linear, angular, angular dimensions, maximum material condition (MMC), Least material condition (LMC), Regardless of feature size (RFS).

TEXT BOOKS:

1. CAD/CAM Concepts and Applications/ Alavala/ PHI.
2. Mastering CAD/CAM / Ibrhim Zeid / Mc Graw Hill International.

REFERENCES :

1. CAD/CAM Principles and Applications/ P.N.Rao/TMH/3rd Edition
2. CAD/CAM /Groover M.P./ Pearson education
3. CAD / CAM / CIM, Radhakrishnan and Subramanian/ New Age
4. Principles of Computer Aided Design and Manufacturing/ Farid Amirouche/ Pearson
5. Computer Numerical Control Concepts and programming/ Warren S Seames/ Thomson.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

COMPUTER AIDED MANUFACTURING

UNIT - I

Compute-Aided Programming: General information, APT programming, Examples Apt programming problems (2D machining only). NC programming on CAD/CAM systems, the design and implementation of post processors. Introduction to CAD/CAM software, Automatic Tool Path generation.

UNIT - II

Tooling for CNC Machines: Interchangeable tooling system, preset and qualified tools, coolant fed tooling system, modular fixturing, quick change tooling system, automatic head changers. DNC Systems and Adaptive Control: Introduction, type of DNC systems, advantages and disadvantages of DNC, adaptive control with optimization, Adaptive control with constraints, Adaptive control of machining processes like turning, grinding.

UNIT - III

Post Processors for CNC:

Introduction to Post Processors: The necessity of a Post Processor, the general structure of a Post Processor, the functions of a Post Processor, DAPP — based- Post Processor: Communication channels and major variables in the DAPP — based Post Processor, the creation of a DAPP — Based Post Processor.

UNIT - IV

Micro Controllers: Introduction, Hardware components, I/O pins, ports, external memory, counters, timers and serial data I/O interrupts. Selection of Micro Controllers Embedded Controllers, Applications and Programming of Micro Controllers. Programming Logic Controllers (PLC's): Introduction, Hardware components of PLC, System, basic structure, principle of operations, Programming mnemonics timers, Internal relays and counters, Applications of PLC's in CNC Machines.

UNIT - V

Computer Aided Process Planning: Hybrid CAAP System, Computer Aided Inspection and quality control, Coordinate Measuring Machine, Limitations of CMM, Computer Aided Testing, Optical Inspection Methods, Artificial Intelligence and expert system: Artificial Neural Networks, Artificial Intelligence in CAD, Experts systems and its structures.

TEXT BOOKS:

1. CAD/CAM Concepts and Applications/ Alavala/ PHI.
2. CAD/CAM Principles and Applications, P.N.Rao, TMH

REFERENCES:

1. Computer Control of Manufacturing Systems / Yoram Koren / Mc Graw Hill. 1983.
2. Computer Aided Design Manufacturing – K. Lalit Narayan, K. Mallikarjuna Rao and M.M.M. Sarcar, PHI, 2008.
3. CAD / CAM / CIM, Radhakrishnan and Subramanian, New Age
4. Principles of Computer Aided Design and Manufacturing, Farid Amirouche, Pearson
5. Computer Numerical Control Concepts and programming, Warren S Seames, Thomson.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED FINITE ELEMENT METHODS

UNIT-I:

Introduction to FEM, basic concepts, historical back ground, applications of FEM, general description, comparison of FEM with other methods, variational approach, Glerkin's Methods. Co-ordinates, basic element shapes, interpolation function, Virtual energy principle, Rayleigh – Ritz method, properties of stiffness matrix, treatment of boundary conditions, solution of system of equations, shape functions and characteristics, Basic equations of elasticity, strain- displacement relations.

UNIT-II:

1-D Structural Problems: Axial bar element – stiffness matrix, load vector, temperature effects, Quadratic shape functions and problems.

Analysis of Trusses : Plane Trusses and Space Truss elements and problems

Analysis of BECAD/CAM : Hermite shape functions – stiffness matrix – Load vector – Problems.

UNIT-III:

2-D problems: CST, LST, force terms, Stiffness matrix and load vectors, boundary conditions, Isoparametric elements – quadrilateral element, shape functions – Numerical Integration. Finite element modeling of Axi-symmetric solids subjected to Axi-symmetric loading with triangular elements.

3-D Problems: Tetrahedran element – Jacobian matrix – Stiffness matrix.

UNIT-VI:

Scalar Field Problems: 1-D Heat conduction-Slabs – fins - 2-D heat conduction problems – Introduction to Torsional problems.

UNIT-V:

Dynamic considerations, Dynamic equations – consistent mass matrix – Eigen Values, Eigen vector, natural frequencies – mode shapes – modal analysis.

TEXT BOOKS:

1. Finite Element Methods: Basic Concepts and applications, Alavala, PHI.
2. Finite Element Method – Zincowitz / Mc Graw Hill

REFERENCES:

1. The Finite Element Methods in Engineering / SS Rao / Pergamon.
2. Introduction to Finite Elements in Engineering, Chandrupatla, Ashok and Belegundu, Prentice – Hall
3. Introduction to Finite element analysis- S.Md.Jalaludeen, Anuradha Publications, print-2012
4. A First Course in the Finite Element Method/Daryl L Logan/Cengage Learning/5th Edition
5. Finite Element Method – Krishna Murthy / TMH
6. Finite Element Analysis – Bathe / PHI



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

MECHANICAL BEHAVIOUR OF MATERIALS (Core Elective – I)

UNIT-I:

Introduction to Deformation Behaviour: Concept of stresses and strains, engineering stresses and strains, Different types of loading and temperature encountered in applications, Tensile Test - stress-strain response for metal, ceramic and polymer, elastic region, yield point, plastic deformation, necking and fracture, Bonding and Material Behaviour, theoretical estimates of yield strength in metals and ceramics.

UNIT-II:

Elasticity Theory: The State of Stress and strain, stress and strain tensor, tensor transformation, principal stress and strain, elastic stress-strain relation, anisotropy, elastic behaviour of metals, ceramics and polymers.

Yielding and Plastic Deformation: Hydrostatic and Deviatoric stress, Octahedral stress, yield criteria and yield surface, texture and distortion of yield surface, Limitation of engineering strain at large deformation, true stress and true strain, effective stress, effective strain, flow rules, strain hardening, Ramberg-Osgood equation, stress-strain relation in plasticity, plastic deformation of metals and polymers

UNIT-III:

Microscopic view of plastic deformation: crystals and defects, classification of defects, thermodynamics of defects, geometry of dislocations, slip and glide, dislocation generation - Frank Read and grain boundary sources, stress and strain field around dislocations, force on dislocation - self-stress, dislocation interactions, partial dislocations, twinning, dislocation movement and strain rate, deformation behavior of single crystal, critical resolved shear stress (CRSS), deformation of poly-crystals - Hall-Petch and other hardening mechanisms, grain size effect - source limited plasticity, Hall-Petch breakdown, dislocations in ceramics and glasses.

UNIT-IV:

Fracture: fracture in ceramics, polymers and metals, different types of fractures in metals, fracture mechanics - Linear fracture mechanics -KIC, elasto-plastic fracture mechanics - JIC, Measurement and ASTM standards, Design based on fracture mechanics, effect of environment, effect of microstructure on KIC and JIC, application of fracture mechanics in the design of metals, ceramics and polymers

UNIT-V:

Deformation under cyclic load - Fatigue: S-N curves, Low and high cycle fatigue, Life cycle prediction, Fatigue in metals, ceramics and polymers

Deformation at High temperature: Time dependent deformation - creep, different stages of creep, creep and stress rupture, creep mechanisms and creep mechanism maps, creep under multi-axial loading, microstructural aspects of creep and design of creep resistant alloys, high temperature deformation of ceramics and polymers.

REFERENCES:

1. G.E. Dieter, "Mechanical Metallurgy", McGraw-Hill, 1986.
2. R.W. Hertzberg, "Deformation and Fracture Mechanics of Engineering Materials", John Wiley and Sons, 1976.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

**STRESS ANALYSIS AND VIBRATION
(Core Elective – I)**

UNIT-I:

Two dimensional elasticity theory in Cartesian coordinates, plane stress problem in polar coordinates
Thick cylinders, Rotating discs - stress concentration.

UNIT- II:

Torsion of non circular prismatic sections, rectangular and axisymmetric, Circular plates, introduction to shell theory — contact stresses.

UNIT- III:

Single degree freedom, two degree freedom system without and with damping - Free and forced vibrations. Transient vibrations.

UNIT- IV:

Transient vibrations of single and two degree freedom systems, multi-degree of freedom systems - applications of matrix methods , continuous systems.

UNIT -V:

Free and forced vibrations of strings bars and beCAD/CAM. Principle of orthogonality - classical and energy methods.

REFERENCES:

1. Theory of Elasticity/Timoshenko S.P. and Goodier J.N./Koakusha Publishers
2. Advanced strength of materials / Den Hortog J.P./Torrent
3. Mechanical Vibrations/ Den Ilartog J.P./ Dover Publications
4. Theory of Vibrations with Applications/ Thomson W.T./ CBS Publishing
5. Mechanical Vibrations/ Rao S.S./ Addison Wesley Longman



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

**RAPID PROTOTYPING TECHNOLOGIES
(Core Elective – I)**

Unit – I

Introduction: Prototyping fundamentals, Historical development, Fundamentals of Rapid Prototyping, Advantages and Limitations of Rapid Prototyping, Commonly used Terms, Classification of RP process, Rapid Prototyping Process Chain: Fundamental Automated Processes, Process Chain.

Unit – II

Liquid-based Rapid Prototyping Systems: Stereo lithography Apparatus (SLA): Models and specifications, Process, working principle, photopolymers, photo polymerization, Layering technology, laser and laser scanning, Applications, Advantages and Disadvantages, Case studies. Solid ground curing (SGC): Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies. Solid-based Rapid Prototyping Systems: Laminated Object Manufacturing (LOM): Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies. Fused Deposition Modeling (FDM): Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies.

Unit-III

Powder Based Rapid Prototyping Systems: Selective laser sintering (SLS): Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies. Three dimensional Printing (3DP): Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies. Rapid Tooling: Introduction to Rapid Tooling (RT), Conventional Tooling Vs. RT, Need for RT. Rapid Tooling Classification: Indirect Rapid Tooling Methods: Spray Metal Deposition, RTV Epoxy Tools, Ceramic tools, Investment Casting, Spin Casting, Die casting, Sand Casting, 3D Keltool process. Direct Rapid Tooling: Direct AIM, LOM Tools, DTM Rapid Tool Process, EOS Direct Tool Process and Direct Metal Tooling using 3DP.

Unit – IV

Rapid Prototyping Data Formats: STL Format, STL File Problems, Consequence of Building Valid and Invalid Tessellated Models, STL file Repairs: Generic Solution, Other Translators, Newly Proposed Formats. Rapid Prototyping Software's: Features of various RP software's like Magic's, Mimics, Solid View, View Expert, 3 D View, Velocity 2 , Rhino, STL View 3 Data Expert and 3 D doctor.

Unit –V

RP Applications: Application – Material Relationship, Application in Design, Application in Engineering, Analysis and Planning, Aerospace Industry, Automotive Industry, Jewelry Industry, Coin Industry, GIS application, Arts and Architecture. RP Medical and Bioengineering Applications: Planning and simulation of complex surgery, Customized Implants & Prosthesis, Design and Production of Medical Devices, Forensic Science and Anthropology, Visualization of Biomolecules.

TEXT BOOKS:

1. Rapid prototyping: Principles and Applications - Chua C.K., Leong K.F. and LIM C.S, World Scientific publications , Third Edition, 2010.

REFERENCE BOOKS:

1. Rapid Manufacturing – D.T. Pham and S.S. Dimov, Springer , 2001
2. Whalers Report 2000 – Terry Wohlers, Wohlers Associates, 2000 Rapid Prototyping & Manufacturing – Paul F.Jacobs, ASME Press, 1996.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

AUTOMATION IN MANUFACTURING (Core Elective – II)

UNIT – I

Over View of Manufacturing and Automation: Production systems, Automation in production systems, Automation principles and strategies, Manufacturing operations, production facilities. Basic elements of an automated system, levels of automation; Hardware components for automation and process control, programmable logic controllers and personal computers.

UNIT – II:

Material Handling and Identification Technologies: Material handling, equipment, Analysis. Storage systems, performance and location strategies, Automated storage systems, AS/RS, types. Automatic identification methods, Barcode technology, RFID.

UNIT – III:

Manufacturing Systems and Automated Production Lines: Manufacturing systems: components of a manufacturing system, Single station manufacturing cells; Manual Assembly lines, line balancing Algorithms, Mixed model Assembly lines, Alternative Assembly systems. Automated production lines, Applications, Analysis of transfer lines.

UNIT – IV:

Automated Assembly Systems: Fundamentals, Analysis of Assembly systems. Cellular manufacturing, part families, cooling, production flow analysis. Group Technology and flexible Manufacturing systems, Quantitative Analysis.

UNIT – V:

Quality Control and Support Systems: Quality in Design and manufacturing, inspection principles and strategies, Automated inspection, contact Vs non contact, CMM. Manufacturing support systems. Quality function deployment, computer aided process planning, concurrent engineering, shop floor control, just in time and lean production.

REFERENCES:

1. Automation, production systems and computer integrated manufacturing/ Mikell.P Groover/PHI/3rd edition/2012.
2. Automation, Production Systems and CIM/ Mike J P. Grover/PHI
3. CAD/CAM/CIM/ P. Radha Krishnan & S. Subrahmanyam and Raju/New Age International Publishers/2003.
4. System Approach to Computer Integrated Design and Manufacturing/ Singh/John Wiley /96.
5. Computer Aided Manufacturing/Tien-Chien Chang, Richard A. Wysk and Hsu-Pin Wang/ Pearson/ 2009.
6. Manufacturing and Automation Technology / R Thomas Wright and Michael Berkeihiser / Good Heart/Willcox Publishers.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

COMPUTER AIDED PROCESS PLANNING (Core Elective – II)

UNIT-I:

Introduction : The Place of Process Planning in the Manufacturing cycle-Process planning and production Planning-Process planning and Concurrent Engineering, CAPP, Group Technology.

UNIT-II:

Part Design Representation : Design Drafting-Dimensioning-Conventional Tolerance- Geometric Tolerance-CAD-input/output devices-Topology - Geometric transformation-Perspective transformation-Data Structure-Geometric modeling for process planning--GT Coding-The OPITZ system-The MICLASS System.

UNIT-III;

Process Engineering and Process Planning: Experience based planning-Decision table and Decision trees-Process capability analysis-Process planning-Variant process planning-Generative approach-Forward and backward planning, Input format, AI.

UNIT-IV

Computer Aided Process Planning Systems: Logical Design of process planning- Implementation considerations-Manufacturing system components, Production Volume, No. of production families - CAM-I, CAPP, MIPLAN, APPAS, AUTOPLAN and PRO, CPPP.

UNIT-V

An Intergarted Process Planning Systems: Totally integrated process planning systems-An Overview-Modulus structure-Data Structure-Operation-Report Generation, Expert process planning

REFERENCE BOOKS:

1. Gideon Halevi and Roland D. Weill, "Principle of process planning- A Logical Approach", Chapman & Hall, 1995
2. Chang T. C. & Richard A.Wysk, "An Introduction to automated process planning systems", PrenticeHall1985
3. Chang,T.C., "An Expert Process Planning System", Prentice Hall,1985
4. Nanua Singh, "Systems Approach to Computer Intergrated Design and Manufacturing", John Wiley & Sons, 1996
5. Rao P.N., "Computer Aided Manufacturing", Tata McGraw Hill Publishing Co., 2000.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

PERFORMANCE MODELING AND ANALYSIS OF MANUFACTURING SYSTEMS (Core Elective – II)

UNIT I:

Manufacturing Systems & Control: Automated Manufacturing Systems – Modeling – Role of performance modeling – simulation models-Analytical models. Product cycle – Manufacturing automation – Economics of scale and scope – input/output model – plant configurations. Performance measures – Manufacturing lead time – Work in process – Machine utilization – Throughput – Capacity – Flexibility – Performability – Quality Control Systems – Control system architecture – Factory communications – Local area network interconnections – Manufacturing automation protocol – Database management system.

UNIT II:

Manufacturing Processes: Examples of stochastics processes – Poisson process - Discrete time Markov chain models – Definition and notation – Sojourn times in states – Examples of DTMCs in manufacturing – Chapman – Kolmogorov equation – Steady-state analysis. Continuous Time Markov Chain Models – Definitions and notation – Sojourn times in states – examples of CTMCs in manufacturing – Equations for CTMC evolution – Markov model of a transfer line. Birth and Death Processes in Manufacturing – Steady state analysis of BD Processes – Typical BD processes in manufacturing.

UNIT III:

Queuing Model: Notation for queues – Examples of queues in manufacturing systems – Performance measures – Little's result – Steady state analysis of M/M/m queue, queues with general distributions and queues with breakdowns – Analysis of a flexible machine center.

UNIT IV:

Queuing Networks: Examples of QN models in manufacturing – Little's law in queuing networks – Tandem queue – An open queuing network with feedback – An open central server model for FMS – Closed transfer line – Closed server model – Garden Newell networks.

UNIT V:

Petrinets: Classical Petri Nets – Definitions – Transition firing and reachability – Representational power – properties – Manufacturing models. Stochastic Petri Nets – Exponential timed Petri Nets – Generalized Stochastic Petri Nets – modeling of KANBAN systems – Manufacturing models.

REFERENCES:

1. Performance Modelling of Automated Manufacturing Systems/ Viswanadham, N and Narahari, Y/ Prentice Hall of India, New Delhi, 1994
2. Probability and Statistics with Reliability, Queuing and Computer Science Applications/ Trivedi, K.S./ Prentice Hall, New Jersey, 1982.
3. Fundamentals of Mathematical Statistics/ Gupta S.C. & Kapoor V.K./ 3rd Edition, Delhi, 1988



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

**NUMERICAL METHODS FOR PARTIAL DIFFERENTIAL EQUATIONS
(Open Elective – I)**

UNIT-I:

Introduction to finite difference formula- Parabolic Equation: Introduction – Explicit finite difference approximation to one dimensional equation Crank – Nicholson implicit method – derivation boundary conditions.

UNIT-II:

Alternate direction implicit (ADI) method finite difference in cylindrical and spherical polar co-ordinates.

Convergence stability and consistency: Definitions of local truncation error and consistency convergence analysis – stability analysis by matrix method eigen value von Neumann stability methods, global rounding error-local truncation error-lax's equation theorem.

UNIT-III:

Hyperbolic Equations: Analytical solution of 1st order quasi linear equation – numerical integration along a characteristic lax wenderoff explicit method.

CFI condition wenderoff implicit approximation – propagation of discontinues – Numerical solution by the method of characteristics.

UNIT-IV:

Elliptic Equations: Introduction – Finite differences in polar co-ordinates – formulas for derivative near a curved boundary analysis of the discretization error of the five point approximation to polman's equation over a rectangle.

UNIT-V:

Systematic iterative methods for large linear systems – necessary and sufficient condition for convergence of iterative methods – steepest descent implicit methods.

Finite Element Method: weighted residual method – variations methods – division of the region into elements linear element – Galerkin formulation.

REFERENCES:

1. Numerical Solution of partial differential equations, Finite Differences methods/ G.D. Smith/ Brunel University, Clarendon Press Oxford.
2. The Finite Differences Methods in Partial Differential equation/ A.R. Mitchel and D.F. Gmra/ John Wiley.
3. Numerical Methods for Engineers and scientists/ Joe D. Hoffman/ Mc Graw Hill
4. Applied Finite Element Analysis/ Larry J. Segerlind/ John Wiley.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

PRODUCTION AND OPERATIONS MANAGEMENT (Open Elective –I)

UNIT - I

Operation Management: Definition – Objectives – Types of production systems – historical development of operations management – Current issues in operation management.

Product design – Requirements of good product design – product development – approaches – concepts in product development – standardization – simplification – Speed to market – Introduction to concurrent engineering.

UNIT – II

Value Engineering: objective – types of values – function & cost – product life cycle- steps in value engineering – methodology in value engineers – FAST Diagram – Matrix Method.

Location – Facility location and layout – Factors considerations in Plant location- Comparative Study of rural and urban sites – Methods of selection plant layout – objective of good layout – Principles – Types of layout – line balancing.

UNIT - III

Aggregate Planning: definition – Different Strategies – Various models of Aggregate Planning – Transportation and graphical models.

Advance inventory control systems push systems – Material Requirement – Terminology – types of demands – inputs to MRP- techniques of MRP – Lot sizing methods – benefits and drawbacks of MRP –Manufacturing Resources Planning (MRP –II), Pull systems – Vs Push system – Just in time (JIT) philosophy Kanban System – Calculation of number of Kanbans Requirements for implementation JIT – JIT Production process – benefits of JIT.

UNIT - IV

Scheduling: Policies – Types of scheduling – Forward and Backward Scheduling – Gantt Charts – Flow shop Scheduling – n jobs and 2 machines, n jobs and 3 machines – job shop Scheduling – 2 jobs and n machines – Line of Balance.

UNIT – V

Project Management: Programming Evaluation Review Techniques (PERT) – three times estimation – critical path – probability of completion of project – critical path method – crashing of simple nature.

REFERENCES:

1. Operations Management/ E.S. Buffs/ John Wiley & Sons / 2007
2. Operations Management Theory and Problems/ Joseph G. Monks / Macmillan / McGraw Hill / 3rd Edition.
3. Production Systems Management/ James I. Riggs / John Wiley & Sons.
4. Production and Operations Management/ Chary/ Mc Graw Hill/2004
5. Operations Management/ Richard Chase/ Mc Graw Hill/2006
6. Production and Operation Management / Panner Selvam / PHI.
7. Production and Operation Analysis/ Nahima/ Mc Graw Hill/2004



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED CAD/CAM LAB

Features and selection of CNC turning and milling centers. Practice in part programming and operation of CNC turning machines, subroutine techniques and use of cycles. Practice in part programming and operating a machining center, tool panning and selection of sequences of operations, tool setting on machine, practice in APT based NC programming. Practice in Robot programming and its languages. Robotic simulation using software. Robo path control, preparation of various reports and route sheets, Simulation of manufacturing system using CAM software, controller operating system commands.

University Updates

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech. (CAD/CAM)
COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

Category	Course Title	marks	Ext. marks	L	P	C
Core Course IV	Design for Manufacturing And Assembly	25	75	4	--	4
Core Course V	Flexible Manufacturing Systems	25	75	4	--	4
Core Course VI	Industrial Robotics	25	75	4	--	4
Core Elective III	Intelligent Manufacturing Systems Special Manufacturing Process Design Optimization	25	75	4	--	4
Core Elective IV	Advanced Mechatronics Design and Manufacturing of MEMS and Micro Systems Fuzzy Logic and Neural Networks	25	75	4	--	4
Open Elective II	Engineering Research and Methodology Quality Engineering in Manufacturing	25	75	4	--	4
Laboratory II	Manufacturing Simulation & Precision Engineering Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

DESIGN FOR MANUFACTURING AND ASSEMBLY

UNIT - I

introduction: Design philosophy steps in Design process - General Design rules for manufacturability - basic principles of design Ling for economical production - creativity in design. Materials: Selection of Materials for design Developments in Material technology - criteria for material selection - Material selection interrelationship with process selection process selection charts.

UNIT- II

Machining Process: Overview of various machining processes - general design rules for machining - Dimensional tolerance and surface roughness - Design for machining - Ease - Redesigning of components for machining ease with suitable examples. General design recommendations for machined parts. **Metal Casting:** Appraisal of various casting processes, selection of casting process, - general design considerations for casting - casting tolerances - use of solidification simulation in casting design - product design rules for sand casting.

UNIT- III

Metal Joining: Appraisal of various welding processes, Factors in design of weldments - general design guidelines - pre and post treatment of welds - effects of thermal stresses in weld joints - design of brazed joints. Forging - Design factors for Forging - Closed dies forging design - parting lines of die5 drop forging die design - general design recommendations. Extrusion & Sheet Metal Work: Design guidelines for extruded sections - design principles for Punching, Blanking, Bending, Deep Drawing - Keeler Goodman Forming Line Diagram - Component Design for Blanking.

UNIT-IV

Assemble Advantages: Development of the assemble process, choice of assemble method, assemble advantages social effects of automation.

Automatic Assembly Transfer Systems: Continuous transfer, intermittent transfer, indexing mechanisms, and operator - paced free – transfer machine.

UNIT-V

Design of Manual Assembly: Design for assembly fits in the design process, general design guidelines for manual assembly, development of the systematic DFA methodology, assembly efficiency, classification system for manual handling, classification system for manual insertion and fastening, effect of part symmetry on handling time, effect of part thickness and size on handling time, effect of weight on handling time, parts requiring two hands for manipulation, effects of combinations of factors, effect of symmetry effect of chamfer design on insertion operations, estimation of insertion time.

REFERENCES:

1. Assembly Automation and Product Design/ Geoffrey Boothroyd/ Marcel Dekker Inc., NY, 1992.
2. Engineering Design - Material & Processing Approach/ George E. Deiter/McGraw Hill Intl. 2nd Ed. 2000.
3. Hand Book of Product Design/ Geoffrey Boothroyd/ Marcel and Dekken, N.Y. 1990.
4. Computer Aided Assembly London/ A Delbainbre/.
5. Product Design for Manufacturing and Assembly/ Geoffrey Boothroyd, Peter Dewhurst & Winston Anstony Knight/CRC Press/2010

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

FLEXIBLE MANUFACTURING SYSTEMS

UNIT - I:

Introduction to flexible manufacturing systems. Planning and scheduling and control of FMS. Knowledge based scheduling.

UNIT - II:

Hierarchy of computer control. Supervisory computer.

UNIT - III:

Software for simulation and database of FMS. Specification and selection, trends, application of simulation software.

UNIT - IV:

Manufacturing data systems data flow, CAD/CAM considerations. Planning FMS database, just in time characteristics, Pull method, quality small lot sizes, work station loads, close supplier ties, flexible workforce — line flow strategy.

UNIT - V:

Preventive maintenance. Karban system, implementation issues.

REFERENCES:

1. Hand Book of Flexible Manufacturing Systems/ Jha N K/ Academic Press.
2. Production System Beyond Large Scale Production/ Talichi Ohno/ Toyota Productivity Press India Pvt. Ltd.
3. Flexible Manufacturing Systems/ H K Shivanand/New Age International/2006

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

INDUSTRIAL ROBOTICS

UNIT - I

introduction: Automation and Robotics, Robot anatomy, robot configuration, motions joint notation work volume, robot drive system, control system and dynamic performance, precision of movement.
Control System and Components: basic concept and modais controllers control system analysis, robot activation and feedback components. Positions sensors, velocity sensors, actuators sensors, power transmission system.

UNIT - II

Motion Analysis and Control: Manipulator kinematics, position representation forward transformation, homogeneous transformation, manipulator path control, robot dynamics, configuration of robot controller.

UNIT - III

End Effectors: Grippers-types, operation, mechanism, force analysis, tools as end effectors consideration in gripper selection and design. **SENSORS:** Desirable features, tactile, proximity and range sensors, uses sensors in robotics.

Machine Vision: Functions, Sensing and Digitizing-imaging, Devices, Lighting techniques, Analog to digital single conversion, image storage, Image processing and Analysis-image data reduction, Segmentation feature extraction. Object recognition, training the vision system, Robotics application.

UNIT - IV

Robot Programming: Lead through programming, Robot programming as a path in space, Motion interpolation, WAIT, SINONAL AND DELAY commands, Branching capabilities and Limitations.
ROBOT LANGUAGES: Textual robot Languages, Generation, Robot language structures, Elements in function.

UNIT - V

Robot Cell DESGIN AND CONTROL: Robot cell layouts-Robot centered cell, In-line robot cell, Considerations in work design, Work and control, Inter locks, Error detect ion, Work wheel controller.

Robot Application: Material transfer, Machine loading/unloading. Processing operation, Assembly and Inspection, Feature Application.

REFERENCES:

1. Industrial Robotics / Groover M P /Pearson Edu.
2. Introduction to Robotic Mechanics and Control by JJ Craig, Pearson, 3rd edition.
3. Robotics / Fu K S/ McGraw Hill.
4. Robotic Engineering / Richard D. Klafter, Prentice Hall
5. Robot Analysis and Intelligence / Asada and Slotine / Wiley Inter-Science.
6. Robot Dynamics & Control – Mark W. Spong and M. Vidyasagar / John Wiley & Sons (ASIA) Pte Ltd.
7. Robotics and Control / Mittal R K & Nagrath I J / TMH

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Core Elective – III)

INTELLIGENT MANUFACTURING SYSTEMS

UNIT I:

Computer Integrated Manufacturing Systems Structure and functional areas of CIM system, - CAD, CAPP, **CAM**, CAQC, ASRS. Advantages of CIM. Manufacturing Communication Systems - MAP/TOP, OSI Model, Data Redundancy, Top- down and Bottom-up Approach, Volume of Information. Intelligent Manufacturing System Components, System Architecture and Data Flow, System Operation.

UNIT II:

Components of Knowledge Based Systems - Basic Components of Knowledge Based Systems, Knowledge Representation, Comparison of Knowledge Representation Schemes, Inference Engine, Knowledge Acquisition.

UNIT III:

Machine Learning - Concept of Artificial Intelligence, Conceptual Learning, Artificial Neural Networks - Biological Neuron, Artificial Neuron, Types of Neural Networks, Applications in Manufacturing.

UNIT IV:

Automated Process Planning - Variant Approach, Generative Approach, Expert Systems for Process Planning, Feature Recognition, Phases of Process planning. Knowledge Based System for Equipment Selection (KBSES) - Manufacturing system design. Equipment Selection Problem, Modeling the Manufacturing Equipment Selection Problem, Problem Solving approach in KBSES, Structure of the KBSES.

UNIT V:

Group Technology: Models and Algorithms Visual Method, Coding Method, Cluster Analysis Method, Matrix Formation - Similarity Coefficient Method, Sorting-based Algorithms, Bond Energy Algorithm, Cost Based method, Cluster Identification Method, Extended CI Method. Knowledge Based Group Technology - Group Technology in Automated Manufacturing System. Structure of Knowledge based system for group technology (KBSCIT) — Data Base, Knowledge Base, Clustering Algorithm.

REFERENCES:

1. Intelligent Manufacturing Systems/ Andrew Kusiak/Prentice Hall.
2. Artificial Neural Networks/ Yagna Narayana/PHI/2006
3. Automation, Production Systems and CIM / Groover M.P./PHI/2007
4. Neural networks: A comprehensive foundation/ Simon Haykin/ PHI.
5. Artificial neural networks/ B.Vegnanarayana/PHI
6. Neural networks in Computer intelligence/ Li Min Fu/ TMH/2003
7. Neural networks/ James A Freeman David M S kapura/ Pearson education/2004
8. Introduction to Artificial Neural Systems/Jacek M. Zurada/JAICO Publishing House Ed. 2006.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Core Elective – III)

SPECIAL MANUFACTURING PROCESS

UNIT- I

Surface Treatment: Scope, Cleaners, Methods of cleaning, Surface coating types, and ceramic and organic methods of coating, economics of coating. Electro forming, Chemical vapor deposition, thermal spraying, Ion implantation, diffusion coating, Diamond coating and cladding.

UNIT- II

Processing of Ceramics: Applications, characteristics, classification .Processing of particulate ceramics, Powder preparations, consolidation, Drying, sintering, Hot compaction, Area of application, finishing of ceramics. Processing of Composites: Composite Layers, Particulate and fiber reinforced composites, Elastomers, Reinforced plastics, MMC, CMC, Polymer matrix composites.

UNIT- III

Fabrication of Microelectronic Devices:

Crystal growth and wafer preparation, Film Deposition oxidation, lithography, bonding and packaging, reliability and yield, Printed Circuit boards, computer aided design in micro electronics, surface mount technology, Integrated circuit economics.

UNIT - IV

E-Manufacturing: Nano manufacturing techniques and micromachining, High Speed Machining and hot machining

UNIT -V

Rapid Prototyping: Working Principles, Methods, Stereo Lithography, Laser Sintering, Fused Deposition Method, Applications and Limitations, Rapid tooling, Techniques of rapid manufacturing

REFERENCES:

1. Manufacturing Engineering and Technology / Kalpakijian / Adisson Wesley, 1995.
2. Process and Materials of Manufacturing / R. A. Lindburg / 1th edition, PHI 1990.
3. Microelectronic packaging handbook / Rao. R. Thummala and Eugene, J. Rymaszewski / Van Nostrand Renihold,
4. MEMS & Micro Systems Design and manufacture / Tai — Run Hsu / TMGH
5. Advanced Machining Processes / V.K.Jain / Allied Publications.
6. Introduction to Manufacturing Processes / John A Schey / Mc Graw Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Core Elective – III)

DESIGN OPTIMIZATION

UNIT- I:

General Characteristics of mechanical elements, adequate and optimum design, principles of optimization, formulation of objective function, design constraints, classification of optimization problems. Single and multivariable optimization techniques

UNIT- II:

Technique of unconstrained minimization. Golden section, Random, Pattern and Gradient search methods, interpolation methods, equality and inequality constraints.

UNIT-III:

Direct methods and indirect methods using penalty function, Lagrange multipliers, Geometric programming, stochastic programming, Genetic algorithms

UNIT-IV:

Engineering applications, structural-design application axial and transverse loaded members for minimum cost, maximum weight. Design of shafts and torsion members, design optimization of springs.

UNIT-V:

Dynamics applications for two degree freedom system. vibration absorbers. Application in mechanisms.

REFERENCES:

1. Engineering Optimization -Theory and Practice/ Singerusu S. Rao/ New Age.
2. Optimum Design of Mechanical elements/ Johnson Ray C/ Wiley, John & Sons
3. Genetic Algorithms in search, Optimization and Machine/ Goldberg D. E. Addison/Wesley / NewYork..
4. Optimization for Engineering Design Algorithms and Examples/ Kalyanamoy Deb/Prentice Hall of India.
5. Introduction to Optimum Design/Jasbir S. Arora/ Academic Press/ Everest/ 3rd Edition

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Core Elective – IV)

ADVANCED MECHATRONICS

UNIT-I

Mechatronics systems, elements, levels of mechatronics system, Mechatronics design process, system, measurement systems, control systems, microprocessor-based controllers, advantages and disadvantages of mechatronics systems. Sensors and transducers, types, displacement, position, proximity, velocity, motion, force, acceleration, torque, fluid pressure, liquid flow, liquid level, temperature and light sensors.

UNIT-II

Solid state electronic devices, PN junction diode, BJT, FET, DIA and TRIAC. Analog signal conditioning, amplifiers, filtering. Introduction to MEMS & typical applications.

UNIT-III

Hydraulic and pneumatic actuating systems, Fluid systems, Hydraulic and pneumatic systems, components, control valves, electro-pneumatic, hydro-pneumatic, electro-hydraulic servo systems: Mechanical actuating systems and electrical actuating systems.

UNIT-IV

Digital electronics and systems, digital logic control, micro processors and micro controllers, programming, process controllers, programmable logic controllers, PLCs versus computers, application of PLCs for control.

UNIT-V

System and interfacing and data acquisition, DAQS, SCADA, A to D and D to A conversions; Dynamic models and analogies, System response. Design of mechatronics systems & future trends.

REFERENCES:

1. MECHATRONICS Integrated Mechanical Electronics Systems/KP Ramachandran & GK Vijaya Raghavan/WILEY India Edition/2008
2. Mechatronics Electronics Control Systems in Mechanical and Electrical Engineering by W Bolton, Pearson Education Press, 3rd edition, 2005.
3. Mechatronics Source Book by Newton C Braga, Thomson Publications, Chennai.
4. Mechatronics – N. Shanmugam / Anuradha Agencies Publishers.
5. Mechatronics System Design / Devdas shetty/Richard/Thomson.
6. Mechatronics/M.D.Singh/J.G.Joshi/PHI.
7. Mechatronics – Electronic Control Systems in Mechanical and Electrical Engg. 4th Edition, Pearson, 2012 W. Bolton
8. Mechatronics – Principles and Application Godfrey C. Onwubolu, Wlsevier, 2006 Indian print

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Core Elective – IV)

DESIGN AND MANUFACTURING OF MEMS AND MICRO SYSTEMS

UNIT I:

Overview and Working Principles of MEMS and Microsystems

MEMS & Microsystems, Evolution of Micro fabrication, Microsystems & Microelectronics, Microsystems & Miniaturization, Applications of MEMS in Industries, Micro sensors, Micro actuation, MEMS with Micro actuators Micro accelerometers, Micro fluidics.

UNIT II:

Engineering Science for Microsystems Design and Fabrication:

Atomic structure of Matter, Ions and Ionization, Molecular Theory of Matter and Intermolecular Force, Doping of Semiconductors, The diffusion Process, Plasma Physics, Electrochemistry, Quantum Physics

UNIT III:

Engineering Mechanics for Microsystems Design:

Static Bending of thin Plates, Mechanical Vibration, Thermo mechanics Fracture Mechanics, Thin-Film Mechanics, Overview of Finite Element Stress Analysis

UNIT IV:

Thermo Fluid Engineering & Microsystems Design:

Overview of Basics of Fluid Mechanics in Macro and Meso scales, Basic equations in Continuum Fluid dynamics, Laminar Fluid Flow in Circular Conduits, Computational Fluid Dynamics, Incompressible Fluid Flow in Micro conduits, Fluid Flow in Sub micrometer and Nano scale, Overview of Heat conduction in Solids, Heat Conduction in Multilayered Thin films and in solids in sub micrometer scale, Design Considerations, Process Design Mechanical Design, Mechanical Design using FEM, Design of a Silicon Die for a Micro pressure Sensor.

UNIT V:

Materials for MEMS & Microsystems and Their Fabrication:

Substrates and Wafers, Active substrate materials, Silicon as a substrate material, Silicon Compounds, Silicon Piezoresistors, Gallium Arsenide, Quartz, Piezoelectric Crystals and Polymers, Photolithography, Ion implantation, Diffusion and oxidation, chemical and physical vapor deposition, Etching, Bulk micro manufacturing, Surface Micromachining, The LIGA Process

REFERENCES:

1. MEMS & Microsystems: Design & Manufacture/ Tai-Ran Hsu/Tata Mc-Graw Hill., ed./2002
2. An Introduction to Microelectromechanical Systems Engineering/ Maluf, M./ Artech House, Boston, 2000
3. Micro robots and Micromechanical Systems/ Trimmer, W.S.N/ Sensors & Actuators, vol19, no.1989.
4. Applied Partial Differential Equations/ Trim, D.W/ PWS-Kent Publishing/ Boston 1990.
5. Fundamentals of Microfabrication. Madou, M/ CRC Press, Boca Raton, 1997.
6. The Finite Element Method in Thermomechanics/ Hsu, T.R / Alien & Unwin, London.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Core Elective – IV)

FUZZY LOGIC AND NEURAL NETWORKS

UNIT-I

Knowledge and Processing – Knowledge and Intelligence- logic frames- production systems. Fundamentals of Fuzzy logic-characteristics of fuzzy logic and systems-Fuzzy sets-Fuzzy number-Equality of fuzzy sets- Empty Fuzzy set –Fuzzy point-universal Fuzzy set. Operations on Fuzzy sets-Intersection-union –complement.

UNIT-II

Fuzzy Relations-classical N-Array Relation-Reflexivity-Anti reflexivity-symmetry –Transitivity-Equivalence-Binary fuzzy relations, operation on Fuzzy relations-Intersection-union-projection-Cartesian product.

UNIT-III

Fuzzy Implications, Translation rules, Triangular norms, Triangular conorm, Fuzzy Rule base system, Fuzzy logic controller, Defuzzification Methods, Fuzzy logic applications-prevention of Road accidents-control room temperature-Robot control system-domestic applications-Industrial applications.

UNIT-IV

Basic concepts of Neural Network-Processing units-connection between units-output rules- Network topologies-paradigms of learning –perception, Back-propagation, classification Models-Association Models, optimization models.

UNIT-V

Rule Based Neural Networks-Network Training –Application of Neural Network in Mathematical Modeling-Knowledge based approaches-applications in Mechanical Engineering –Fuzzy –Neural, example, Neuro –Fuzzy examples-Intelligence in Automation.

REFERENCES:

1. Intelligent Control Fuzzy Logic Applications/ Clarence W.de Silva/ CRS Press,1995.
2. Fuzzy logic &Neural Networks/ Chennakesava R. Alavala/ New Age International,2008
3. Fuzzy Logic with engineering Applications/ Timothy J. Ross/ Mc Graw Hill Inc., 1995.
4. Neural Networks in Computer Intelligence/ Limin Fu / Tata McGraw Hill Publishing Company Ltd.,2003
5. Stamations and Understanding Neural Networks and Fuzzy Logic/ V. Karthalopoulos Basic concepts Applications, IEE Neural Networks Council PHI 2001.
6. Neural Networks Algorithms, Applications/ James A. Freeman and David M. Skapura &Programming Techniques/ Pearson Education Asia,2001
7. Artificial Neural Networks/ Yegnarayane.B/ Prentice Hall- 2001.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Open Elective – II)

ENGINEERING RESEARCH AND METHODOLOGY

UNIT-I

Research Methodology: Objectives and Motivation of Research, Types of Research, Research Approaches, Significance of Research, Research Methods versus Methodology, Research and Scientific Method, Importance of Research Methodology, Research Process, Criteria of Good Research, Problems Encountered by Researchers in India, Benefits to the society in general. **Defining the Research Problem:** Definition of Research Problem, Problem Formulation, Necessity of Defining the Problem, Technique involved in Defining a Problem.

UNIT-II :

Literature Survey: Importance of Literature Survey, Sources of Information, Assessment of Quality of Journals and Articles, Information through Internet. **Literature Review:** Need of Review, Guidelines for Review, Record of Research Review.

UNIT-III:

Research Design: Meaning of Research Design, Need of Research Design, Feature of a Good Design, Important Concepts Related to Research Design, Different Research Designs, Basic Principles of Experimental Design, Developing a Research Plan, Design of Experimental Set-up, Use of Standards and Codes.

UNIT-IV:

Data Collection: Exploring the data, Description and Analysis of Data, Sample Design and Sampling, Role of Statistics for Data Analysis, Functions of Statistics, Estimates of Population, Parameters, Parametric V/s Non Parametric methods, Descriptive Statistics, Points of Central tendency, Measures of Variability, Measures of relationship, Inferential Statistics-Estimation, Hypothesis Testing, Use of Statistical software. **Data Analysis:** Deterministic and random data, Uncertainty analysis, Tests for significance: Chi-square, student's t test, Regression modeling, Direct and Interaction effects, ANOVA, F-test, Time Series analysis, Autocorrelation and Autoregressive modeling.

UNIT-V:

Research Report Writing: Format of the Research report, Style of writing report, References/Bibliography/Webliography, Technical paper writing/Journal report writing. **Research Proposal Preparation:** Writing a Research Proposal and Research Report, Writing Research Grant Proposal.

REFERENCES:

1. C.R Kothari, Research Methodology, Methods & Technique; New Age International Publishers, 2004
2. R. Ganesan, Research Methodology for Engineers, MJP Publishers, 2011
3. Y.P. Agarwal, Statistical Methods: Concepts, Application and Computation, Sterling Publs., Pvt., Ltd., New Delhi, 2004
4. Vijay Upagade and Aravind Shende, Research Methodology, S. Chand & Company Ltd., New Delhi, 2009
5. P. Ramdass and A. Wilson Aruni, Research and Writing across the Disciplines, MJP Publishers, Chennai, 2009

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

(Open Elective – II)

QUALITY ENGINEERING IN MANUFACTURING

UNIT - I

Quality Value and Engineering: An overall quality system, quality engineering in production design, quality engineering in design of production processes. Loss Function and Quality Level: Derivation and use of quadratle loss function, economic consequences of tightening tolerances as a means to improve quality, evaluations and types tolerances.(N-type,S-type and L-type)

UNIT II:

Tolerance Design and Tolerancing: Functional limits, tolerance design for N-type. L-type and S-type characteristics, tolerance allocation fbr multiple components. Parameter and Tolerance Design: Introduction to parameter design, signal to noise ratios, Parameter design strategy, some of the case studies on parameter and tolerance designs.

UNIT – III

Analysis of Variance (ANOVA): Introduction to ANOVA, Need for ANOVA, NO-way ANOVA, One-way ANOVA, Two-way ANOVA, Critique of F-test, ANOVA for four level factors, multiple level factors.

UNIT - IV

Orthogonal Arrays: Typical test strategies, better test strategies, efficient test strategies, steps in designing, conducting and analyzing an experiment. Interpolation of Experimental Results: Interpretation methods, percent contributor, estimating the mean.

UNIT - V

Six Sigma and the Technical System: Six sigma DMAIC methodology, tools fpr process improvement, six sigma in services and small organizations, statistical foundations, statistical methodology.

REFERENCES:

1. Taguchi Techniques for Quality Engineering / Phillip J. Ross / McGraw Hill/ Intl. II Edition, 1995.
2. Quality Engineering in Production systems / G. Taguchi, A. Elsayed et al / Mc.Graw Hill Intl. Edition, 1989.
3. Taguchi Methods explained: Practical steps to Robust Design / Papan P. Bagchi / Prentice Hall Pvt. Ltd., New Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year II Sem. (CAD/CAM)

MANUFACTURING SIMULATION & PRECISION ENGINEERING LABORATORY

A. MANUFACTURING SIMULATION

The students will be given training on the use and application of the following software to manufacturing problems:

1. Auto MOD Software.
2. PROMOD
3. SLAM-II
4. CAFIMS
5. Flexsim

They also learn how to write sub routines in C-language and interlinking with the above packages. Problems for modelling and simulation experiments:

1. AGV planning
2. ASRS simulation and performance evaluation
3. Machines, AGVs and AS/RS integrated problems
4. JIT system
5. Kanban flow
6. Material handling systems
7. M.R.P. Problems
8. Shop floor scheduling etc.

B. PRECISION ENGINEERING

1. Hydraulic and Pneumatic circuits
2. Closed loop control systems
3. Study of the chip formation in turning process
4. Study of operation of tool and cutter grinder, twist drill grinder, Centreless grinder
5. Determination of cutting forces in turning
6. Experiments in unconventional manufacturing processes-AJM and study of USM, EDM, Laser Machining and Plasma spraying
7. Inspection of parts using tool makers microscope, roughness and form tester
8. Study of micro-controllers, programming on various CNC machine tools and also controllers
9. Studies on PLC programming
10. Study and programming of robots
11. Condition monitoring in machining process using acoustic emission.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH (STRUCTURAL ENGINEERING)

COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Theory of Elasticity and plasticity	25	75	4	--	4
Core Course II	Theory of Plates	25	75	4	--	4
Core Course III	Advanced Structural Analysis	25	75	4	--	4
Core Elective I	Advanced Concrete Technology Tall Buildings Advanced Foundation Engineering	25	75	4	--	4
Core Elective II	Advanced R.C. Design Bridge Engineering Plastic Analysis & Design	25	75	4	--	4
Open Elective I	Computer Oriented Numerical Methods Reliability Engineering Experimental Stress Analysis	25	75	4	--	4
Laboratory I	Advanced Concrete Lab	25	75	--	4	2
Seminar I	Seminar	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Finite Element Method	25	75	4	--	4
Core Course V	Structural Dynamics	25	75	4	--	4
Core Course VI	Pre-stressed Concrete	25	75	4	--	4
Core Elective III	Advanced Steel Design Soil Dynamic & Foundation Engineering Stability of Structures	25	75	4	--	4
Core Elective IV	Design of shells & folded plates Earthquake Resistant Design of Buildings Fracture Mechanics	25	75	4	--	4
Open Elective II	Repair & Rehabilitation of Buildings Composite Materials Optimisation Techniques	25	75	4	--	4
Laboratory II	CAD Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C
Comprehensive Viva-Voce	--	100	--	--	4
Project work Review I	50	--	--	24	12
Total Credits			--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C
Project work Review II	50	--	--	8	4
Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits			--	24	16



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

THEORY OF ELASTICITY AND PLASTICITY

Objectives : To impart knowledge on the basic concepts of theory of elasticity, and solve the Structural Engineering problems.

UNIT-I

Introduction: Elasticity - notation for forces and stresses - components of stresses - components of strain - Hooks law. Plane stress and plane strain analysis - plane stress - plane strain - differential equations of equilibrium - boundary conditions - compatibility equations - stress function - boundary condition.

UNIT II.

Two dimensional problems in rectangular coordinates - solution by polynomials - Saint-Venant's principle - determination of displacements - bending of simple beams - application of Fourier series for two dimensional problems - gravity loading. Two dimensional problems in polar coordinates - stress distribution symmetrical about an axis - pure bending of curved bars - strain components in polar coordinates - displacements for symmetrical stress distributions - simple symmetric and asymmetric problems - general solution of two-dimensional problem in polar coordinates - application of general solution in polar coordinates.

UNIT III.

Analysis of stress and strain in three dimensions - principal stresses - stress ellipsoid - director surface - determination of principal stresses - max shear stresses – homogeneous deformation - principal axes of strain rotation. General Theorems: Differential equations of equilibrium - conditions of compatibility - determination of displacement - equations of equilibrium in terms of displacements - principle of superposition - uniqueness of solution - the reciprocal theorem.

UNIT IV.

Torsion of Prismatic Bars - torsion of prismatic bars - bars with elliptical cross sections - other elementary solution - membrane analogy - torsion of rectangular bars - solution of torsion problems by energy method - use of soap films in solving torsion problems - hydro dynamical analogies - torsion of shafts, tubes, bars etc. Bending of Prismatic Bars: Stress function - bending of cantilever - circular cross section - elliptical cross section - rectangular cross section - bending problems by soap film method - displacements.

UNIT V.

Theory of Plasticity: Introduction - concepts and assumptions - yield criterions.

REFERENCES

1. Theory of Elasticity by Timoshenko, McGrawhill Publications.
2. Theory of Plasticity by J.Chakrabarty, McGrawhill Publications.
3. Theory of Elasticity by Y.C.Fung.
4. Theory of Elasticity by Gurucharan Singh.

Course outcomes: The learner will be able to solve problems of elasticity and plasticity and be able to apply numerical methods to solve continuum problems.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

THEORY OF PLATES

Objectives : To impart knowledge on the behavior of plates and to analyse the problems pertaining to beams on elastic foundation.

UNIT I

Cylindrical Bending : Different kind of plates – Assumptions - Derivation of differential equation for cylindrical bending of long rectangular plates - Analysis of uniformly loaded rectangular plates with edges simply supported and fixed subjected to uniform load.

Pure Bending of Plates : Slope and curvature of slightly bent plates – Relations between moments and curvature - Particular cases of pure bending - Strain energy in pure bending –Energy methods like Ritz and Galerkin Methods to rectangular plates subjected to simple loadings.

UNIT II

Small Deflection Theory of Thin Rectangular Plates : Assumptions – Derivation of governing differential equation for thin plates – Boundary conditions – simply supported plate under sinusoidal load – Navier's solution – Application to different cases – Levy's solution for various boundary conditions subjected to different loadings like uniform and hydrostatic pressure.

UNIT III

Circular Plates : Symmetrical loading – Relations between slope, deflection, moments and curvature – Governing differential equation – Uniformly loaded plates with clamped and simply supported edges – Central hole – bending by moments and shearing forces uniformly distributed.

Orthotropic Plates : Introduction – Bending of anisotropic plates - Derivation of governing differential equation – Determination of Rigidities in various cases like R.C. slabs, corrugated sheet – Application to the theory of grid works.

UNIT IV

Plates on Elastic Foundations : Governing differential equation – deflection of uniformly loaded simply supported rectangular plate – Navier and Levy type solutions - Large plate loaded at equidistant points by concentrated forces P.

UNIT V

Buckling of Plates: Governing equation for Bending of plate under the combined action of in-plane loading and lateral loads – Buckling of rectangular plates by compressive forces acting in one and two directions in the middle plane of plate

Finite Difference Methods: Introduction - Application to rectangular plates subjected to simple loading.

REFERENCES:

1. Theory of Plates and Shells by Timoshenko, McGraw Hill Book Co., New York.
2. Theory and Analysis of Plates by P. Szilard, Prentice Hall.
3. Theory of Plates by Chandrasekhar, University Press.
4. Plate Analysis by N. K. Bairagi, Khanna Publishers. New Delhi.

Outcomes : The learner will be able to understand the behavior of plates for loadings and boundary conditions.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED STRUCTURAL ANALYSIS

Objectives : To impart knowledge on the analysis of indeterminate structures like continuous beams, trusses and portal frames.

UNIT I

Introduction to matrix methods of analysis - static indeterminacy and kinematic indeterminacy - degree of freedom - coordinate system - structure idealization stiffness and flexibility matrices - suitability element stiffness equations - elements flexibility equations - mixed force - displacement equations - for truss element, beam element and tensional element.

Transformation of coordinates - element stiffness matrix - and load vector - local and global coordinates.

UNIT II

Assembly of stiffness matrix from element stiffness matrix - direct stiffness method - general procedure - band matrix - semi bandwidth - computer algorithm for assembly by direct stiffness matrix method.

UNIT III

Analysis of plane truss - continuous beam - plane frame and grids by flexibility methods.

UNIT IV

Analysis of plane truss - continuous beam - plane frame and grids by stiffness methods.

UNIT V. Special analysis procedures - static condensation and sub structuring - initial and thermal stresses.

Shear walls- Necessity - structural behaviour of large frames with and without shear walls - approximate methods of analysis of shear walls.

REFERENCES

1. Matrix Analysis of Frames structures by William Weaver J.R and James M.Gere, CBS publications.
2. Advanced Structural Analysis by Ashok.K.Jain, New Channel Brothers.
3. Basic Structural Analysis by C.S.Reddy.
4. Matrix Structural Analysis by Madhu B. Kanchi.
5. Indeterminate Structural Analysis by K.U.Muthu *et al.*, I.K.International Publishing House Pvt. Ltd.
6. Matrix Methods of Structural Analysis by J.Meek.
7. Structural Analysis by Ghali and Neyveli.

Outcomes: The learner will be able to analyse different indeterminate structures using Matrix methods.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED CONCRETE TECHNOLOGY
(Core Elective – I)

Objectives : To impart knowledge on concrete making materials, concrete mix design for proportioning and their testing.

UNIT – I

Concrete Making Materials: Cement- Bogue's compounds – Hydration Process– Types of cement – Aggregates – Gradation Charts – Combined aggregate-Alkali Silica Reaction -Admixtures – Chemical and Mineral admixtures.

UNIT – II

Fresh and Hardened Concrete: Fresh Concrete - workability tests on Concrete Setting times of Fresh Concrete - Segregation and bleeding.

Hardened Concrete : Abram's law- Gel space ratios, Maturity Concept – Stress Behaviour – Creep and Shrinkage – Durability tests on concrete - Non destructive testing of concrete.

UNIT - III

High Strength Concrete – Micro structure – Manufacturing and Properties- Design of HSC Using Erintroy Shaklok Method- Ultra High Strength Concrete.

High Performance Concrete- Requirements and properties of High Performance Concrete- Design Considerations.

UNIT –IV

Special Concrete: Self Compacting concrete – Polymer concrete – Fiber reinforced concrete – Reactive Powder concrete – Requirements and Guidelines – Advantages and Applications. Light weight concrete.

Concrete mix design : Quality Control - Quality assurance - Quality audit- Mix Design method - BIS method, ACI method, DOE method.

UNIT –V

Form work – materials – structural requirements – form work systems – connections – specifications – design of form work – shores – removal for forms – reshoring – failure of form work.

TEXT BOOKS:

1. Properties of Concrete by A.M.Neville, ELBS publications.
2. Concrete: Micro Structure, Properties and Materials by P.K.Mehta, Tata Mc Graw Hill Publishing House Pvt. Ltd
3. Concrete Technology by A.K. Santhakumar, Oxford Press.
4. Concrete Technology by M.S.Shetty, S.Chand & Co.

REFERENCES:

1. Special Structural concretes by Rajat Siddique, Galgotia Publications.
2. Design of Concrete Mixes by N.Krishna Raju, CBS Publications.

Outcomes : The learner will be able to design concrete mixes of different grades and also use the special concretes.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

TALL BUILDINGS
(Core Elective – I)

Objective : To impart knowledge on analysis of tall buildings.

Unit-I

Introduction : Classification of Buildings – Low-rise, medium-rise, high-rise – Evolution of tall buildings – Ordinary framed buildings & Shear-wall buildings –Behaviour of buildings under lateral loads like Wind loads, Earthquake loads & Blast loads – Basic structural & functional design requirements –Strength, Stiffness & Stability.

Unit-II

Lateral load resisting elements : Frames, Shear walls & Tubes – Shear, Bending & combined modes of deformation – Structural behavior of Rigid frames – Simplified methods of analysis – Substitute frame method, Portal method, Cantilever method, Equivalent frame method –Structural behaviour of Shear walls – Approaches of analysis – Elastic continuum approach & Discrete approach -- Structural behavior of Tubes –Actions.

Unit-III

Choice of System for a Building : Frame building, Shear wall building, Shear walls acting with frames, Single framed tubes – Other structural forms – Staggered Wall-beam system, Tube-in-tube system, Base isolation technique for earthquake resistance. Load distribution in a tall building – Load resisted by different shear walls & frames – Determinate & Indeterminate problems – Equivalent Stiffness method.

Unit-IV

Methods of Analysis : Shear walls without Openings – Estimation of Stiffness by simple Cantilever theory & Deep beam theory – Shear walls with Openings – Equivalent frame for large openings – Muto's method for small openings –Elastic Continuum approach – Coull & Chowdhry's method – Design Charts – Limitations of Continuum approach. Shear wall- Frame Interaction : Sharing of loads between wall & frame - Different methods – comparison -- Khan & Sbrounis' method – Design charts - - MacLeod's method - Advantages & limitations -- Cooperation of Floor slabs – Equivalent width.

Unit-V

Modern Methods : Analysis of Tall buildings by Stiffness method – Available Softwares for analysis of tall buildings.

REFERENCES

1. Concrete & Composite Design of Tall Buildings by Taranath B., Mc Graw Hill.
2. Reinforced Concrete Design of Tall Buildings by Bungales. Taranath, CRC Press.
3. Analysis of Shear Walled Buildings by S. M. A. Kazimi & R. Chandra, Tor-steel Research Foundation, Calcutta, India.
4. Analysis of Framed Structures by Gere & Weaver
5. Design of Building Structures by Wolfgang Schuller, Prentice Hall

Outcomes : The learner will be able to analyse and chose a appropriate systems for tall buildings.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED FOUNDATION ENGINEERING
(Core Elective – I)

Objective: To determine the bearing capacity of shallow and deep foundations and to estimate settlements of structures subjected to external loads, leading to design of foundations resting on soils.

Unit-I

Soil Exploration: Exploration Methods; Planning the Exploration Program; Boring and Sampling; In Situ Tests: Standard & Cone Penetration Tests, Field Vane, Dilatometer, Pressure meter; Rock Sampling, Core Recovery, RQD; Geophysical Exploration; Preparation of Soil Report, Case Studies.

Unit-II

Shallow Foundations: Bearing Capacity:- Shear Failure; Effect of Water Table; Footings with Eccentric or Inclined Loads, Footings on Layered Soils, Slopes on finite layer with a Rigid Base at Shallow Depth, effect of compressibility of soil, on soils with strength increasing with depth, Plate Load tests, Presumptive bearing capacity.

Unit-III

Settlement: Components – Immediate, Primary and Secondary Settlements, Consolidation, Stresses and Displacements in Homogeneous, Layered and Anisotropic Soils; Bearing Pressure using SPT, CPT, Dilatometer and Pressure meter; Settlement of foundations on Sands-Schmertmann and Burland & Burbridge methods; Structure Tolerance to Settlement and Differential Settlements, Rotation, Codal Provisions.

Unit-IV

Deep Foundations: Single Pile: Vertically loaded piles, Static capacity- α , β and λ Methods, Dynamic formulae; Wave Equation Analyses; Point Bearing Resistance with SPT and CPT Results; Bearing Resistance of Piles on Rock; Settlement; Pile Load Test; Uplift Resistance; Laterally Loaded Piles -Ultimate Lateral Resistance; Negative Skin Friction; Batter Piles; Under Reamed Piles; Ultimate Capacity of Pile Groups in Compression, Pullout & Lateral Load; Efficiency; Settlements of Pile Groups; Interaction of Axially & Laterally Loaded Pile Groups, Codal Provisions.

Unit-V

Special Topics of Foundation Engineering

Foundations on Collapsible Soils: Origin and occurrence, Identification, Sampling and Testing, Preventive and Remedial measures.

Foundations on Expansive Soils: The nature, origin and occurrence, Identifying, testing and evaluating expansive soils, typical structural distress patterns and Preventive design & construction measures.

***Introduction to Reliability-Based Design:** Brief introduction of probability and statistics, LRFD for structural strength requirements, LRFD for geotechnical strength requirements, Serviceability requirements

TEXT BOOKS

1. Das, B. M. - Principles of Foundation Engineering 5th Edition Nelson Engineering (2004)
2. Donald P Coduto – Foundation Design Principles and Practices, 2nd edition, Pearson, Indian edition, 2012. Phi Learning (2008)

REFERENCE BOOKS

1. Bowles, J. E. - Foundation Analysis & Design 5th Edition McGraw-Hill Companies, Inc. (1996)
2. Poulos, H. G. & Davis, E. H. - Pile Foundation Analysis and Design john wiley & sons inc (1980-08)
3. Tomlinson, M. J. - Foundation Design and Construction - Prentice Hall (2003).
4. Baecher, G.B. & Christian, J.T. – Reliability and Statistics in Geotechnical Engineering, Wiley Publications (2003)

Outcome: Students should be in a position to design foundations for varieties of structures resting on soil deposits, and appreciate the importance of reliability based design in geotechnical engineering.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED REINFORCED CONCRETE DESIGN
(Core Elective – II)

Objectives : To impart knowledge on the behavior and design on various reinforced concrete structural elements.

UNIT I

Basic Design Concepts: Behaviour in flexure, Design of singly reinforced rectangular sections, Design of doubly reinforced rectangular sections, Design of flanged beams, Design of shear, Design for Torsion, Limit state of Serviceability: Deflections of Reinforced concrete beams and slabs, short term deflection and long term deflection, estimation of crack width in RCC members, calculation of crack widths.

UNIT II

Limit Analysis of R.C.Structures: Rotation of a plastic hinge, Redistribution of moments, moment rotation characteristics of RC member, I.S. code provisions, applications for fixed and continuous beam. Yield line analysis for slabs: Upper bound and lower bound theorems – yield line criterion – Virtual work and equilibrium methods of analysis for square and circular slabs with simple and continuous end conditions.

UNIT III

Design of Ribbed slabs, Flat slabs: Analysis of the Slabs for Moment and Shears, Ultimate Moment of Resistance, Design for shear, Deflection, Arrangement of Reinforcements.

Flat slabs: Direct design method – Distribution of moments in column strips and middle strip-moment and shear transfer from slabs to columns – Shear in Flat slabs-Check for one way and two way shears - Introduction to Equivalent frame method. Limitations of Direct design method, Distribution of moments in column strips and middle strip.

UNIT IV

Design of Reinforced Concrete Deep Beams & Corbels: Steps of Designing Deep Beams, Design by IS 456, Checking for Local Failures, Detailing of Deep Beams, Analysis of Forces in a Corbels , Design of Procedure of Corbels, Design of Nibs.

UNIT V

Design of Compression members: Estimation of effective length of a column-Code requirements on Slenderness Limits, Design of Short Columns under Axial Compression, Design of Short Columns with Uniaxial Bending, Design of Short Columns under Biaxial Bending, Design of Slender Columns.

Design of Combined Footings- Distribution of soil Pressure – Geometry of Two Column Combined Footing – Design Considerations in Combined Footing for Two – Columns.

TEXT BOOKS:

1. Reinforced concrete design by S. Unnikrishna Pillai & Menon, Tata Mc. Graw Hill, 2nd Edition, 2004
2. Advanced Reinforced Concrete Design – P.C. Varghese, Prentice Hall of India, 2008
3. Limit state theory and design of reinforced concrete by Dr. S.R. Karve and Dr. V.L. Shah, Standard Publishers, Pune, 3rd Edition, 1994.
4. Principles of Reinforced Concrete Design by Mete A. Sozen, Toshikatsu Ichinose, Santiago Pujol July 14, 2014 CRC Press

REFERENCE BOOKS:

1. Reinforced concrete design by Kenneth Leet, Tata Mc. Graw-Hill International, editions, 2nd edition, 1991.
2. Reinforced concrete structural elements – Behaviour, Analysis and design by P.Purushotham, Tata Mc.Graw-Hill, 1994.
3. Design of concrete structures – Arthus H. Nilson, David Darwin, and Charles W. Dolar, Tata Mc. Graw-Hill, 3rd Edition, 2005.



4. Reinforced concrete structures, Vol.1, by B.C. Punmia, Ashok Kumar Jain and Arun Kumar Jain, Laxmi Publications, 2004.
5. Reinforced Concrete Structures – I.C. Syal & A.K. Goel, S. Chand, 2004.

Outcomes : The learner will be able to design the reinforced concrete elements like beams, slabs and compression members.

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

BRIDGE ENGINEERING
(Core Elective – II)

Objectives : To impart knowledge on the behavior and design aspects of various types of bridges.

UNIT I.

Concrete Bridges: Introduction-Types of Bridges-Economic span length-Types of loading-Dead load-live load-Impact Effect-Centrifugal force-wind loads-Lateral loads-Longitudinal forces-Sesmic loads-Frictional resistance of expansion bearings-Secondary Stresses-Temperature Effect-Erection Forces and effects-Width of roadway and footway-General Design Requirements.

UNIT II.

Solid slab Bridges: Introduction-Method of Analysis and Design.

UNIT III

Girder Bridges: Introduction-Method of Analysis and Design-Courbon's Theory, Grillage analogy

UNIT IV.

Pre-Stressed Concrete Bridges: Basic principles-General Design requirements-Mild steel reinforcement in prestressed concrete member-Concrete cover and spacing of pre-stressing steel-Slender beams-Composite Section-Propped-Design of Propped Composite Section-Unpropped composite section-Two-stage Prestressing-Shrinking stresses-General Design requirements for Road Bridges.

UNIT V.

Analysis of Bridge Decks: Harmonic analysis and folded plate theory-Grillage analogy- Finite strip method and FEM. Sub-structure of bridges: Substructure- Beds block-Piers- Pier Dimensions- Design loads for piers- Abutments- Design loads for Abutments.

REFERENCES

1. Design of Concrete Bridges by M.G.Aswani, V.N.Vazirani and M.M.Ratwani.
2. Essentials of Bridge Engineering by Johnson Victor, Oxford & IBH.
3. Bridge Deck Behaviour by E.C.Hambly.
4. Design of Bridges by N.Krishna Raju, Oxford & IBH.
5. Design of Bridges by V.V.Sastry, Dhanpat Rai & Co
6. Concrete Bridge Design and Practice by V.K.Raina.

Outcomes: The learner will be able to analyze and design of different types of bridges.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

PLASTIC ANALYSIS AND DESIGN
(Core Elective – II)

Objectives : To impart knowledge on the analysis of steel structures like continuous beams, steel frames and connection, using Plastic Analysis.

UNIT – I

Analysis of Structures for Ultimate Load: Fundamental Principles – statical method of Analysis – Mechanism method of analysis – Method of analysis, Moment check – Carry over factor – Moment Balancing Method.

UNIT - II

Design of Continuous Beams: Continuous Beams of uniform section throughout – Continuous Beams with different cross-sections.

UNIT - III

Secondary Design Problems: Introduction – Influence of Axial force on the plastic moment – influence of shear force – local buckling of flanges and webs – lateral buckling – column stability.

UNIT - IV

Design of Connections: Introduction – requirement for connections – straight corner connections – Haunched connection – Interior Beam-Column connections.

UNIT - V

Design of Steel Frames: Introduction – Single bay, single storey frames – simplified procedures for Single span frames – Design of Gable frames with Haunched Connection. Ultimate Deflections: Introduction – Deflection at ultimate load – Deflection at working load – Deflections of Beams and Single span frames.

REFERENCES:

1. Plastic Design of Steel Frames, L.S.Beedle.
2. Plastic Analysis, B.G.Neal.
3. Plastic Analysis, Horve.

Outcomes : The learner will be able to design continuous beams and steel frames.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

COMPUTER ORIENTED NUMERICAL METHODS
(Open Elective – I)

Objectives : To impart knowledge about various methods of analysing linear equations and understand the different mathematical techniques.

Unit I:

Solutions of linear equations: Direct method – Cramer's rule, Gauss – Elimination method- Gauss – Jordan elimination – Triangulation (LU Decomposition) method – Iterative methods Jacobi – Iteration method – Gauss – Seidel iteration, Successive over –relaxation method.

Eigen values and eigen vectors: Jacobi method for symmetric matrices- Given's method for symmetric matrices-Householder's method for symmetric matrices-Rutishauser method of arbitrary matrices – Power method.

UNIT II:

Interpolation: Linear Interpolation - Higher order Interpolation - Lagrange Interpolation – Interpolating polynomials using finite differences- Hermite Interpolation -piece-wise and spline Interpolation.

Unit III

Finite Difference and their Applications: Introduction- Differentiation formulas by Interpolating parabolas – Backward and forward and central differences- Derivation of Differentiation formulae using Taylor series- Boundary conditions- Beam deflection – Solution of characteristic value problems- Richardson's extrapolation- Use of unevenly spaced pivotal points- Integration formulae by interpolating parabolas- Numerical solution to spatial differential equations

UNIT IV.

Numerical Differentiation: Difference methods based on undetermined coefficients- optimum choice of step length– Partial differentiation.

Numerical Integration: Method based on interpolation-method based on undetermined coefficient – Gauss – Lagrange interpolation method- Radaua integration method- composite integration method – Double integration using Trapezoidal and Simpson's method.

UNIT V

Ordinary Differential Equation: Euler's method – Backward Euler method – Mid point method – single step method, Taylor's series method- Boundary value problems.

REFERENCES:

1. Numerical methods for scientific and engineering computations. M.K.Jain-S.R.K.Iyengar – R.K.Jain Willey Eastern Limited.
2. Numerical methods by S.S.Shastry.
3. Applied numerical analysis by – Curtis I.Gerala- Addison Wasley – published campus.
4. Numerical methods for Engineers Stevan C.Chopra, Raymond P.Canal Mc. Graw Hill book company.
5. C Language and Numerical methods by C.Xavier – New age international publisher.
6. Computer based numerical analysis by Dr. M.Shanta Kumar, Khanna Book publishers, New Delhi.

Outcomes : The learner will be able to apply various mathematical techniques to Structural engineering problems.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

RELIABILITY ENGINEERING
(Open Elective – I)

Objectives : To impart knowledge on concepts of reliability, discrete distributions and hierarchical systems.

UNIT I

Basic Concepts of Reliability : Introduction, Reliability and Quality, Failures and Failure Modes, Causes of Failures and Unreliability, Maintainability and Availability, History of Reliability, Reliability Literature.

UNIT II

Design for Reliability : Constraints and Considerations : Reliability Analysis, Mathematical Models and Numerical Evaluation, Designing for Higher Reliability, Redundancy Techniques, Equipment Hierarchy, Reliability and Cost.

UNIT –III

Discrete Distributions : Density and distributions, Continuous Distributions, Numerical Characteristics of Random Variables, Laplace Transform.

UNIT-IV

Maintainability and Availability Concepts : Introduction, Maintainability Function, Availability Function, Frequency of Failure, Two-unit parallel system with Repair, K-out-of M systems, Preventive Maintenance.

UNIT-V:

Hierarchical Systems : Introduction, Logic Diagram Approach, Conditional Probability Approach, System Cost, Illustrations and Discussions, Reliability Approximations.

TEXT BOOKS :

1. Reliability Engineering by E. Balagurusamy, McGraw Hill Education(India) Pvt. Ltd.
2. Reliability Evaluation of Engineering Systems by Roy Billinton & Ronald N. Allan, Springer.
3. Reliability of Structures, Second Edition by Andrzej S. Nowak, Kevin R. Collins December 20, 2012 by CRC Press

Outcomes : The learner will be able to design a reliable systems and develop and analyse reliability and cost models for hierarchical systems.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

EXPERIMENTAL STRESS ANALYSIS
(Open Elective – I)

Objectives : To impart knowledge on the strain measurement, brittle coating and photo elasticity.

UNIT I

Basic equations and Plane Elasticity Theory: Introduction, Strain equations of Transformation, Compatibility, Stress-Strain Relations-Two dimensional State of Stress. The Plane-Elastic problem, The Plane-Strain Approach, Plane Stress, Airy's Stress function-Cartesian Co-ordinates-Two dimensional problems in Polar Co-ordinates, Polar Components of Stress in terms of Airy's Stress function, Forms.

Principles of Experimental Approach: Merit of Experimental Analysis introduction, uses of experimental stress analysis-Advantages of experimental stress analysis, Different methods, Simplification of problems.

UNIT II

Strain Measurement using Strain Gauges: Definition of strain and its relation to Experimental Determinations, properties of strain-gauge systems, Types of strain gauges, Mechanical and Optical strain gauges. Electrical Strain Gauges - Introduction, LVDT - resistance strain gauge - various types - gauge factor, Materials for adhesion base, etc.

Strain Rosettes: Introduction, The three element rectangular Rosette - The delta rosette - Corrections for Transverse strain effects.

UNIT III

Brittle Coating Method: Introduction, Coating stresses - Failure theories - Brittle coating Crack pattern - Crack detection - Types of Brittle coating - Test procedures for brittle coating analysis - Calibration procedures - Analysis of brittle coating data.

UNIT IV

Theory of Photo Elasticity: Introduction, Temporary double refraction - The stress optic law - Effects of stressed model in a Polaris cope for various arrangements - Fringe sharpening, Brewster stress optic law.

UNIT V

Two Dimensional Photo Elasticity: Introduction, Isochromatic Fringe patterns - Isoclinic fringe patterns, passage of light through plane Polaris cope and circular Polaris cope, Isoclinic fringe pattern - Computation techniques - calibration methods, separation methods, scaling Model to Proto type stress- Materials for photo - elasticity, properties of photo elastic materials.

REFERENCES :

1. Experimental Stress Analysis by J.W.Dally and W.F.Riley
2. Experimental Stress Analysis by Dr. Sadhu Singh
3. Experimental Stress Analysis by Dove and Adams

Outcomes : The learner will be able to understand the properties of strain-gauge systems and the computation techniques.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED CONCRETE LABORATORY

Objectives : To impart knowledge on the test on cement and aggregates.

1. Tests on cement - Consistency, Setting times, Soundness, Compressive Strength.
2. Gradation Charts of Aggregates.
3. Bulking of fine Aggregate.
4. Aggregate Crushing and Impact value
5. Workability Tests on Fresh self compacting concrete
6. Air Entrainment Test on fresh concrete.
7. Marsh cone test.
8. Permeability of Concrete.
9. Non Destructive Testing of Concrete.
10. Accelerated Curing of Concrete.
11. Influence of W/C ratio on strength and Aggregate / Cement ratio on workability and Strength
12. Influence of Different Chemical Admixtures on concrete.

Outcomes : The learner will be able to understand the properties of the materials and the behavior of the concrete.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH (STRUCTURAL ENGINEERING)

COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Finite Element Method	25	75	4	--	4
Core Course V	Structural Dynamics	25	75	4	--	4
Core Course VI	Pre-stressed Concrete	25	75	4	--	4
Core Elective III	Advanced Steel Design Soil Dynamic & Foundation Engineering Stability of Structures	25	75	4	--	4
Core Elective IV	Design of shells & folded plates Earthquake Resistant Design of Buildings Fracture Mechanics	25	75	4	--	4
Open Elective II	Repair & Rehabilitation of Buildings Composite Materials Optimisation Techniques	25	75	4	--	4
Laboratory II	CAD Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

FINITE ELEMENT METHODS

Objectives : To impart knowledge about various finite element techniques and development of finite element code.

UNIT I

Introduction: Concepts of FEM - steps involved - merits and demerits - energy principles – Discretization - Raleigh - Ritz method of functional approximation.
Principles of Elasticity: Stress equations - strain displacement relationships in matrix form plane stress, plane strain and axi-symmetric bodies of revolution with axi-symmetric loading.

UNIT II

One dimensional FEM: Stiffness matrix for beam and bar elements - shape functions for 1-D elements.

Two dimensional FEM: Different types of elements for plane stress and plane strain analysis - displacement models - generalized coordinates - shape functions - convergent and compatibility requirements - geometric invariance - natural coordinate system - area and volume coordinates - generation of element stiffness and nodal load matrices

UNIT III

Isoparametric formulation: Concept - different isoparametric elements for 2D analysis -formulation of 4-noded and 8-noded isoparametric quadrilateral elements - Lagrange elements - serendipity elements.

Axi Symmetric Analysis: bodies of revolution - axi symmetric modeling - strain displacement relationship - formulation of axi symmetric elements.

Three dimensional FEM: Different 3-D elements-strain-displacement relationship – formulation of hexahedral and isoparametric solid element.

UNIT IV

Introduction to Finite Element Analysis of Plates: Basic theory of plate bending - thin plate theory - stress resultants - Mindlin's approximations - formulation of 4-noded isoperimetric quadrilateral plate element – Shell Element.

UNIT V

Introduction to non – linear finite analysis – basic methods – application to Special structures.

REFERENCES:

1. Concepts and Applications of Finite Element Analysis by Robert D.Cook, David S. Malkus and Michael E. Plesha, John Wiley & Sons.
2. Finite element Methods by OC Zienkiewicz
3. Finite element analysis, theory and programming by GS Krishna Murthy.
4. Introduction to Finite element Method by Tirupathi Chandra Patla and Belugunudu.
5. Introduction to Finite element Method by JN Reddy.

Outcome : The learner will be able to solve continuum problems using finite element analysis.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

STRUCTURAL DYNAMICS

Objectives : To impart knowledge on the fundamental of structural dynamics and their applications.

UNIT I:

Theory of vibrations: Introduction - Elements of vibratory system - Degrees of Freedom - Continuous System - Lumped mass idealization - Oscillatory motion - Simple Harmonic motion - Vectorial representation of S.H.M. - Free vibrations of single degree of freedom system - undamped and damped vibrations - critical damping - Logarithmic decrement - Forced vibration of SDOF systems - Harmonic excitation - Dynamic magnification factor – Phase angle – Bandwidth

UNIT II

Introduction to Structural Dynamics : Fundamental objectives of dynamic analysis -Types of prescribed loading - Methods of discretization - Formulation of equations of motion by different methods – Direct equilibration using Newton's law of motion / D'Alembert's principle, Principle of virtual work and Hamilton principle.

Single Degree of Freedom Systems : Formulation and solution of the equation of motion - Free vibration response - Response to Harmonic, Periodic, Impulsive and general dynamic loadings - Duhamel integral.

UNIT III

Multi Degree of Freedom Systems : Selection of the degrees of Freedom - Evaluation of structural property matrices - Formulation of the MDOF equations of motion -Undamped free vibrations - Solutions of Eigen value problem for natural frequencies and mode shapes - Analysis of Dynamic response – Normal co-ordinates - Uncoupled equations of motion - Orthogonal properties of normal modes - Mode superposition procedure.

UNIT IV

Practical Vibration Analysis: Introduction - Stodola method - Fundamental mode analysis - Analysis of second and higher modes - Holzer method - Basic procedure.

Continuous Systems: Introduction - Flexural vibrations of beams - Elementary case – Derivation of governing differential equation of motion - Analysis of undamped free vibrations of beams in flexure - Natural frequencies and mode-shapes of simple beams with different end conditions - Principles of application to continuous beams.

UNIT V

Introduction to Earthquake Analysis: Introduction - Excitation by rigid base translation - Lumped mass approach - SDOF and MDOF systems - I. S. Code methods of analysis for obtaining response of multi storeyed buildings.

REFERENCES:

1. Dynamics of Structures by Clough & Penzien, McGraw Hill, New york
2. Structural Dynamics by Mario Paz, C.B.S Publishers, New Delhi.
3. Dynamics of Structures by Anil K. Chopra, Pearson Education (Singapore), Delhi.
4. I.S: 1893 (Part 1) - 2002, "Code of practice for Earthquake resistant design of Structures"

Outcomes : The learner will be able to understand the equation of motion, dynamics response of single and multi degree-of freedom systems.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

PRE-STRESSED CONCRETE

Objectives : To impart knowledge on basics of prestressing and designing of different structural elements using Prestressing techniques.

UNIT I.

General Principles of Prestressed Concrete : Pre-tensioning and post – tensioning – Prestressing by straight, concentric, eccentric, bent and parabolic tendons – Different methods and systems of prestressing like Hoyer system, Freyssinet system, Magnel Blaton system – Lee-Mc call system. **Losses of Prestress :** Loss of prestress in pre-tensioned and post-tensioned members due to various causes like elastic shortening of concrete, shrinkage of concrete, creep of concrete, relaxation of steel, slip in anchorage, bending of member and frictional loss – Analysis of sections for flexure.

UNIT II.

Design of Section for Flexure : Allowable stresses – Elastic design of simple beams having rectangular and I-section for flexure – kern lines – cable profile and cable layout.

Design of Sections for Shear : Shear and Principal stresses – Improving shear resistance by different prestressing techniques – horizontal, sloping and vertical prestressing – Analysis of rectangular and I-beam – Design of shear reinforcement – Indian code provisions.

UNIT III.

Deflections of Prestressed Concrete Beams : Short term deflections of uncracked members– Prediction of long-time deflections – load – deflection curve for a PSC beam – IS code requirements for max. deflections.

UNIT IV

Transfer of Prestress in Pretensioned Members : Transmission of prestressing force by bond – Transmission length – Flexural bond stresses – IS code provisions – Anchorage zone stresses in post tensioned members – stress distribution in End block – Analysis by approximate, Guyon and Magnel methods – Anchorage zone reinforcement.

UNIT V.

Statically Indeterminate Structures : Advantages & disadvantages of continuous PSC beams – Primary and secondary moments – P and C lines – Linear transformation concordant and non-concordant cable profiles – Analysis of continuous beams and simple portal frames (single bay and single story)

REFERENCES :

1. Prestressed concrete by Krishna Raju, Tata Mc Graw Hill Book – Co ., New Delhi.
2. Design of prestress concrete structures by T.Y. Lin and Burn, John Wiley, New York.
3. Prestressed concrete by S. Ramamrutham Dhanpat Rai & Sons, Delhi.
4. IS 1343 -2012 Prestressed Concrete – Code of Practice, Bureau of Indian Standards.

Outcomes : The learner will be able to understand the prestressing techniques, design the various structural elements using Prestressing techniques.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

ADVANCED STEEL DESIGN
(Core Elective – III)

Objectives : To impart knowledge on behavior and design of various connections, industrial and steel girders.

UNIT-I:

Simple connections – Bolted, Pinned and Welded Connections: Bolted Connections- Load Transfer Mechanism – Failure of Bolted Joints – Specifications for Bolted Joints – Bearing – Type Connections – Tensile Strength of Plate – Strength and Efficiency of the Joint – Combined Shear and Tension – Slip – Critical Connections – Praying Action – Combined Shear and Tension for Slip- Critical Connections. Design of Groove welds- Design of Fillet Welds- Design of Intermittent fillet welds- Failure of Welds.

UNIT –II

Eccentric and Moment Connections: Introduction – Beams – Column Connections- Connections Subjected to Eccentric Shear – Bolted Framed Connections- Bolted Seat Connections – Bolted Bracket Connections. Bolted Moment Connections – Welded Framed Connections – Welded Bracket Connections - Moment Resistant Connections.

UNIT III

Analysis and Design of Industrial Buildings : Dead loads, live loads and wind loads on roofs. Design wind speed and pressure, wind pressure on roofs; wind effect on cladding and louvers; Design of angular roof truss, tubular truss, truss for a railway platform. Design of purlins for roofs, design of built up purlins, design of knee braced trusses and stanchions. Design of bracings.

UNIT IV :

Design of Steel Truss Girder Bridges : Types of truss bridges, component parts of a truss bridge, economic proportions of trusses, self weight of truss girders, design of bridge compression members, tension members; wind load on truss girder bridges; wind effect on top lateral bracing; bottom lateral bracing; portal Bracing; sway bracing.

UNIT V :

Design of Steel Bunkers and Silos : Introduction – Janseen's Theory – Airy's Theory – Design of Parameters – Design Criteria – Analysis of Bins – Hopper Bottom –Design of Bins.

REFERENCES:

1. Limit State Design of Steel Structures S.K. Duggal Mc Graw Hill Education Private Ltd. New Delhi.
2. Design of Steel Structures. P. Dayaratnam, Publisher : S. Chand, Edition 2011 – 12.
3. Design Steel Structures Volume – II, Dr. Ramachandra & Vivendra Gehlot Scientific Publishes Journals Department.
4. Design of Steel Structures Galyord & Gaylord, Publisher ; Tata Mc Graw Hill, Education. Edition 2012.
5. Indian Standard Code – IS – 800-2007 General Construction in Steel- Code of Practice.

Outcomes : The learner will be able to design different steel structures.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

SOIL DYNAMICS AND FOUNDATIONS ENGINEERING
(Core Elective-III)

Objective: To understand the wave propagation in soils, determine dynamic properties of soil for analyzing and designing foundations subjected to vibratory loading.

Unit-I

Fundamentals of Vibration: Definitions, Simple harmonic motion, Response of SDOF systems of Free and Forced vibrations with and without viscous damping, Frequency dependent excitation, Systems under transient loads, Rayleigh's method of fundamental frequency, Logarithmic decrement, Determination of viscous damping, Transmissibility, Systems with Two and Multiple degrees of freedom, Vibration measuring instruments.

Unit-II

Wave Propagation and Dynamic Soil Properties: Propagation of seismic waves in soil deposits - Attenuation of stress waves, Stress-strain behaviour of soils under cyclic loads, Strength of cyclically loaded soils, Dynamic soil properties - Laboratory and field testing techniques, Elastic constants of soils, Correlations for shear modulus and damping ratio in sand, gravels, clays and lightly cemented sand. Liquefaction of soils and its evaluation using simple methods.

Unit-III

Vibration Analyses: Types, General Requirements, Permissible amplitude, Allowable soil pressure, Modes of vibration of a rigid foundation block, Methods of analysis, Lumped Mass models, elastic half space method, elasto-dynamics, effect of footing shape on vibratory response, dynamic response of embedded block foundation, Vibration isolation.

Unit-IV

Design of Machine Foundations: Analysis and design of block foundations for reciprocating engines, Dynamic analysis and design procedure for a hammer foundation, IS code of practice design procedure for foundations of reciprocating and impact type machines. Vibration isolation and absorption techniques.

Unit-V

Machine Foundations on Piles: Introduction, Analysis of piles under vertical vibrations, Analysis of piles under translation and rocking, Analysis of piles under torsion, Design procedure for a pile supported machine foundation.

TEXT BOOKS:

1. Swami Saran - Soil Dynamics and Machine Foundation, Galgotia Publications Pvt.Ltd. (2010)
2. Prakash, S. - Soil Dynamics, McGraw Hill Book Company (1981)

REFERENCES:

1. Prakash, S. and Puri, V. K. - Foundation for Machines: Analysis and Design, John Wiley & Sons, 1998.
2. Kameswara Rao, N. S. V. - Vibration Analysis and Foundation Dynamics, Wheeler Publication Ltd., 1998.
3. Das, B. M. & Ramana, G.V. - Principles of Soil Dynamics, 2nd Edition, CL Engineering Publishers, 2010.

Outcomes: Able to understand the fundamentals of wave propagation in soil media, evaluate the dynamic properties of soil, and design foundations for centrifugal and reciprocating machines.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

STABILITY OF STRUCTURES
(Core Elective – III)

Objectives : To impart knowledge on the elastic, inelastic buckling and torsional buckling of structures.

UNIT – I

Beam Columns: Differential equations for beam columns- beam columns with concentrated loads – continuous lateral loads-couples- beam columns with built in ends – continuous beams with axial load – application of trigonometrically series – Effects of initial curvature on deflections – Determination of allowable stresses.

UNIT - II

Elastic Buckling of bars and frames: Elastic Buckling of straight columns – Effect of shear stress on buckling – Eccentrically and laterally loaded columns- Buckling of frames-large deflections of buckled bars-Energy methods- Buckling of bars on elastic foundations- Buckle line of bar with intermediate compressive forces - Buckling of bars with change in cross-section – Effect of shear force on critical load- built up columns.

UNIT - III

In Elastic Buckling: Buckle line of straight bar- Double modulus theory – Tangent modulus theory, Inelastic lateral Buckling. Experiments and design formulae: Experiments on columns – Critical stress diagram – Empirical formulae for design – various end conditions

UNIT - IV

Torsion Buckling: Pure torsion of thin walled bars of open cross section – Non-uniform torsion of thin walled bars of open cross section- Torsional buckling – Buckling by torsion and flexure.

UNIT – V

Lateral buckling of simply supported Beams: Beams of Rectangular cross-section subjected to pure bending. Buckling of simply supported Rectangular plates: Derivation of equation of plate subjected to constant compression in one and two directions.

REFERENCES

1. Theory of elastic Stability by Timshenko & Gere-Mc Graw Hill
2. Stability of metallic structures by Blunch- Mc Graw Hill
3. Theory of Beam- Columns Vol I by Chen. & Atste Mc. Graw Hill

Outcomes : The learner will be able to understand buckling of bars and frames.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

DESIGN OF SHELLS AND FOLDED PLATES
(Core Elective – IV)

Objectives : To impart knowledge on the behavior and design of shells and Folded plates.

UNIT I

Shells – functional behaviour – examples – structural behaviour of shells classification of shells – Definitions – various methods of analysis of shells – merits and demerits of each method – 2D. Membrane equation.
Equations of equilibrium: Derivation of stress resultants – cylindrical shells – Flugge's equations.

UNIT II

Derivation of the governing DKJ equation for bending theory, - Schorer's theory - Application to the analysis and design of short and long shells.
Beam theory of cylindrical shells: Beam and arch action, Analysis using beam theory.

UNIT III

Introduction to the shells of Double curvatures: Geometry, analysis and design of elliptic paraboloid, conoid and hyperbolic parabolic shapes, inverted umbrella type.

UNIT IV

Axi- Symmetrical shells: General equation - Analysis and axi-symmetrical by membrane theory. Application to spherical shells and hyperboloid of revolution cooling towers.

UNIT V

Folded plates – Introduction – Types of folded plates – structural behaviour of folded plates – advantages – Assumptions Whitney method of analysis – Edge shear equation - Analysis of folded plates of Whitney's method.
Simpson's method of Analysis of folded plates – moment and stress distribution – no rotation and rotation solutions – continuous folded plates – pre stressed continuous folded plates.

TEXT BOOKS:

1. Analysis and design of concrete shell roofs By G.S.Ramaswami.
2. Design of concrete shell roofs By Chatterjee.

REFERENCES:

- 1 Design of concrete shell roofs By Billington
- 2 Shell Analysis By N.K.Bairagi.
- 3 Advanced R.C Design By Dr.N.Krishna Raju.

Outcomes : The learner will be able to analyse and design the shells and folded plates.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

EARTHQUAKE RESISTANT DESIGN OF BUILDINGS
(Core Elective – IV)

Objectives : To impart knowledge on the seismology and behavior of buildings during earthquakes.

UNIT - I

Engineering Seismology: Earthquake phenomenon cause of earthquakes-Faults- Plate tectonics-Seismic waves- Terms associated with earthquakes-Magnitude/Intensity of an earthquake-scales-Energy released-Earthquake measuring instruments-Seismoscope, Seismograph, accelerograph-Characteristics of strong ground motions- Seismic zones of India.

UNIT - II

Conceptual design: Introduction-Functional planning-Continuous load path-Overall form-simplicity and symmetry-elongated shapes-stiffness and strength-Horizontal and Vertical members-Twisting of buildings-Ductility-definition-ductility relationships-flexible buildings-framing systems-choice of construction materials-unconfined concrete-confined concrete-masonry-reinforcing steel. Introduction to earthquake resistant design: Seismic design requirements-regular and irregular configurations-basic assumptions-design earthquake loads-basic load combinations-permissible stresses-seismic methods of analysis-factors in seismic analysis-equivalent lateral force method-dynamic analysis-response spectrum method-Time history method.

UNIT - III

Reinforced Concrete Buildings: Principles of earthquake resistant design of RC members- Structural models for frame buildings- Seismic methods of analysis- Seismic design methods- IS code based methods for seismic design- Seismic evaluation and retrofitting- Vertical irregularities- Plan configuration problems- Lateral load resisting systems- Determination of design lateral forces-Equivalent lateral force procedure- Lateral distribution of base shear. Masonry Buildings: Introduction-Elastic properties of masonry assemblage- Categories of masonry buildings- Behaviour of unreinforced and reinforced masonry walls- Behaviour of walls- Box action and bands- Behaviour of infill walls- Improving seismic behaviour of masonry buildings- Load combinations and permissible stresses- Seismic design requirements- Lateral load analysis of masonry buildings.

UNIT - IV

Structural Walls and Non-Structural Elements: Strategies in the location of structural walls- sectional shapes- variations in elevation- cantilever walls without openings – Failure mechanism of non-structures- Effects of non-structural elements on structural system- Analysis of non-structural elements- Prevention of non-structural damage- Isolation of non-structures.

UNIT - V

Ductility Considerations in Earthquake Resistant Design of RC Buildings: Introduction- Impact of Ductility- Requirements for Ductility- Assessment of Ductility- Factors affecting Ductility- Ductile detailing considerations as per IS 13920. Behaviour of beams, columns and joints in RC buildings during earthquakes-Vulnerability of open ground storey and short columns during earthquakes. Capacity Based Design: Introduction to Capacity Design, Capacity Design for Beams and Columns-Case studies.

REFERENCE BOOKS:

1. Earthquake Resistant Design of structures – S. K. Duggal, Oxford University Press
2. Earthquake Resistant Design of structures – Pankaj Agarwal and Manish Shrikhande, Prentice Hall of India Pvt. Ltd.
3. Seismic Design of Reinforced Concrete and Masonry Building – T. Paulay and M.J.N. Priestly, John Wiley & Sons
4. Masonry and Timber structures including earthquake Resistant Design –Anand S.Arya, Nem chand & Bros
5. Earthquake –Resistant Design of Masonry Building –Miha Tomazevic, Imperial college Press.
6. Earthquake Tips – Learning Earthquake Design and Construction C.V.R. Murty

**REFERENCE CODES:**

1. IS: 1893 (Part-1) -2002. "Criteria for Earthquake Resistant – Design of structures." B.I.S., New Delhi.
2. IS:4326-1993, " Earthquake Resistant Design and Construction of Building", Code of Practice B.I.S., New Delhi.
3. IS:13920-1993, " Ductile detailing of concrete structures subjected to seismic force" – Guidelines, B.I.S., New Delhi.

Outcomes : The learner will be able to analyse and design buildings to resist seismic forces.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

FRACTURE MECHANICS
(Core Elective – IV)

Objectives : To impart knowledge on the mechanisms of failure and non linear fracture mechanics.

UNIT I

Fundamentals of Fracture Mechanics, Mechanisms of fracture and crack growth

UNIT II

Cleavage fracture, ductile fracture, fatigue cracking, Environment assisted cracking, Quasi brittle materials.

UNIT III

Service failure analysis, linear elastic fracture mechanics, Griffith's criteria, stress intensity factors, crack tip plastic zone, Erwin's plastic zone correction, R curves, compliance, J Integral, nonlinear analysis, Review of concrete behaviour in tension and compression, Basic frameworks for modeling of quasibrittle materials.

UNIT IV

Nonlinear Fracture Mechanics – Discrete crack concept/Smeared crack concept, Size effect, Plasticity models for concrete – Associated and non-associated flow, Failure surfaces for quasibrittle materials.

UNIT V

Concept of CTOD and CMD, Material models, crack models, band models, models based on continuum damage mechanics

REFERENCES:

1. Elementary engineering fracture mechanics – David Broek – Sijthoff & Noordhoff -Alphen aan den Rijn – Netherlands
2. Fracture mechanics of concrete structures–Theory and applications – Rilem Report- Edited by L. Elfgreen – Chapman and Hall – 1989.
3. Fracture mechanics–applications to concrete–Edited by Victor, C. Li, & Z.P. Bazant–ACI SP 118.
4. Valliappan S. "Continuum Mechanics Fundamentals" (1982), Oxford IBH, N D. New Delhi.
5. Venkataraman and Patel "Structural Mechanics with introduction to Elasticity and Plasticity" – Mcgraw Hill, 1990.
6. Shanes – "Introduction to Solid Mechanics – II Edition, PH, 1989.

Outcomes : The learner will be able to understand the behavior of concrete with tension and compression failure surfaces and concepts of CTOD and CMD.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

REPAIR & REHABILITATION OF BUILDINGS
(Open Elective – II)

Objectives : To impart knowledge on the distress in structures.

UNIT – I

Introduction – Deterioration of Structures – Distress in Structures – Causes and Prevention. Mechanism of Damage – Types of Damage.

UNIT – II

Corrosion of Steel Reinforcement – Causes – Mechanism and Prevention. Damage of Structures due to Fire – Fire Rating of Structures – Phenomena of Desiccation.

UNIT – III

Inspection and Testing – Symptoms and Diagnosis of Distress - Damage assessment – NDT.

UNIT – IV

Repair of Structure – Common Types of Repairs – Repair in Concrete Structures – Repairs in Under Water Structures – Guniting – Shot Create – Underpinning. Strengthening of Structures – Strengthening Methods – Retrofitting – Jacketing.

UNIT – V

Health Monitoring of Structures – Use of Sensors – Building Instrumentation.

REFERENCES

1. **Concrete Technology** by A.R. Santakumar, Oxford University press
2. **Defects and Deterioration in Buildings**, E F & N Spon, London
3. **Non-Destructive Evaluation of Concrete Structures** by Bungey - Surrey University Press
4. **Maintenance , Repair & Rehabilitation and Minor Works of Buildings** by P.C.Varghese, PHI.
5. **Maintenance and Repair of Civil Structures**, B.L. Gupta and Amit Gupta, Standard Publications.
6. **Concrete Repair and Maintenance Illustrated**, RS Means Company Inc W. H. Ranso, (1981)
7. **Building Failures : Diagnosis and Avoidance**, EF & N Spon, London, B. A. Richardson, (1991).

Outcomes : The learner will be able to understand the reasons for distress in structures and will be able to suggest suitable solutions.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

COMPOSITE MATERIALS
(Open Elective – II)

Objectives : To impart knowledge on the properties of composite materials, their uses and advantages.

UNIT - I

Introduction: Requirements of structural materials, influence of nature of materials in structural form, Nature of structural materials- Homogeneous materials, composite materials.

UNIT - II

Macro mechanical Properties of composite Laminae: Introduction, Assumptions and Idealizations, Stress Strain relationships for composite Laminae- Isotropic, Orthotropic laminae, Strength Characteristics- Basic concepts, Strength hypothesis for isotropic and Orthotropic laminae. Macro mechanical Analysis of composite Laminae: Introduction, Assumptions and Limitations, Stiffness characteristics of glass reinforced laminae- Stress- Strain relationships in continuous, discontinuous fibre laminae, Strength characteristics of glass reinforced laminae- Strengths in continuous, discontinuous fibre laminae.

UNIT - III

Behaviour of Glass Fibre-Reinforced laminates: Introduction, Stiffness characteristics of Laminated composites-Behaviour of Laminated beams and plates, Strength characteristics of Laminated composites- Strength analysis and failure criteria, Effect of inter laminar structures. Glass Reinforced Composites: Introduction, Continuously reinforced laminates- uni-directionally and multi directionally continuously reinforced laminates, Discontinuously reinforced laminates – Stiffness and Strength properties.

UNIT - IV

GRP properties relevant to structural Design: Introduction, Short-term strength and stiffness-Tensile, Compressive, Flexural and Shearing. Long term strength and stiffness properties, Temperature effects, Effect of fire, Structural joints- Adhesive, mechanical, Combinational, Transformed sections.

UNIT - V

Design of GRP Box Beams: Introduction, loading, span and cross-sectional shape, Selection of material, Beam manufacture, Beam stresses, Experimental Behaviour, Effect on Beam performance- Modulus of Elasticity, Compressive Strength, I value, prevention of compression buckling failure, Behaviour under long term loading.

Design of Stressed skinned roof structure: Introduction, loading and material properties, preliminary design, and computer analysis.

REFERENCE:

1. GRP in Structural Engineering M.Holmes and D.J.Just.
2. Mechanics of Composite materials and Structures by Manjunath Mukhopadhyay; Universities Press

Outcomes : The learner will be able to understand use of different composite materials and design GRP Box beams.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

OPTIMIZATION TECHNIQUES
(Open Elective -II)

Objective: To understand the theory of optimization methods and algorithms developed for solving various types of optimization problems

Unit-I

Linear Programming: Introduction and need for optimization in engineering design, formulating linear programs, graphical solution of linear programs, special cases of linear programming.

Unit-II

The Simplex Method: Converting a problem to standard form, the theory of the simplex method, the simplex algorithm, special situations in the simplex algorithm, obtaining initial feasible solution.

Unit-III

Duality and Sensitivity Analysis: Sensitivity analysis, shadow prices, dual of a normal linear program, duality theorems, dual simplex method. Integer Programming: Formulating integer programming problems, the branch-and-bound algorithm for pure integer programs, the branch-and-bound algorithm for mixed integer programs.

Unit-IV

Non-linear Programming: Introduction to non-linear programming (NLP), Convex and concave functions, NLP with one variable, Line search algorithms, Multivariable unconstrained problems, constrained problems, Lagrange Multiplier, The Karush-Kuhn-Tucker (KKT) conditions, the method of steepest ascent, convex combination method, penalty function, Quadratic programming,

Unit-V

Dynamic programming: Evolutionary algorithms: Genetic Algorithm, concepts of multiobjective optimization, Markov Process, Queuing Models.

TEXT BOOK

1. S.S. Rao, Engineering Optimization: Theory and Practice, Wiley & Sons, New Jersey, 2009.

REFERENCES:

1. F.H. Hiller and G.J. Liberman, Introduction to Operations Research, Tata-McGraw-Hill, 2010.
2. Introduction to Optimum Design by J.S. Arora,
3. W.L. Winston, Operations Research: Applications and Algorithm, 4th Edition, Cengage Learning, 1994.
4. K. Deb, Optimization for Engineering Design, Prentice Hall, 2013.
5. M.C. Joshi and K.M. Moudgalay, Optimization: Theory and Practice, Narosa, 2004.
6. K. Deb, Multi-objective Optimization using evolutionary algorithms, John Wiley and Sons, 2009.

Outcome: The student will be able to understand the basic principles of optimization, and in a position to formulate optimization models for a wide range of civil engineering problems and able to solve them.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (Structural Engg.)

CAD LAB

Objectives : To impart knowledge on the use of various softwares

1. Program using arrays and functions for matrix manipulation.
2. Programs to draw bending moment and shear force diagrams. Using graphic in C
3. Program for design of slabs. Using Excel
4. Program for design of beams. Using Excel
5. Program for design of column and footing using excel
6. Analysis of truss using STAAD Pro.
7. Analysis of multistoreyed space frame, using STAAD Pro.
8. Analysis of Bridge deck slab.

Outcomes : The learner will be able to understand and design the structures us

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.TECH. (COMPUTER SCIENCE AND ENGINEERING)
COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Data Structures and Algorithms	25	75	4	--	4
Core Course II	Database Internals	25	75	4	--	4
Core Course III	Distributed Systems	25	75	4	--	4
Core Elective I	1. Network Security 2. Android Application Development 3. Cloud Computing 4. Internet of Things	25	75	4	--	4
Core Elective II	1. Machine Learning 2. Parallel and Distributed Algorithms 3. Software Architecture and Design Patterns 4. Embedded Systems	25	75	4	--	4
Open Elective I	Open Elective – 1	25	75	4	--	4
Laboratory I	Data Structures and Algorithms Lab	25	75	--	4	2
Seminar I	Seminar	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Network Programming	25	75	4	--	4
Core Course V	Information Retrieval Systems	25	75	4	--	4
Core Course VI	Internet Technologies and Services	25	75	4	--	4
Core Elective III	Core Elective– 3 1. Data Mining 2. Storage Area Networks 3. Semantic Web and Social Networks 4. Cyber Security	25	75	4	--	4
Core Elective IV	Core Elective– 4 1. Big Data Analytics 2. Soft Computing 3. Software Process and Project Management 4. Distributed Computing	25	75	4	--	4
Open Elective II	Open Elective – 2	25	75	4	--	4
Laboratory II	Internet Technologies and Services Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits			24	8	28	

II Year - I Semester

	Course Title	Int. marks	Ext. marks	L	P	C
	Comprehensive Viva-Voce	--	100	--	--	4
	Project work Review I	50	--	--	24	12
Total Credits				--	24	16

II Year - II Semester

	Course Title	Int. marks	Ext. marks	L	P	C
	Project work Review II	50	--	--	8	4
	Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits				--	24	16

Open Electives

1. Basic Computer Programming skills are required for all open electives. Additionally, knowledge on the specified area mentioned in prerequisites is required for opting the open elective
2. Note: A student can register for any open elective subject provided that he has not already registered for the same subject

S.NO	Open Electives	Prerequisites
1.	"R" Programming	Maths, Statistics
2.	Android Application Development	Java
3.	Algorithmics	----
4.	Big Data Analytics	Data Bases , Maths
5.	Bioinformatics	Data Structures
6.	Biometrics	----
7.	Cyber Security	Internet Technologies
8.	Computer Forensics	Maths, Data Structures
9.	Distributed Systems Security	Information Security
10.	E-Commerce	Internet Technologies
11.	Embedded Systems	Digital logic
12.	Information Security	Maths
13.	Intellectual Property Rights	---
14.	Internet of Things	Java
15.	Java Programming	---
16.	Linux Programming	---
17.	Mobile Computing	Java
18.	Mobile Application Security	Mobile Application Development
19.	OpenStack cloud computing	Linux Programming
20.	Operations Research	Maths, Data Structures
21.	Principles of Information Security	-----
22.	Scripting Languages	---
23.	Social Media Intelligence	---
24.	Software Engineering	---
25.	Storage Area Networks	Computer Networks
26.	Web Usability	-----

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

DATA STRUCTURES AND ALGORITHMS

Objectives:

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.
- Significance of algorithms in the computer field
- Various aspects of algorithm development
- Qualities of a good solution

UNIT I

Algorithms, Performance analysis- time complexity and space complexity, Asymptotic Notation-Big Oh, Omega and Theta notations, Complexity Analysis Examples.

Data structures-Linear and non linear data structures, ADT concept, Linear List ADT, Array representation, Linked representation, Vector representation, singly linked lists -insertion, deletion, search operations, doubly linked lists-insertion, deletion operations, circular lists. Representation of single, two dimensional arrays, Sparse matrices and their representation.

UNIT II

Stack and Queue ADTs, array and linked list representations, infix to postfix conversion using stack, implementation of recursion, Circular queue-insertion and deletion, Dequeue ADT, array and linked list representations, Priority queue ADT, implementation using Heaps, Insertion into a Max Heap, Deletion from a Max Heap, java.util package-ArrayList, Linked List, Vector classes, Stacks and Queues in java.util, Iterators in java.util.

UNIT III

Searching-Linear and binary search methods, Hashing-Hash functions, Collision Resolution methods-Open Addressing, Chaining, Hashing in java.util-HashMap, HashSet, Hashtable.

Sorting –Bubble sort, Insertion sort, Quick sort, Merge sort, Heap sort, Radix sort, comparison of sorting methods.

UNIT IV

Trees- Ordinary and Binary trees terminology, Properties of Binary trees, Binary tree ADT, representations, recursive and non recursive traversals, Java code for traversals, Threaded binary trees.

Graphs- Graphs terminology, Graph ADT, representations, graph traversals/search methods-dfs and bfs, Java code for graph traversals, Applications of Graphs-Minimum cost spanning tree using Kruskal's algorithm, Dijkstra's algorithm for Single Source Shortest Path Problem.

UNIT V

Search trees- Binary search tree-Binary search tree ADT, insertion, deletion and searching operations, Balanced search trees, AVL trees-Definition and examples only, Red Black trees –Definition and examples only, B-Trees-definition, insertion and searching operations, Trees in java.util- TreeSet, Tree Map Classes, Tries(examples only),Comparison of Search trees.

Text compression-Huffman coding and decoding, Pattern matching-KMP algorithm.

TEXT BOOKS:

1. Data structures, Algorithms and Applications in Java, S.Sahni, Universities Press.
2. Data structures and Algorithms in Java, Adam Drozdek, 3rd edition, Cengage Learning.
3. Data structures and Algorithm Analysis in Java, M.A.Weiss, 2nd edition, Addison-Wesley (Pearson Education).

REFERENCE BOOKS:

1. Java for Programmers, Deitel and Deitel, Pearson education.
2. Data structures and Algorithms in Java, R.Lafore, Pearson education.
3. Java: The Complete Reference, 8th edition, Herbert Schildt, TMH.
4. Data structures and Algorithms in Java, M.T.Goodrich, R.Tomassia, 3rd edition, Wiley India Edition.
5. Data structures and the Java Collection Framework, W.J.Collins, Mc Graw Hill.
6. Classic Data structures in Java, T.Budd, Addison-Wesley (Pearson Education).
7. Data structures with Java, Ford and Topp, Pearson Education.
8. Data structures using Java, D.S.Malik and P.S.Nair, Cengage learning.
9. Data structures with Java, J.R.Hubbard and A.Huray, PHI Pvt. Ltd.
10. Data structures and Software Development in an Object-Oriented Domain, J.P.Tremblay and G.A.Cheston, Java edition, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

DATABASE INTERNALS

Objectives:

By the end of the course, you will know:

- History and Structure of databases
- How to design a database
- How to convert the design into the appropriate tables
- Handling Keys appropriately
- Enforcing Integrity Constraints to keep the database consistent
- Normalizing the tables to eliminate redundancies
- Querying relational data
- and processing the queries
- Storage Optimizing Strategies for easy retrieval of data through index
- Triggers, Procedures and Cursors, Transaction Management
- Distributed databases management system concepts and Implementation

UNIT I

Database System Applications, Purpose of Database Systems, View of Data – Data Abstraction, Instances and Schemas, Data Models – the ER Model, Relational Model, Other Models – Database Languages – DDL,DML, Database Access from Applications Programs, Transaction Management, Data Storage and Querying, Database Architecture, Database Users and Administrators, ER diagrams,. Relational Model: Introduction to the Relational Model – Integrity Constraints Over Relations, Enforcing Integrity constraints, Querying relational data, Logical data base Design, Introduction to Views –Altering Tables and Views, Relational Algebra, Basic SQL Queries, Nested Queries, Complex Integrity Constraints in SQL, Triggers

UNIT II

Introduction to Schema Refinement – Problems Caused by redundancy, Decompositions – Problem related to decomposition, Functional Dependencies - Reasoning about FDS, Normal Forms – FIRST, SECOND, THIRD Normal forms – BCNF –Properties of Decompositions- Loss less- join Decomposition, Dependency preserving Decomposition, Schema Refinement in Data base Design – Multi valued Dependencies – FOURTH Normal Form, Join Dependencies, FIFTH Normal form.

UNIT III

Transaction Management: The ACID Properties, Transactions and Schedules, Concurrent Execution of Transactions – Lock Based Concurrency Control, Deadlocks – Performance of Locking – Transaction Support in SQL.

Concurrency Control: Serializability, and recoverability – Introduction to Lock Management – Lock Conversions, Dealing with Deadlocks, Specialized Locking Techniques – Concurrency Control without Locking.

Crash recovery: Introduction to Crash recovery, Introduction to ARIES, the Log, and Other Recovery related Structures, the Write-Ahead Log Protocol, Check pointing, recovering from a System Crash, Media recovery

UNIT IV

Overview of Storage and Indexing: Data on External Storage, File Organization and Indexing – Clustered Indexes, Primary and Secondary Indexes, Index data Structures – Hash Based Indexing, Tree based Indexing

Storing data: Disks and Files: -The Memory Hierarchy – Redundant Arrays of Independent Disks.

Tree Structured Indexing: Intuitions for tree Indexes, Indexed Sequential Access Methods (ISAM)

B+ Trees: A Dynamic Index Structure, Search, Insert, Delete.

Hash Based Indexing: Static Hashing, Extendable hashing, Linear Hashing, Extendable Vs Linear Hashing.

UNIT V

Distributed databases: Introduction to distributed databases, Distributed DBMS architectures, Storing data in a distributed DBMS, Distributed catalog management, Distributed query processing Updating distributed data, Distributed transactions, Distributed concurrency control, Distributed recovery

TEXT BOOKS:

1. Data base Management Systems, Raghu Ramakrishnan, Johannes Gehrke, TMH, 3rd Edition, 2003.
2. Data base System Concepts, A.Silberschatz, H.F. Korth, S.Sudarshan, McGraw hill, VI edition, 2006.

3. Fundamentals of Database Systems 5th edition, Ramez Elmasri, Shamkant B.Navathe, Pearson Education, 2008.

REFERENCE BOOKS:

1. Introduction to Database Systems, C.J.Date, Pearson Education.
2. Database Management System Oracle SQL and PL/SQL, P.K.Das Gupta, PHI.
3. Database System Concepts, Peter Rob & Carlos Coronel, Cengage Learning, 2008.
4. Database Systems, A Practical approach to Design Implementation and Management Fourth edition, Thomas Connolly, Carolyn Begg, Pearson education.
5. Database-Principles, Programming, and Performance, P.O'Neil & E.O'Neil, 2nd ed, ELSEVIER
6. Fundamentals of Relational Database Management Systems, S.Sumathi, S.Esakkirajan, Springer.
7. Introduction to Database Management, M.L.Gillenson and others, Wiley Student Edition.
8. Database Development and Management, Lee Chao, Auerbach publications, Taylor & Francis Group.
9. Distributed Databases Principles & Systems, Stefano Ceri, Giuseppe Pelagatti, TMH.
10. Principles of Distributed Database Systems, M. Tamer Ozsu, Patrick Valduriez, Pearson Education, 2nd Edition.
11. Distributed Database Systems, Chhanda Ray, Pearson.

University Updates

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

DISTRIBUTED SYSTEMS

Objectives:

- Understand the need for distributed systems and their applications.
- Understand the concepts of remote procedure calls, remote file systems, distributed agreement, clock synchronization, and security.

UNIT I

Characterization of Distributed Systems-Introduction, Examples of Distributed systems, Resource sharing and web, challenges, System models-Introduction, Architectural and Fundamental models, Networking and Internetworking, Interprocess Communication.

Distributed objects and Remote Invocation-Introduction, Communication between distributed objects, RPC, Events and notifications, Case study-Java RMI.

UNIT II

Operating System Support- Introduction, OS layer, Protection, Processes and Threads, Communication and Invocation, Operating system architecture, Distributed File Systems-Introduction, File Service architecture, case study- SUN network file systems.

Name Services-Introduction, Name Services and the Domain Name System, Case study of the Global Name Service, Case study of the X.500 Directory Service.

UNIT III

Peer to Peer Systems-Introduction, Napster and its legacy, Peer to Peer middleware, Routing overlays, Overlay case studies-Pastry, Tapestry, Application case studies-Squirrel, OceanStore, Time and Global States-Introduction, Clocks, events and Process states, Synchronizing physical clocks, logical time and logical clocks, global states, distributed debugging.

Coordination and Agreement-Introduction, Distributed mutual exclusion, Elections, Multicast communication, consensus and related problems.

UNIT IV

Transactions and Concurrency control-Introduction, Transactions, Nested Transactions, Locks, Optimistic concurrency control, Timestamp ordering, Comparison of methods for concurrency control. Distributed Transactions-Introduction, Flat and Nested Distributed Transactions, Atomic commit protocols, Concurrency control in distributed transactions, Distributed deadlocks, Transaction recovery. Replication-Introduction, System model and group communication, Fault tolerant services, Transactions with replicated data.

UNIT V

Security-Introduction, Overview of Security techniques, Cryptographic algorithms, Digital signatures, Case studies-Kerberos, TLS, 802.11 Wi-Fi.

Distributed shared memory, Design and Implementation issues, Sequential consistency and Ivy case study, Release consistency and Munin case study, Other consistency models, CORBA case study-Introduction, CORBA RMI, CORBA Services.

TEXT BOOKS:

1. Distributed Systems Concepts and Design, G Coulouris, J Dollimore and T Kindberg, Fourth Edition, Pearson Education.
2. Distributed Systems, S.Ghosh, Chapman & Hall/CRC, Taylor & Francis Group, 2010.

REFERENCE BOOKS:

1. Distributed Computing, S.Mahajan and S.Shah, Oxford University Press.
2. Distributed Operating Systems Concepts and Design, Pradeep K.Sinha, PHI.
3. Advanced Concepts in Operating Systems, M Singhal, N G Shivarathri, TMH.
4. Reliable Distributed Systems, K.P.Birman, Springer.
5. Distributed Systems – Principles and Paradigms, A.S. Tanenbaum and M.V. Steen, Pearson Education.
6. Distributed Operating Systems and Algorithm Analysis, R.Chow, T.Johnson, Pearson.
7. Distributed Operating Systems, A.S.Tanenbaum, Pearson education.
8. Distributed Computing, Principles, Algorithms and Systems, Ajay D.Kshemakalyani and Mukesh Singhal, Cambridge, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

NETWORK SECURITY (CORE ELECTIVE-I)

Objectives:

- Understand the basic categories of threats to computers and networks
- Understand various cryptographic algorithms.
- Describe public-key cryptosystem.
- Describe the enhancements made to IPv4 by IPSec
- Understand Intrusions and intrusion detection
- Discuss the fundamental ideas of public-key cryptography.
- Generate and distribute a PGP key pair and use the PGP package to send an encrypted e-mail message.
- Discuss Web security and Firewalls

UNIT – I

Attacks on Computers and Computer Security: Introduction, The need for security, Security approaches, Principles of security, Types of Security attacks, Security services, Security Mechanisms, A model for Network Security **Cryptography: Concepts and Techniques:** Introduction, plain text and cipher text, substitution techniques, transposition techniques, encryption and decryption, symmetric and asymmetric key cryptography, steganography, key range and key size, possible types of attacks.

UNIT – II

Symmetric key Ciphers: Block Cipher principles & Algorithms(DES, AES, Blowfish), Differential and Linear Cryptanalysis, Block cipher modes of operation, Stream ciphers, RC4, Location and placement of encryption function, Key distribution **Asymmetric key Ciphers:** Principles of public key cryptosystems, Algorithms(RSA, Diffie-Hellman, ECC), Key Distribution

UNIT – III

Message Authentication Algorithms and Hash Functions: Authentication requirements, Functions, Message authentication codes, Hash Functions, Secure hash algorithm, Whirlpool, HMAC, CMAC, Digital signatures, knapsack algorithm **Authentication Applications:** Kerberos, X.509 Authentication Service, Public – Key Infrastructure, Biometric Authentication

UNIT – IV

E-Mail Security: Pretty Good Privacy, S/MIME **IP Security:** IP Security overview, IP Security architecture, Authentication Header, Encapsulating security payload, combining security associations, key management

UNIT – V

Web Security: Web security considerations, Secure Socket Layer and Transport Layer Security, Secure electronic transaction **Intruders, Virus and Firewalls:** Intruders, Intrusion detection, password management, Virus and related threats, Countermeasures, Firewall design principles, Types of firewalls **Case Studies on Cryptography and security:** Secure Inter-branch Payment Transactions, Cross site Scripting Vulnerability, Virtual Elections.

TEXT BOOKS:

1. Cryptography and Network Security : William Stallings, Pearson Education, 5th Edition
2. Cryptography and Network Security: Atul Kahate, Mc Graw Hill, 2nd Edition.
3. Network Security and Cryptography: Bernard Menezes, CENGAGE Learning

REFERENCE BOOKS:

1. Cryptography and Network Security: C K Shyamala, N Harini, Dr T R Padmanabhan, Wiley India, 1st Edition.
2. Cryptography and Network Security : Forouzan Mukhopadhyay, Mc Graw Hill, 2nd Edition
3. Information Security, Principles and Practice : Mark Stamp, Wiley India.
4. Principles of Computer Security: WM.Arthur Conklin, Greg White, TMH
5. Introduction to Network Security: Neal Krawetz, CENGAGE Learning.
6. Principles of Information security by Michael E Whitman and Herbert J.Mattord.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

ANDROID APPLICATION DEVELOPMENT (CORE ELECTIVE-I)

Objectives:

- To demonstrate their understanding of the fundamentals of Android operating systems
- To demonstrate their skills of using Android software development tools
- To demonstrate their ability to develop software with reasonable complexity on mobile platform
- To demonstrate their ability to deploy software to mobile devices
- To demonstrate their ability to debug programs running on mobile devices

Unit I:

Introduction to Android Operating System: Android OS design and Features – Android development framework, SDK features, Installing and running applications on Eclipse platform, Creating AVDs, Types of Android applications, Best practices in Android programming, Android tools
Android application components – Android Manifest file, Externalizing resources like values, themes, layouts, Menus etc, Resources for different devices and languages, Runtime Configuration Changes
Android Application Lifecycle – Activities, Activity lifecycle, activity states, monitoring state changes

Unit II:

Android User Interface: Measurements – Device and pixel density independent measuring units
Layouts – Linear, Relative, Grid and Table Layouts
User Interface (UI) Components – Editable and non editable TextViews, Buttons, Radio and Toggle Buttons, Checkboxes, Spinners, Dialog and pickers
Event Handling – Handling clicks or changes of various UI components
Fragments – Creating fragments, Lifecycle of fragments, Fragment states, Adding fragments to Activity, adding, removing and replacing fragments with fragment transactions, interfacing between fragments and Activities, Multi-screen Activities

Unit III

Intents and Broadcasts: Intent – Using intents to launch Activities, Explicitly starting new Activity, Implicit Intents, Passing data to Intents, Getting results from Activities, Native Actions, using Intent to dial a number or to send SMS
Broadcast Receivers – Using Intent filters to service implicit Intents, Resolving Intent filters, finding and using Intents received within an Activity
Notifications – Creating and Displaying notifications, Displaying Toasts

Unit IV

Persistent Storage: Files – Using application specific folders and files, creating files, reading data from files, listing contents of a directory
Shared Preferences – Creating shared preferences, saving and retrieving data using Shared Preference
Database – Introduction to SQLite database, creating and opening a database, creating tables, inserting retrieving and deleting data, Registering Content Providers, Using content Providers (insert, delete, retrieve and update)

Unit V

Advanced Topics: Alarms – Creating and using alarms
Using Internet Resources – Connecting to internet resource, using download manager
Location Based Services – Finding Current Location and showing location on the Map, updating location

TEXT BOOKS:

1. Professional Android 4 Application Development, Reto Meier, Wiley India, (Wrox) , 2012
2. Android Application Development for Java Programmers, James C Sheusi, Cengage Learning, 2013

REFERENCE:

1. Beginning Android 4 Application Development, Wei-Meng Lee, Wiley India (Wrox), 2013

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

CLOUD COMPUTING (CORE ELECTIVE-I)

Objectives:

- To learn the new computing model which enables shared resources on demand over the network.
- To learn about the pay-per-use scenarios.
- To learn about the new kind of service models and deployment models.
- To learn about the virtualization technology.
- To learn the python programming or various services and models.
- To develop cloud applications in Python

UNIT-I

Principles of Parallel and Distributed Computing, Introduction to cloud computing, Cloud computing Architecture, cloud concepts and technologies, cloud services and platforms, Cloud models, cloud as a service, cloud solutions, cloud offerings, introduction to Hadoop and Mapreduce.

UNIT –II

Cloud Platforms for Industry, Healthcare and education, Cloud Platforms in the Industry, cloud applications. Virtualization, cloud virtualization technology, deep dive: cloud virtualization, Migrating in to cloud computing, Virtual Machines Provisioning and Virtual Machine Migration Services, On the Management of Virtual Machines for cloud Infrastructure, Comet cloud, T-Systems,

UNIT-III

Cloud computing Applications: Industry, Health, Education, Scientific Applications, Business and Consumer Applications, Understanding Scientific Applications for Cloud Environments, Impact of Cloud computing on the role of corporate IT. Enterprise cloud computing Paradigm, Federated cloud computing Architecture, SLA Management in Cloud Computing, Developing the cloud: cloud application Design.

UNIT-IV

Python Basics, Python for cloud, cloud application development in python, Cloud Application Development in Python. Programming Google App Engine with Python: A first real cloud Application, Managing Data in the cloud, Google app engine Services for Login Authentication, Optimizing UI and Logic, Making the UI Pretty: Templates and CSS, Getting Interactive. Map Reduce Programming Model and Implementations.

UNIT-V

Cloud management, Organizational Readiness and change management in the cloud age ,Cloud Security ,Data security in the cloud, Legal Issues in the Cloud , Achieving Production Readiness for the cloud Services

TEXT BOOKS:

2. Cloud Computing: Raj Kumar Buyya , James Broberg, andrzej Goscinski, 2013 Wiley
3. Mastering Cloud Computing: Raj Kumar buyya, Christian Vecchiola,selvi-2013.
4. Cloud Computing: Arshdeep Bahga, Vijay Madiseti, 2014, University Press.
5. Cloud computing: Dr Kumar Saurab Wiley India 2011.

REFERENCES;

1. Code in the Cloud: Mark C.Chu-Carroll 2011, SPD.(Second part of IV UNIT)
2. Essentials of cloud computing : K Chandrasekharan CRC Press.
3. Cloud Computing: John W. Rittinghouse, James Ransome, CRC Press.
4. Virtualization Security: Dave shackleford 2013. SYBEX a wiley Brand.
5. Cloud computing and Software Services: Ahson , Ilyas.2011.
6. Cloud Computing Bible: Sosinsky 2012. Wiley India .
7. Cloud Computing: Dan C. Marinescu-2013, Morgan Kaufmann.
8. Distributed and Cloud Computing, Kai Hwang, Geoffery C.Fox, Jack J.Dongarra, Elsevier, 2012.
- 9 . Fundamentals of Python Kenneth A.Lambert | B.L.Juneja

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

INTERNET OF THINGS (CORE ELECTIVE-I)

Objectives:

To introduce the terminology, technology and its applications
To introduce the concept of M2M (machine to machine) with necessary protocols
To introduce the Python Scripting Language which is used in many IoT devices
To introduce the Raspberry PI platform, that is widely used in IoT applications
To introduce the implementation of web based services on IoT devices

Unit I

Introduction to Internet of Things –Definition and Characteristics of IoT,
Physical Design of IoT – IoT Protocols, IoT communication models, IoT Communication APIs
IoT enabled Technologies – Wireless Sensor Networks, Cloud Computing, Big data analytics,
Communication protocols, Embedded Systems, IoT Levels and Templates
Domain Specific IoTs – Home, City, Environment, Energy, Retail, Logistics, Agriculture, Industry, health and Lifestyle

Unit II

IoT and M2M – Software defined networks, network function virtualization, difference between SDN and NFV for IoT
Basics of IoT System Management with NETCOZF, YANG- NETCONF, YANG, SNMP NETOPEER

Unit III

Introduction to Python - Language features of Python, Data types, data structures, Control of flow, functions, modules, packaging, file handling, data/time operations, classes, Exception handling
Python packages - JSON, XML, HTTPLib, URLLib, SMTPLib

Unit IV

IoT Physical Devices and Endpoints - Introduction to Raspberry PI-Interfaces (serial, SPI, I2C)
Programming – Python program with Raspberry PI with focus of interfacing external gadgets, controlling output, reading input from pins.

Unit V

IoT Physical Servers and Cloud Offerings – Introduction to Cloud Storage models and communication APIs
Webserver – Web server for IoT, Cloud for IoT, Python web application framework
Designing a RESTful web API

TEXT BOOK:

Internet of Things - A Hands-on Approach, Arshdeep Bahga and Vijay Madisetti, Universities Press, 2015, ISBN: 9788173719547
Getting Started with Raspberry Pi, Matt Richardson & Shawn Wallace, O'Reilly (SPD), 2014, ISBN: 9789350239759

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

MACHINE LEARNING (CORE ELECTIVE-II)

Objectives:

- To be able to formulate machine learning problems corresponding to different applications.
- To understand a range of machine learning algorithms along with their strengths and weaknesses.
- To understand the basic theory underlying machine learning.
- To be able to apply machine learning algorithms to solve problems of moderate complexity.
- To be able to read current research papers and understands the issues raised by current research.

UNIT I

INTRODUCTION - Well-posed learning problems, Designing a learning system, Perspectives and issues in machine learning

Concept learning and the general to specific ordering – Introduction, A concept learning task, Concept learning as search, Find-S: finding a maximally specific hypothesis, Version spaces and the candidate elimination algorithm, Remarks on version spaces and candidate elimination, Inductive bias

UNIT II

Decision Tree learning – Introduction, Decision tree representation, Appropriate problems for decision tree learning, The basic decision tree learning algorithm, Hypothesis space search in decision tree learning, Inductive bias in decision tree learning, Issues in decision tree learning

Artificial Neural Networks – Introduction, Neural network representation, Appropriate problems for neural network learning, Perceptions, Multilayer networks and the back propagation algorithm, Remarks on the back propagation algorithm, An illustrative example face recognition
Advanced topics in artificial neural networks

Evaluation Hypotheses – Motivation, Estimation hypothesis accuracy, Basics of sampling theory, A general approach for deriving confidence intervals, Difference in error of two hypotheses, Comparing learning algorithms

UNIT III

Bayesian learning – Introduction, Bayes theorem, Bayes theorem and concept learning, Maximum likelihood and least squared error hypotheses, Maximum likelihood hypotheses for predicting probabilities, Minimum description length principle, Bayes optimal classifier, Gibbs algorithm, Naïve Bayes classifier, An example learning to classify text, Bayesian belief networks The EM algorithm

Computational learning theory – Introduction, Probability learning an approximately correct hypothesis, Sample complexity for Finite Hypothesis Space, Sample Complexity for infinite Hypothesis Spaces, The mistake bound model of learning - **Instance-Based Learning**- Introduction, k -Nearest Neighbour Learning, Locally Weighted Regression, Radial Basis Functions, Case-Based Reasoning, Remarks on Lazy and Eager Learning

Genetic Algorithms – Motivation, Genetic Algorithms, An illustrative Example, Hypothesis Space Search, Genetic Programming, Models of Evolution and Learning, Parallelizing Genetic Algorithms

UNIT IV

Learning Sets of Rules – Introduction, Sequential Covering Algorithms, Learning Rule Sets: Summary, Learning First Order Rules, Learning Sets of First Order Rules: FOIL, Induction as Inverted Deduction, Inverting Resolution

Analytical Learning - Introduction, Learning with Perfect Domain Theories: Prolog-EBG Remarks on Explanation-Based Learning, Explanation-Based Learning of Search Control Knowledge

UNIT V

Combining Inductive and Analytical Learning – Motivation, Inductive-Analytical Approaches to Learning, Using Prior Knowledge to Initialize the Hypothesis, Using Prior Knowledge to Alter the Search Objective, Using Prior Knowledge to Augment Search Operators,

Reinforcement Learning – Introduction, The Learning Task, Q Learning, Non-Deterministic, Rewards and Actions, Temporal Difference Learning, Generalizing from Examples, Relationship to Dynamic Programming

TEXT BOOKS:

1. Machine Learning – Tom M. Mitchell, - MGH
2. Machine Learning: An Algorithmic Perspective, Stephen Marsland, Taylor & Francis (CRC)

REFERENCE BOOKS:

1. Machine Learning Methods in the Environmental Sciences, Neural Networks, William W Hsieh, Cambridge Univ Press.
2. Richard o. Duda, Peter E. Hart and David G. Stork, pattern classification, John Wiley & Sons Inc., 2001
3. Chris Bishop, Neural Networks for Pattern Recognition, Oxford University Press, 1995
4. Machine Learning by Peter Flach , Cambridge.

University Updates

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

**PARALLEL AND DISTRIBUTED ALGORITHMS
(CORE ELECTIVE –II)**

Objectives:

- To learn parallel and distributed algorithms development techniques for shared memory and message passing models.
- To study the main classes of parallel algorithms.
- To study the complexity and correctness models for parallel algorithms.

UNIT-I

Basic Techniques, Parallel Computers for increase Computation speed, Parallel & Cluster Computing

UNIT-II

Message Passing Technique- Evaluating Parallel programs and debugging, Portioning and Divide and Conquer strategies examples

UNIT-III

Pipelining- Techniques computing platform, pipeline programs examples

UNIT-IV

Synchronous Computations, load balancing, distributed termination examples, programming with shared memory, shared memory multiprocessor constructs for specifying parallel sharing data parallel programming languages and constructs, open MP

UNIT-V

Distributed shared memory systems and programming achieving constant memory distributed shared memory programming primitives, algorithms – sorting and numerical algorithms.

TEXT BOOK:

1. Parallel Programming, Barry Wilkinson, Michael Allen, Pearson Education, 2nd Edition.

REFERENCE BOOK:

1. Introduction to Parallel algorithms by Jaja from Pearson, 1992.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

SOFTWARE ARCHITECTURE AND DESIGN PATTERNS (CORE ELECTIVE –II)

Objectives:

After completing this course, the student should be able to:

- To understand the concept of patterns and the Catalog.
- To discuss the Presentation tier design patterns and their affect on: sessions, client access, validation and consistency.
- To understand the variety of implemented bad practices related to the Business and Integration tiers.
- To highlight the evolution of patterns.
- To how to add functionality to designs while minimizing complexity
- To understand what design patterns really are, and are not
- To learn about specific design patterns.
- To learn how to use design patterns to keep code quality high without overdesign.

UNIT I

Envisioning Architecture

The Architecture Business Cycle, What is Software Architecture, Architectural patterns, reference models, reference architectures, architectural structures and views.

Creating an Architecture

Quality Attributes, Achieving qualities, Architectural styles and patterns, designing the Architecture, Documenting software architectures, Reconstructing Software Architecture.

UNIT II

Analyzing Architectures

Architecture Evaluation, Architecture design decision making, ATAM, CBAM.

Moving from one system to many

Software Product Lines, Building systems from off the shelf components, Software architecture in future.

UNIT III

Patterns

Pattern Description, Organizing catalogs, role in solving design problems, Selection and usage.

Creational and Structural patterns

Abstract factory, builder, factory method, prototype, singleton, adapter, bridge, composite, façade, flyweight.

UNIT IV

Behavioral patterns

Chain of responsibility, command, Interpreter, iterator, mediator, memento, observer, state, strategy, template method, visitor.

UNIT V

Case Studies

A-7E – A case study in utilizing architectural structures, The World Wide Web - a case study in interoperability, Air Traffic Control – a case study in designing for high availability, Celsius Tech – a case study in product line development,

TEXT BOOKS:

1. Software Architecture in Practice, second edition, Len Bass, Paul Clements & Rick Kazman, Pearson Education, 2003.
2. Design Patterns, Erich Gamma, Pearson Education, 1995.

REFERENCE BOOKS:

1. Beyond Software architecture, Luke Hohmann, Addison wesley, 2003.
2. Software architecture, David M. Dikel, David Kane and James R. Wilson, Prentice Hall PTR, 2001
3. Software Design, David Budgen, second edition, Pearson education, 2003
4. Head First Design patterns, Eric Freeman & Elisabeth Freeman, O'REILLY, 2007.
5. Design Patterns in Java, Steven John Metsker & William C. Wake, Pearson education, 2006
6. J2EE Patterns, Deepak Alur, John Crupi & Dan Malks, Pearson education, 2003.
7. Design Patterns in C#, Steven John metsker, Pearson education, 2004.
8. Pattern Oriented Software Architecture, F.Buschmann & others, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – I Sem

EMBEDDED SYSTEMS (CORE ELECTIVE-II)

Objectives:

- To explain various embedded system applications and design requirements.
- To construct embedded system hardware.
- To develop software programs to control embedded system.
- To generate product specification for embedded system.

UNIT I

Introduction to Embedded Systems: Embedded Systems, Processor Embedded into a System, Embedded Hardware Units and Devices in a System, Embedded Software, Complex System Design, Design Process in Embedded System, Formalization of System Design, Classification of Embedded Systems

UNIT II

8051 and Advanced Processor Architecture: 8051 Architecture, 8051 Micro controller Hardware, Input/output Ports and Circuits, External Memory, Counter and Timers, Serial data Input/output, Interrupts, Introduction to Advanced Architectures, Real World Interfacing, Processor and Memory organization - **Devices and Communication Buses for Devices Network:** Serial and parallel Devices & ports, Wireless Devices, Timer and Counting Devices, Watchdog Timer, Real Time Clock, Networked Embedded Systems, Internet Enabled Systems, Wireless and Mobile System protocols

UNIT III

Embedded Programming Concepts: Software programming in Assembly language and High Level Language, Data types, Structures, Modifiers, Loops and Pointers, Macros and Functions, object oriented Programming, Embedded Programming in C++ & JAVA

UNIT IV

Real – Time Operating Systems: OS Services, Process and Memory Management, Real – Time Operating Systems, Basic Design Using an RTOS, Task Scheduling Models, Interrupt Latency, Response of Task as Performance Metrics - **RTOS Programming:** Basic functions and Types of RTOSes, RTOS VxWorks, Windows CE

UNIT V

Embedded Software Development Process and Tools: Introduction to Embedded Software Development Process and Tools, Host and Target Machines, Linking and Locating Software, Getting Embedded Software into the Target System, Issues in Hardware-Software Design and Co-Design - **Testing, Simulation and Debugging Techniques and Tools:** Testing on Host Machine, Simulators, Laboratory Tools

TEXT BOOK:

1. Embedded Systems, Raj Kamal, Second Edition TMH.

REFERENCE BOOKS:

1. Embedded/Real-Time Systems, Dr.K.V.K.K.Prasad, dreamTech press
2. The 8051 Microcontroller and Embedded Systems, Muhammad Ali Mazidi, Pearson.
3. The 8051 Microcontroller, Third Edition, Kenneth J.Ayala, Thomson.
4. An Embedded Software Primer, David E. Simon, Pearson Education.
5. Micro Controllers, Ajay V Deshmukhi, TMH.
6. Microcontrollers, Raj kamal, Pearson Education.
7. Introduction to Embedded Systems,Shibu K.V,TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech-CSE – I Year – I Sem****DATA STRUCTURES AND ALGORITHMS LAB****Objectives:**

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.

Sample Problems on Data structures:

1. Write Java programs that use both recursive and non-recursive functions for implementing the following searching methods:
 - a) Linear search
 - b) Binary search
2. Write Java programs to implement the following using arrays and linked lists
 - a) List ADT
3. Write Java programs to implement the following using an array.
 - a) Stack ADT
 - b) Queue ADT
4. Write a Java program that reads an infix expression and converts the expression to postfix form. (Use stack ADT).
5. Write a Java program to implement circular queue ADT using an array.
6. Write a Java program that uses both a stack and a queue to test whether the given string is a palindrome or not.
7. Write Java programs to implement the following using a singly linked list.
 - a) Stack ADT
 - b) Queue ADT
8. Write Java programs to implement the deque (double ended queue) ADT using
 - a) Array
 - b) Singly linked list
 - c) Doubly linked list.
9. Write a Java program to implement priority queue ADT.
10. Write a Java program to perform the following operations:
 - a) Construct a binary search tree of elements.
 - b) Search for a key element in the above binary search tree.
 - c) Delete an element from the above binary search tree.
11. Write a Java program to implement all the functions of a dictionary (ADT) using Hashing.
12. Write a Java program to implement Dijkstra's algorithm for Single source shortest path problem.
13. Write Java programs that use recursive and non-recursive functions to traverse the given binary tree in
 - a) Preorder
 - b) Inorder
 - c) Postorder.
14. Write Java programs for the implementation of bfs and dfs for a given graph.
15. Write Java programs for implementing the following sorting methods:
 - a) Bubble sort
 - b) Insertion sort
 - c) Quick sort
 - d) Merge sort
 - e) Heap sort
 - f) Radix sort
 - g) Binary tree sort
16. Write a Java program to perform the following operations:
 - a) Insertion into a B-tree
 - b) Searching in a B-tree
17. Write a Java program that implements Kruskal's algorithm to generate minimum cost spanning tree.
18. Write a Java program that implements KMP algorithm for pattern matching.

REFERENCE BOOKS:

1. Data Structures and Algorithms in java, 3rd edition, A.Drozdek, Cengage Learning.
 2. Data Structures with Java, J.R.Hubbard, 2nd edition, Schaum's Outlines, TMH.
 3. Data Structures and algorithms in Java, 2nd Edition, R.Lafore, Pearson Education.
 4. Data Structures using Java, D.S.Malik and P.S. Nair, Cengage Learning.
 5. Data structures, Algorithms and Applications in java, 2nd Edition, S.Sahani, Universities Press.
 6. Design and Analysis of Algorithms, P.H.Dave and H.B.Dave, Pearson education.
 7. Data Structures and java collections frame work, W.J.Collins, Mc Graw Hill.
 8. Java: the complete reference, 7th editon, Herbert Schildt, TMH.
 9. Java for Programmers, P.J.Deitel and H.M.Deitel, Pearson education / Java: How to Program P.J.Deitel and H.M.Deitel , 8th edition, PHI.
 10. Java Programming, D.S.Malik,Cengage Learning.
 11. A Practical Guide to Data Structures and Algorithms using Java, S.Goldman & K.Goldman, Chapman & Hall/CRC, Taylor & Francis Group.
- (Note: Use packages like java.io, java.util, etc)**

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.TECH. (COMPUTER SCIENCE AND ENGINEERING)
COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Network Programming	25	75	4	--	4
Core Course V	Information Retrieval Systems	25	75	4	--	4
Core Course VI	Internet Technologies and Services	25	75	4	--	4
Core Elective III	Core Elective– 3 1. Data Mining 2. Storage Area Networks 3. Semantic Web and Social Networks 4. Cyber Security	25	75	4	--	4
Core Elective IV	Core Elective– 4 1. Big Data Analytics 2. Soft Computing 3. Software Process and Project Management 4. Distributed Computing	25	75	4	--	4
Open Elective II	Open Elective – 2	25	75	4	--	4
Laboratory II	Internet Technologies and Services Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits			24	8	28	

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

NETWORK PROGRAMMING

Objectives:

- To understand to Linux utilities
- To understand file handling, signals
- To understand IPC, network programming in Java
- To understand processes to communicate with each other across a Computer Network.

UNIT – I

Linux Utilities- File handling utilities, Security by file permissions, Process utilities, Disk utilities, Networking utilities, Filters, Text processing utilities and Backup utilities.

Bourne again shell(bash) - Introduction, pipes and redirection, here documents, running a shell script, the shell as a programming language, shell meta characters, file name substitution, shell variables, command substitution, shell commands, the environment, quoting, test command, control structures, arithmetic in shell, shell script examples.

Review of C programming concepts-arrays, strings (library functions), pointers, function pointers, structures, unions, libraries in C.

UNIT - II

Files- File Concept, File types File System Structure, Inodes, File Attributes, file I/O in C using system calls, kernel support for files, file status information-stat family, file and record locking-lockf and fcntl functions, file permissions- chmod, fchmod, file ownership-chown, lchown , fchown, links-soft links and hard links – symlink, link, unlink.

File and Directory management – Directory contents, Scanning Directories- Directory file APIs.

Process- Process concept, Kernel support for process, process attributes, process control – process creation, replacing a process image, waiting for a process, process termination, zombie process, orphan process.

UNIT - III

Signals- Introduction to signals, Signal generation and handling, Kernel support for signals, Signal function, unreliable signals, reliable signals, kill, raise , alarm, pause, abort, sleep functions.

Interprocess Communication - Introduction to IPC mechanisms, Pipes- creation, IPC between related processes using unnamed pipes, FIFOs-creation, IPC between unrelated processes using FIFOs(Named pipes), differences between unnamed and named pipes, popen and pclose library functions, Introduction to message queues, semaphores and shared memory.

Message Queues- Kernel support for messages, UNIX system V APIs for messages, client/server example.

Semaphores-Kernel support for semaphores, UNIX system V APIs for semaphores.

UNIT – IV

Shared Memory- Kernel support for shared memory, UNIX system V APIs for shared memory, client/server example.

Network IPC - Introduction to Unix Sockets, IPC over a network, Client-Server model ,Address formats(Unix domain and Internet domain), Socket system calls for Connection Oriented - Communication, Socket system calls for Connectionless-Communication, Example-Client/Server Programs- Single Server-Client connection, Multiple simultaneous clients, Socket options – setsockopt, getsockopt, fcntl.

UNIT-V

Network Programming in Java-Network basics, TCP sockets, UDP sockets (datagram sockets), Server programs that can handle one connection at a time and multiple connections (using multithreaded server), Remote Method Invocation (Java RMI)-Basic RMI Process, Implementation details-Client-Server Application.

TEXT BOOKS:

1. Unix System Programming using C++, T.Chan, PHI.(Units II,III,IV)
2. Unix Concepts and Applications, 4th Edition, Sumitabha Das, TMH.(Unit I)
3. An Introduction to Network Programming with Java, Jan Graba, Springer, rp 2010.(Unit V)
4. Unix Network Programming ,W.R. Stevens, PHI.(Units II,III,IV)
5. Java Network Programming,3rd edition, E.R. Harold, SPD, O'Reilly.(Unit V)

REFERENCE BOOKS:

1. Linux System Programming, Robert Love, O'Reilly, SPD.
2. Advanced Programming in the UNIX environment, 2nd Edition, W.R.Stevens, Pearson Education.
3. UNIX for programmers and users, 3rd Edition, Graham Glass, King Ables, Pearson Education.
4. Beginning Linux Programming, 4th Edition, N.Matthew, R.Stones, Wrox, Wiley India Edition.
5. Unix Network Programming The Sockets Networking API, Vol.-I,W.R.Stevens, Bill Fenner, A.M.Rudoff, Pearson Education.
6. Unix Internals, U.Vahalia, Pearson Education.
7. Unix shell Programming, S.G.Kochan and P.Wood, 3rd edition, Pearson Education.
8. C Programming Language, Kernighan and Ritchie, PHI

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

INFORMATION RETRIEVAL SYSTEMS

Objectives:

On completion of this course you should have gained a good understanding of the foundation concepts of information retrieval techniques and be able to apply these concepts into practice. Specifically, you should be able to:

- To use different information retrieval techniques in various application areas
- To apply IR principles to locate relevant information large collections of data
- To analyze performance of retrieval systems when dealing with unmanaged data sources
- To implement retrieval systems for web search tasks.

UNIT I

Boolean retrieval. The term vocabulary and postings lists. Dictionaries and tolerant retrieval. Index construction. Index compression.

UNIT II

Scoring, term weighting and the vector space model. Computing scores in a complete search system. Evaluation in information retrieval. Relevance feedback and query expansion.

UNIT III

XML retrieval. Probabilistic information retrieval. Language models for information retrieval. Text classification. Vector space classification.

UNIT IV

Support vector machines and machine learning on documents, Flat clustering, Hierarchical clustering, Matrix decompositions and latent semantic indexing.

UNIT V

Web search basics. Web crawling and indexes, Link analysis.

TEXT BOOK:

1. Introduction to Information Retrieval , Christopher D. Manning and Prabhakar Raghavan and Hinrich Schütze, Cambridge University Press, 2008.

REFERENCE BOOKS:

1. Information Storage and Retrieval Systems: Theory and Implementation, Kowalski, Gerald, Mark T Maybury, Springer.
2. Modern Information Retrieval, Ricardo Baeza-Yates, Pearson Education, 2007.
3. Information Retrieval: Algorithms and Heuristics, David A Grossman and Ophir Frieder, 2nd Edition, Springer, 2004.
4. Information Retrieval Data Structures and Algorithms, William B Frakes, Ricardo Baeza-Yates, Pearson Education, 1992.
5. Information Storage & Retrieval, Robert Korfhage, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

INTERNET TECHNOLOGIES AND SERVICES

Objective:

The student who has knowledge of programming with java should be able to develop web based solutions using multi-tier architecture. S/he should have good understanding of different technologies on client and server side components as Follows:

Client Side: HTML5, CSS3, Javascript, Ajax, JQuery and JSON

Server Side: Servlets, JSP

Database: MySQL with Hibernate and Connection Pooling

Framework: Struts with validation framework, Internationalization (I18N)

SOA: Service Oriented Architecture, Web services fundamentals, Axis framework for WS

UNIT I

Client Side Technologies:

Overview of HTML - Common tags, XHTML, capabilities of HTML5

Cascading Style sheets, CSS3 enhancements, linking to HTML Pages, Classes in CSS

Introduction to JavaScripts, variables, arrays, methods and string manipulation, BOM/DOM (Browser/Document Object Model), accessing elements by ID, Objects in JavaScript

Dynamic HTML with JavaScript and with CSS, form validation with JavaScript, Handling Timer Events

Simplifying scripting with JQuery, JASON for Information exchange.

UNIT II

Introduction to Java Servlets:

Introduction to Servlets: Lifecycle of a Servlet, Reading request and initialization parameters, Writing output to response, MIME types in response, Session Tracking: Using Cookies and Sessions

Steps involved in Deploying an application

Database Access with JDBC and Connection Pooling

Introduction to XML, XML Parsing with DOM and SAX Parsers in Java

Ajax - Ajax programming with JSP/Servlets, creating XML Http Object for various browsers, Sending request, Processing response data and displaying it.

Introduction to Hibernate

UNIT III

Introduction to JSP:

JSP Application Development: Types of JSP Constructs (Directives, Declarations, Expressions, Code Snippets), Generating Dynamic Content, Exception Handling, Implicit JSP Objects, Conditional Processing, Sharing Data Between JSP pages, Sharing Session and Application Data, Using user defined classes with jsp:useBean tag, Accessing a Database from a JSP

UNIT IV

Introduction to Struts Framework:

Introduction to MVC architecture, Anatomy of a simple struts2 application, struts configuration file, Presentation layer with JSP, JSP bean, html and logic tag libraries, Struts Controller class, Using form data in Actions, Page Forwarding, validation frame work, Internationalization

UNIT V

Service Oriented Architecture and Web Services

Overview of Service Oriented Architecture – SOA concepts, Key Service Characteristics, Technical Benefits of a SOA

Introduction to Web Services– The definition of web services, basic operational model of web services, basic steps of implementing web services.

Core fundamentals of SOAP – SOAP Message Structure, SOAP encoding, SOAP message exchange models,

Describing Web Services –Web Services life cycle, anatomy of WSDL

Introduction to Axis– Installing axis web service framework, deploying a java web service on axis.

Web Services Interoperability – Creating java and .Net client applications for an Axis Web Service
(Note: The Reference Platform for the course will be open source products Apache Tomcat Application Server, MySQL database, Hibernate and Axis)

TEXT BOOKS:

1. Web Programming, building internet applications, Chris Bates 3rd edition, WILEY Dreamtech .
2. The complete Reference Java 7th Edition , Herbert Schildt., TMH.
3. Java Server Pages,Hans Bergsten, SPD, O'Reilly.
4. Professional Jakarta Struts - James Goodwill, Richard Hightower, Wrox Publishers.
5. Developing Java Web Services, R. Nagappan, R. Skoczylas, R.P. Sriganesh, Wiley India, rp – 2008.
6. Understanding SOA with Web Services, Eric Newcomer and Greg Lomow, Pearson Edition – 2009
7. Java Web Service Architecture, James McGovern, Sameer Tyagi et al., Elsevier - 2009

REFERENCE BOOKS:

1. Programming the world wide web,4th edition,R.W.Sebesta,Pearson
2. Core SERVLETS ANDJAVASERVER PAGES VOLUME 1: CORE
3. TECHNOLOGIES , Marty Hall and Larry Brown Pearson
4. Internet and World Wide Web – How to program , Dietel and Nieto PHI/Pearson.
5. Jakarta Struts Cookbook , Bill Siggelkow, S P D O'Reilly.
6. Professional Java Server Programming,S.Allamaraju & othersApress(dreamtech).
7. Java Server Programming ,Ivan Bayross and others,The X Team,SPD
8. Web Warrior Guide to Web Programmimg-Bai/Ekedaw-Cengage Learning.
9. Beginning Web Programming-Jon Duckett ,WROX.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

DATA MINING (Core Elective –III)

Objectives:

- To develop the abilities of critical analysis to data mining systems and applications.
- To implement practical and theoretical understanding of the technologies for data mining
- To understand the strengths and limitations of various data mining models;

UNIT-I

Data mining Overview and Advanced Pattern Mining

Data mining tasks – mining frequent patterns, associations and correlations, classification and regression for predictive analysis, cluster analysis, outlier analysis; advanced pattern mining in multilevel, multidimensional space – mining multilevel associations, mining multidimensional associations, mining quantitative association rules, mining rare patterns and negative patterns.

UNIT-II

Advance Classification

Classification by back propagation, support vector machines, classification using frequent patterns, other classification methods – genetic algorithms, roughset approach, fuzzy set approach;

UNIT-III

Advance Clustering

Density - based methods – DBSCAN, OPTICS, DENCLUE; Grid-Based methods – STING, CLIQUE; Exception – maximization algorithm; clustering High- Dimensional Data; Clustering Graph and Network Data.

UNIT-IV

Web and Text Mining

Introduction, web mining, web content mining, web structure mining, web usage mining, Text mining – unstructured text, episode rule discovery for texts, hierarchy of categories, text clustering.

UNIT-V

Temporal and Spatial Data Mining

Introduction; Temporal Data Mining – Temporal Association Rules, Sequence Mining, GSP algorithm, SPADE, SPIRIT Episode Discovery, Time Series Analysis, Spatial Mining – Spatial Mining Tasks, Spatial Clustering. Data Mining Applications.

TEXT BOOKS:

1. Data Mining Concepts and Techniques, Jiawei Han, Micheline Kamber, Jian pei, Morgan Kaufmann.
2. Data Mining Techniques – Arun K. Pujari, Universities Press.

REFERENCE BOOKS:

1. Introduction to Data Mining – Pang-Ning Tan, Vipin kumar, Michael Steinbach, Pearson.
2. Data Mining Principles & Applications – T.V Suresh Kumar, B.Esware Reddy, Jagadish S. Kalimani, Elsevier.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

STORAGE AREA NETWORKS (Core Elective-III)

Objectives:

- To understand Storage Area Networks characteristics and components.
- To become familiar with the SAN vendors and their products
- To learn Fibre Channel protocols and how SAN components use them to communicate with each other
- To become familiar with Cisco MDS 9000 Multilayer Directors and Fabric Switches Thoroughly learn Cisco SAN-OS features.
- To understand the use of all SAN-OS commands. Practice variations of SANOS features

UNIT I: Introduction to Storage Technology

Review data creation and the amount of data being created and understand the value of data to a business, challenges in data storage and data management, Solutions available for data storage, Core elements of a data center infrastructure, role of each element in supporting business activities

UNIT II: Storage Systems Architecture

Hardware and software components of the host environment, Key protocols and concepts used by each component ,Physical and logical components of a connectivity environment ,Major physical components of a disk drive and their function, logical constructs of a physical disk, access characteristics, and performance Implications, Concept of RAID and its components , Different RAID levels and their suitability for different application environments: RAID 0, RAID 1, RAID 3, RAID 4, RAID 5, RAID 0+1, RAID 1+0, RAID 6, Compare and contrast integrated and modular storage systems ,High-level architecture and working of an intelligent storage system

UNIT III: Introduction to Networked Storage

Evolution of networked storage, Architecture, components, and topologies of FC-SAN, NAS, and IP-SAN, Benefits of the different networked storage options, understand the need for long-term archiving solutions and describe how CAS fulfills the need, understand the appropriateness of the different networked storage options for different application environments

UNIT IV: Information Availability & Monitoring & Managing Datacenter

List reasons for planned/unplanned outages and the impact of downtime, Impact of downtime, Differentiate between business continuity (BC) and disaster recovery (DR) ,RTO and RPO, Identify single points of failure in a storage infrastructure and list solutions to mitigate these failures , Architecture of backup/recovery and the different backup/recovery topologies , replication technologies and their role in ensuring information availability and business continuity, Remote replication technologies and their role in providing disaster recovery and business continuity capabilities

Identify key areas to monitor in a data center, Industry standards for data center monitoring and management, Key metrics to monitor for different components in a storage infrastructure, Key management tasks in a data center

UNIT V: Securing Storage and Storage Virtualization

Information security, Critical security attributes for information systems, Storage security domains, List and analyzes the common threats in each domain, Virtualization technologies, block-level and file-level virtualization technologies and processes

Case Studies

The technologies described in the course are reinforced with EMC examples of actual solutions. Realistic case studies enable the participant to design the most appropriate solution for given sets of criteria.

TEXT BOOK:

1. EMC Corporation, Information Storage and Management, Wiley.

REFERENCE BOOKS:

1. Robert Spalding, "Storage Networks: The Complete Reference", Tata McGraw Hill, Osborne, 2003.
2. Marc Farley, "Building Storage Networks", Tata McGraw Hill ,Osborne, 2001.
3. Meeta Gupta, Storage Area Network Fundamentals, Pearson Education Limited, 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

SEMANTIC WEB AND SOCIAL NETWORKS (Core Elective –III)

Objectives:

- To learn Web Intelligence
- To learn Knowledge Representation for the Semantic Web
- To learn Ontology Engineering
- To learn Semantic Web Applications, Services and Technology
- To learn Social Network Analysis and semantic web

UNIT –I:

Web Intelligence

Thinking and Intelligent Web Applications, The Information Age ,The World Wide Web, Limitations of Today's Web, The Next Generation Web, Machine Intelligence, Artificial Intelligence, Ontology, Inference engines, Software Agents, Berners-Lee www, Semantic Road Map, Logic on the semantic Web.

UNIT -II:

Knowledge Representation for the Semantic Web

Ontologies and their role in the semantic web, Ontologies Languages for the Semantic Web –Resource Description Framework(RDF) / RDF Schema, Ontology Web Language(OWL), UML, XML/XML Schema.

UNIT-III:

Ontology Engineering

Ontology Engineering, Constructing Ontology, Ontology Development Tools, Ontology Methods, Ontology Sharing and Merging, Ontology Libraries and Ontology Mapping, Logic, Rule and Inference Engines.

UNIT-IV:

Semantic Web Applications, Services and Technology

Semantic Web applications and services, Semantic Search, e-learning, Semantic Bioinformatics, Knowledge Base ,XML Based Web Services, Creating an OWL-S Ontology for Web Services, Semantic Search Technology, Web Search Agents and Semantic Methods,

UNIT-V: .

Social Network Analysis and semantic web

What is social Networks analysis, development of the social networks analysis, Electronic Sources for Network Analysis – Electronic Discussion networks, Blogs and Online Communities, Web Based Networks. Building Semantic Web Applications with social network features.

TEXT BOOKS:

1. Thinking on the Web - Berners Lee, Godel and Turing, Wiley inter science, 2008.
2. Social Networks and the Semantic Web, Peter Mika, Springer, 2007.

REFERENCE BOOKS:

1. Semantic Web Technologies, Trends and Research in Ontology Based Systems, J.Davies, R.Studer, P.Warren, John Wiley & Sons.
2. Semantic Web and Semantic Web Services -Liyang Lu Chapman and Hall/CRC Publishers,(Taylor & Francis Group)
3. Information Sharing on the semantic Web - Heiner Stuckenschmidt; Frank Van Harmelen, Springer Publications.
4. Programming the Semantic Web, T.Segaran, C.Evans, J.Taylor, O'Reilly, SPD.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

CYBER SECURITY (Core Elective-III)

Objectives:

To learn about cyber crimes and how they are planned
To learn the vulnerabilities of mobile and wireless devices
To learn about the crimes in mobile and wireless devices

UNIT-I

Introduction to Cybercrime: Introduction, Cybercrime and Information security, who are cybercriminals, Classifications of Cybercrimes, Cybercrime: The legal Perspectives and Indian Perspective, Cybercrime and the Indian ITA 2000, A Global Perspective on Cybercrimes.

Cyber offenses: How criminals Plan Them: Introduction, How Criminals plan the Attacks, Social Engineering, Cyber stalking, Cyber cafe and Cybercrimes, Botnets: The Fuel for Cybercrime, Attack Vector, Cloud Computing.

UNIT-II

Cybercrime: Mobile and Wireless Devices: Introduction, Proliferation of Mobile and Wireless Devices, Trends in Mobility, Credit card Frauds in Mobile and Wireless Computing Era, Security Challenges Posed by Mobile Devices, Registry Settings for Mobile Devices, Authentication service Security, Attacks on Mobile/Cell Phones, Mobile Devices: Security Implications for Organizations, Organizational Measures for Handling Mobile, Organizational Security Policies and Measures in Mobile Computing Era, Laptops.

UNIT III

Cybercrimes and Cyber security: the Legal Perspectives: Introduction: Cyber Crime and Legal Landscape around the world, Why Do We Need Cyber laws: The Indian Context, The Indian IT Act, Challenges to Indian Law and Cybercrime Scenario In India, Digital signatures and the Indian IT Act, Amendments to the Indian IT Act, Cybercrime and Punishment
Cyber law, Technology and Students: Indian Scenario.

UNIT IV

Understanding Computer Forensics

Introduction, Historical background of Cyber forensics, Digital Forensics Science, The Need for Computer Forensics, Cyber Forensics and Digital evidence, Forensics Analysis of Email, Digital Forensics Lifecycle, Chain of Custody concept, Network Forensics, Approaching a computer, Forensics Investigation, Challenges in Computer Forensics, Special Tools and Techniques
Forensics Auditing

UNIT V

Cyber Security: Organizational Implications

Introduction, Cost of Cybercrimes and IPR issues, Web threats for Organizations, Security and Privacy Implications, Social media marketing: Security Risks and Perils for Organizations, Social Computing and the associated challenges for Organizations.

TEXT BOOKS:

1. **Cyber Security:** *Understanding Cyber Crimes, Computer Forensics and Legal Perspectives*, Nina Godbole and Sunil Belapure, Wiley INDIA.
2. **Introduction to Cyber Security**, Chwan-Hwa(john) Wu, J. David Irwin. CRC Press T&F Group

REFERENCE BOOK:

1. **Cyber Security Essentials**, James Graham, Richard Howard and Ryan Otson, CRC Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

BIG DATA ANALYTICS (Core Elective –IV)

Objectives:

To understand about big data
To learn the analytics of Big Data
To Understand the MapReduce fundamentals

Unit I

Big Data Analytics : What is big data, History of Data Management ; Structuring Big Data ; Elements of Big Data ; Big Data Analytics; Distributed and Parallel Computing for Big Data;
Big Data Analytics:What is Big Data Analytics, What Big Data Analytics Isn't, Why this sudden Hype Around Big Data Analytics, Classification of Analytics, Greatest Challenges that Prevent Business from Capitalizing Big Data; Top Challenges Facing Big Data; Why Big Data Analytics Important; Data Science; Data Scientist; Terminologies used in Big Data Environments; Basically Available Soft State Eventual Consistency (BASE); Open source Analytics Tools;

Unit- II

Understanding Analytics and Big Data: Comparing Reporting and Analysis, Types of Analytics; Points to Consider during Analysis; Developing an Analytic Team; Understanding Text Analytics;
Analytical Approach and Tools to Analyze Data: Analytical Approaches; History of Analytical Tools; Introducing Popular Analytical Tools; Comparing Various Analytical Tools.

Unit III

Understanding MapReduce Fundamentals and HBase : The MapReduce Framework; Techniques to Optimize MapReduce Jobs; Uses of MapReduce; Role of HBase in Big Data Processing; Storing Data in Hadoop : Introduction of HDFS, Architecture, HDFS Files, File system types, commands, org.apache.hadoop.io package, HDFS High Availability; Introducing HBase, Architecture, Storing Big Data with HBase , Interacting with the Hadoop Ecosystem; HBase in Operations-Programming with HBase; Installation, Combining HBase and HDFS;

Unit IV

Big Data Technology Landscape and Hadoop : NoSQL, Hadoop; RDBMS versus Hadoop; Distributed Computing Challenges; History of Hadoop; Hadoop Overview; Use Case of Hadoop; Hadoop Distributors; HDFS (Hadoop Distributed File System), HDFS Daemons, read,write, Replica Processing of Data with Hadoop; Managing Resources and Applications with Hadoop YARN.

Unit V

Social Media Analytics and Text Mining: Introducing Social Media; Key elements of Social Media; Text mining; Understanding Text Mining Process; Sentiment Analysis, Performing Social Media Analytics and Opinion Mining on Tweets;
Mobile Analytics: Introducing Mobile Analytics; Define Mobile Analytics; Mobile Analytics and Web Analytics; Types of Results from Mobile Analytics; Types of Applications for Mobile Analytics; Introducing Mobile Analytics Tools;

TEXT BOOKS

1. BIG DATA and ANALYTICS, Seema Acharya, Subhasinin Chellappan, Wiley publications.
2. BIG DATA, Black Book™ , DreamTech Press, 2015 Edition.
3. BUSINESS ANALYTICS 5e , BY Albright |Winston

REFERENCE BOOKS:

1. Rajiv Sabherwal, Irma Becerra- Fernandez," Business Intelligence –Practice, Technologies and Management", John Wiley 2011.
2. Lariss T. Moss,ShakuAtre, " Business Intelligence Roadmap", Addison-Wesley It Service.
3. Yuli Vasiliev, " Oracle Business Intelligence : The Condensed Guide to Analysis and Reporting", SPD Shroff, 2012.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

SOFT COMPUTING (Core Elective –IV)

Objectives:

To give students knowledge of soft computing theories fundamentals, i.e. Fundamentals of artificial and neural networks, fuzzy sets and fuzzy logic and genetic algorithms.

UNIT-I

AI Problems and Search: AI problems, Techniques, Problem Spaces and Search, Heuristic Search Techniques- Generate and Test, Hill Climbing, Best First Search Problem reduction, Constraint Satisfaction and Means End Analysis. Approaches to Knowledge Representation- Using Predicate Logic and Rules.

UNIT-II

Artificial Neural Networks: Introduction, Basic models of ANN, important terminologies, Supervised Learning Networks, Perceptron Networks, Adaptive Linear Neuron, Back propagation Network. Associative Memory Networks. Traing Algorithms for pattern association, BAM and Hopfield Networks.

UNIT-III

Unsupervised Learning Network- Introduction, Fixed Weight Competitive Nets, Maxnet, Hamming Network, Kohonen Self-Organizing Feature Maps, Learning Vector Quantization, Counter Propagation Networks, Adaptive Resonance Theory Networks. Special Networks-Introduction to various networks.

UNIT-IV

Introduction to Classical Sets (crisp Sets)and Fuzzy Sets- operations and Fuzzy sets. Classical Relations -and Fuzzy Relations- Cardinality, Operations, Properties and composition. Tolerance and equivalence relations.

Membership functions- Features, Fuzzification, membership value assignments, Defuzzification.

UNIT-V

Fuzzy Arithmetic and Fuzzy Measures, Fuzzy Rule Base and Approximate Reasoning Fuzzy Decision making

Fuzzy Logic Control Systems, Genetic Algorithm- Introduction and basic operators and terminology. Applications: Optimization of TSP, Internet Search Technique.

TEXT BOOKS:

1. Principles of Soft Computing- S N Sivanandam, S N Deepa, Wiley India, 2007
2. Soft Computing and Intelligent System Design -Fakhreddine O Karray, Clarence D Silva,. Pearson Edition, 2004.

REFERENCE BOOKS:

1. Artificial Intelligence and SoftComputing- Behavioural and Cognitive Modeling of the Human Brain- Amit Konar, CRC press, Taylor and Francis Group.
2. Artificial Intelligence – Elaine Rich and Kevin Knight, TMH, 1991, rp2008.
3. Artificial Intelligence – Patric Henry Winston – Third Edition, Pearson Education.
4. A first course in Fuzzy Logic-Hung T Nguyen and Elbert A Walker, CRC. Press Taylor and Francis Group.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

SOFTWARE PROCESS AND PROJECT MANAGEMENT (Core Elective – IV)

Objectives:

- Describe and determine the purpose and importance of project management from the perspectives of planning, tracking and completion of project.
- Compare and differentiate organization structures and project structures.
- Implement a project to manage project schedule, expenses and resources with the application of suitable project management tools.

UNIT I

Software Process Maturity : Software maturity Framework, Principles of Software Process Change, Software Process Assessment, The Initial Process, The Repeatable Process, The Defined Process, The Managed Process, The Optimizing Process.

Process Reference Models : Capability Maturity Model (CMM), CMMI, PCMM, PSP, TSP.

UNIT II

Software Project Management Renaissance : Conventional Software Management, Evolution of Software Economics, Improving Software Economics, The old way and the new way.

Life-Cycle Phases and Process artifacts : Engineering and Production stages, inception phase, elaboration phase, construction phase, transition phase, artifact sets, management artifacts, engineering artifacts and pragmatic artifacts, model based software architectures.

UNIT III

Workflows and Checkpoints of process : Software process workflows, Iteration workflows, Major milestones, Minor milestones, Periodic status assessments.

Process Planning: Work breakdown structures, Planning guidelines, cost and schedule estimating process, iteration planning process, Pragmatic planning.

UNIT IV

Project Organizations: Line-of- business organizations, project organizations, evolution of organizations, process automation.

Project Control and process instrumentation : The seven core metrics, management indicators, quality indicators, life-cycle expectations, Pragmatic software metrics, and metrics automation.

UNIT V

CCPDS-R Case Study and Future Software Project Management Practices : Modern Project Profiles, Next-Generation software Economics, Modern Process Transitions.

TEXT BOOKS:

1. Managing the Software Process, *Watts S. Humphrey*, Pearson Education.
2. Software Project Management, *Walker Royce*, Pearson Education.

REFERENCE BOOKS:

1. Effective Project Management: Traditional, Agile, Extreme, Robert Wysocki, Sixth edition, Wiley India, 2011.
2. An Introduction to the Team Software Process, Watts S. Humphrey, Pearson Education, 2000
3. Process Improvement essentials, James R. Persse, O'Reilly, 2006
3. Software Project Management, Bob Hughes & Mike Cotterell, fourth edition, TMH, 2006
4. Applied Software Project Management, Andrew Stellman & Jennifer Greene, O'Reilly, 2006.
5. Head First PMP, Jennifer Greene & Andrew Stellman, O'Reilly, 2007
6. Software Engineering Project Management, Richard H. Thayer & Edward Yourdon, 2nd edition, Wiley India, 2004.
7. The Art of Project Management, Scott Berkun, SPD, O'Reilly, 2011.
8. Applied Software Project Management, Andrew Stellman & Jennifer Greene, SPD, O'Reilly, 2011.
9. Agile Project Management, Jim Highsmith, Pearson education, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

DISTRIBUTED COMPUTING (Core Elective-IV)

Objectives:

- Foundation of cooperative distributed systems engineering
- Supporting technologies with a special attention to agent-oriented paradigm
- Service-oriented computing and grid computing
- The implementation component includes a term-project

UNIT I

Introduction

The different forms of computing, The strengths and weaknesses of Distributed computing, Operating system concepts relevant to distributed computing, the architecture of distributed applications. Paradigms for Distributed Applications, choosing a Paradigm for an application (trade-offs).

UNIT II

Cluster Computing

Parallel computing overview, cluster computing – Introduction, Cluster Architecture, parallel programming models and Paradigms, Applications of Clusters.

UNIT III

Grid Computing

Introduction, Grid Computing Anatomy – Architecture, Architecture and relationship to other Distributed Technologies, Grid computing road map. Merging the Grid services Architecture with the Web Services Architecture.

UNIT IV

Open Grid Service Architecture – Introduction, Architecture and Goal, Sample Use cases: Commercial Data Center, National Fusion Collaboratory, Online Media and Entertainment. OGSA platform Components, Open Grid Services Infrastructure.

UNIT V

Globus GT 3 Toolkit – Architecture, Programming Model, A sample implementation, High Level services, OGSI.NET Middleware Solutions.

TEXT BOOKS:

1. Grid Computing, Joshy Joseph & Craig Fellenstein, Pearson education, 2004
2. Distributed Computing, Principles and Applications, M.L.Liu, Pearson Education, 2004
3. High Performance Cluster Computing, Rajkumar Buyya, Pearson education.

REFERENCE BOOKS:

1. Grid Computing – Making the global infrastructure a reality, Fran Berman, Geoffrey C Fox, Anthony J G Hey, Wiley India, 2010.
2. A Networking Approach to Grid Computing, D.Minoli, Wiley & sons, 2006.
3. Grid Computing: A Practical Guide to Technology and Applications, A.Abbas, Firewall Media, 2008.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech-CSE – I Year – II Sem.

INTERNET TECHNOLOGIES AND SERVICES LAB

Objectives:

- Write syntactically correct HTTP messages and describe the semantics of common HTTP methods and header fields
- Discuss differences between URIs, URNs, and URLs, and demonstrate a detailed understanding of http-scheme URLs, both relative and absolute
- Describe the actions, including those related to the cache, performed by a browser in the process of visiting a Web address
- Install a web server and perform basic administrative procedures, such as tuning communication parameters, denying access to certain domains, and interpreting an access log
- Write a valid standards-conformant HTML document involving a variety of element types, including hyperlinks, images, lists, tables, and forms
- Use CSS to implement a variety of presentation effects in HTML and XML documents, including explicit positioning of elements
- Demonstrate techniques for improving the accessibility of an HTML document

List of Sample Problems:

i) Internet Technologies

1. Develop static pages (using Only HTML) of an online Book store. The pages should resemble: www.amazon.com the website should consist the following pages.
Home page, Registration and user Login
User Profile Page, Books catalog
Shopping Cart, Payment By credit card
Order Conformation
2. Validate the Registration, user login, user profile and payment by credit card pages using JavaScript.
3. Create and save an XML document at the server, which contains 10 users information. Write a program, which takes User Id as an input and returns the user details by taking the user information from the XML document.
4. Install TOMCAT web server. Convert the static web pages of assignments 2 into dynamic web pages using Servlets and cookies. Hint: Users information (user id, password, credit card number) would be stored in web.xml. Each user should have a separate Shopping Cart.
5. Redo the previous task using JSP by converting the static web pages of assignments 2 into dynamic web pages. Create a database with user information and books information. The books catalogue should be dynamically loaded from the database. Follow the MVC architecture while doing the website.
6. Implement the "Hello World!" program using JSP Struts Framework.

ii) Additional Assignment Problems

Write an HTML page including any required Javascript that takes a number from one text field in the range of 0 to 999 and shows it in another text field in words. If the number is out of range, it should show "out of range" and if it is not a number, it should show "not a number" message in the result box.

Write a java swing application that takes a text file name as input and counts the characters, words and lines in the file. Words are separated with white space characters and lines are separated with new line character.

Write a simple calculator servlet that takes two numbers and an operator (+, -, /, * and %) from an HTML page and returns the result page with the operation performed on the operands. It should check in a database if the same expression is already computed and if so, just return the value from database. Use MySQL or PostgreSQL.

Write an HTML page that contains a list of 5 countries. When the user selects a country, its capital should be printed next to the list. Add CSS to customize the properties of the font of the capital (color, bold and font size).

Write a servlet that takes name and age from an HTML page. If the age is less than 18, it should send a page with "Hello <name>, you are not authorized to visit this site" message, where <name> should be replaced with the entered name. Otherwise it should send "Welcome <name> to this site" message.

Write a calculator program in HTML that performs basic arithmetic operations (+, -, /, * and %). Use CSS to change the foreground and background color of the values, buttons and result display area separately. Validate the input strings using JavaScript regular expressions. Handle any special cases like division with zero reasonably. The screen may look similar to the following:

Value 1	Operator	Value 2		Result
	+ ▼		=	

Write a Java program that creates a calculator GUI, as shown in figure. Extra components may be added for convenience:

Color Scheme
 ▼

0	▲	+	▼	0	▲
	▼				▼
Result			0		

The Color Scheme may be Black on White or Blue on Yellow (selectable) and accordingly all components colors must be changed. The values can be either entered or increased or decreased by a step of 10. The operators are +, -, / and * (selectable). Once any change takes place, the result must be automatically computed by the

program.

Write a Java Application that will read an XML file that contains personal information (Name, Mobile Number, age and place). It reads the information using SAX parser. After reading the information, it shows two input Text Fields in a window, one for tag name and the other for value. Once these two values are given, it should list all the records in the XML file that match the value of the given field in a text area (result box). For example, if the two text boxes are entered with "name" and "ABCD" then it should show all the records for which name is "ABCD"? An Illustration is given below that takes a mobile number and lists all the records that have the same mobile number.

Field	mobile	
Value	9449449449	OK
Result	abc, 22, Hyd def, 23, Delhi xxx, 44, Chennai	

Consider the following web application for implementation:

The user is first served a login page which takes user's name and password. After submitting the details the server checks these values against the data from a database and takes the following decisions.

If name and password matches, serves a welcome page with user's full name.

If name matches and password doesn't match, then serves "password mismatch" page

If name is not found in the database, serves a registration page, where users full name, present user name (used to login) and password are collected. Implement this application in:

1. Pure JSP
2. Pure Servlets
3. Struts Framework

Implement a simple arithmetic calculator with +, -, /, *, % and = operations using Struts Framework
The number of times the calculator is used should be displayed at the bottom (use session variable).

iii) Internet Technologies and Services Lab - Additional Problems

Create a web Service in Java that takes two city names from the user and returns the distance between these two from data available from a table in MySQL. Write a java and a C# client which use the above service
Write a Java program that takes a file as input and encrypts it using DES encryption. The program should check if the file exists and its size is not zero.
Write a Java program that generates a key pair and encrypts a given file using RSA algorithm.
Write a Java program that finds digest value of a given string.
Consider the following xml file for encryption <pre><?xml version="1.0"> <transaction> <from>12345</from> <to>54321</to> <amount>10000</amount> <secretcode>abc123</secretcode> <checksum></checksum> </transaction></pre> Replace <from> and <to> values with the RSA encrypted values represented with base64 encoding assuming that the public key is available in a file in local directory "pubkey.dat". Encrypt <secretcode> with AES algorithm with a password 'secret'. The checksum of all the field values concatenated with a delimiter character '+' will be inserted in the checksum and the xml file is written to encrypted.xml file.
Assume that a file 'config.xml', which has the following information: <pre><users> <user> <name>abc</name> <pwd>pwd123</pwd> <role>admin</role> <md5>xxx</md5> </user> <user> <name>def</name> <pwd>pwd123</pwd> <role>guest</role> <md5>xxx</md5> </user> </users></pre> Replace name and role with DES encrypted values and pwd with RSA encrypted values (represent the values with base64 encoding). The public key is available in "public.key" file in current directory. Replace xxx with respective MD5 values of all the fields for each user. Write the resulting file back to config.xml.
Write an HTML page that gives 3 multiple choice (a,b,c and d) questions from a set of 5 preloaded questions randomly. After each question is answered change the color of the question to either green or blue using CSS. Finally on clicking OK button that is provided, the score should be displayed as a pop-up window. Use Java Script for dynamic content.
Write an HTML page that has 3 countries on the left side ("USA", "UK" and "INDIA") and on the right side of each country, there is a pull-down menu that contains the following entries: ("Select Answer", "New Delhi", "Washington" and "London"). The user will match the Countries with their respective capitals by selecting an item from the menu. The user chooses all the three answers (whether right or wrong). Then colors of the countries should be changed either to green or to red depending on the answer. Use CSS for changing color.
Write an HTML Page that can be used for registering the candidates for an entrance test. The fields are: name, age, qualifying examination (diploma or 10+2), stream in qualifying examination. If qualifying examination is "diploma", the stream can be "Electrical", "Mechanical" or "Civil". If the qualifying examination is 10+2, the stream can be "MPC" or "BPC". Validate the name to accept only characters and spaces.
Write an HTML page that has two selection menus. The first menu contains the states ("AP", "TN" and "KN") and depending on the selection the second menu should show the following items: "Hyderabad", "Vijayawada", "Kurnool" for AP, "Chennai", "Salem", "Madurai" for TN and "Bangalore", "Bellary", "Mysore" for KN.
Write an HTML page that has phone buttons 0 to 9 and a text box that shows the dialed number. If 00 is pressed at the beginning, it should be replaced with a + symbol in the text box. If the number is not a valid international number (+ followed by country code and 10 digit phone number) the color of the display should be red and it should turn to green when the number is valid. Consider only "+91, +1 and +44 as valid country codes. Use CSS for defining colors.
Write an HTML page that has a text box for phone number or Name. If a number is entered in the box the name should be displayed next to the number. If 00 is pressed at the beginning, it should be replaced with a + symbol in the text box. If a name is entered in the text box, it should show the number next to the name. If the

corresponding value is not found, show it in red and show it in green otherwise. Use CSS for colors. Store at least 5 names and numbers in the script for testing.
A library consists of 10 titles and each title has a given number of books initially. A student can take or return a book by entering his/her HTNo as user ID and a given password. If there are at least two books, the book is issued and the balance is modified accordingly. (a) Use RDBMS and implement it with JSP. (b) Use XML File for data and Implement it with JSP (c) Use RDBMS and implement it with Servlets (d) Use XML File for data and Implement it with Servlets
A Bus Reservation System contains the details of a bus seat plan for 40 seats in 2x2 per row arrangement, where the seats are numbered from 1 to 40 from first row to last row. The customer can visit the website and can reserve a ticket of his choice if available by entering his details (Name, Address, Gender and Age). The customer can cancel the ticket by entering the seat number and his name as entered for reservation. (a) Use RDBMS and implement it with JSP. (b) Use XML File for data and Implement it with JSP (c) Use RDBMS and implement it with Servlets (d) Use XML File for data and Implement it with Servlets.
Implement a simple messaging system with the following details: When a student logs in with his/her HTNO and a given password, they should get all the messages posted to him/her giving the ID of sender and the actual message. Each message may be separated with a ruler. There should be a provision for the user to send a message to any number of users by giving the IDs separated with commas in the "To" text box. (a) Use RDBMS and implement it with JSP. (b) Use XML File for data and Implement it with JSP (c) Use RDBMS and implement it with Servlets (d) Use XML File for data and Implement it with Servlets.
There is an image of 600x100 size which can be logically divided into 12 button areas with labels (0-9, +, =). Write a javascript calculator program that uses this image as input virtual keyboard and three text areas for two input numbers and result of sum of these numbers. Add a CSS that can be used to change the colors of text and background of text areas and the page. The input numbers can be up to 4 digits each.
Develop a web application that takes user name and password as input and compares them with those available in an xml user database. If they match, it should display the welcome page that contains the user's full name and last used date and time retrieved from a client cookie. On logout it stores new time to the cookie and displays a goodbye page. If authentication fails, it should store the attempt number to the client cookie and displays an error page. Add necessary CSS that takes care of the font, color of foreground and background.
A web application has the following specifications: The first page (Login page) should have a login screen where the user gives the login name and password. Both fields must be validated on client side for a minimum length of 4 characters, name should be lower case a-z characters only and password should contain at least one digit. On submitting these values, the server should validate them with a MySQL database and if failed, show the login page along with a message saying "Login Name or Password Mismatch" in Red color below the main heading and above the form. If successful, show a welcome page with the user's full name (taken from database) and and a link to Logout. On logout, a good bye page is displayed with the total time of usage (Logout time – login time). Specify the Schema details of table and web.xml file contents. Implement it using (a) JSP Pages (b) Servlets (c) Struts
Design a struts based web portal for an international conference with following specifications: The welcome page should give the details of the conference and a link to login. If login fails, direct them back for re-login and also provide a link for registration. On successful registration/login, the user will be directed to a page where s/he can see the status (accepted/rejected) of their already submitted papers followed by a form for submitting a doc file to the conference. Provide a logout button on all pages including the home page, once the user logs in. Implement validation framework to check that the user name is in the form of CCDDCC and password is in the form of (CCSDDD) (C for character, S for special character (one of @, #, \$, %, ^, & and !) and D for digit)., Database should be accessed through Connection Pool for MySql for user information. Provide scope for internationalization in future. Assume any missing information and mention it first.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. (ELECTRICAL POWER SYSTEMS/POWER ENGINEERING AND ENERGY SYSTEMS/
POWER SYSTEM CONTROL AND AUTOMATION/ ELECTRICAL POWER ENGINEERING)
COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Advanced Power System Analysis	25	75	4	--	4
Core Course II	Advanced Power System Protection	25	75	4	--	4
Core Course III	Modern Control Theory	25	75	4	--	4
Core Elective I	1. EHV AC Transmission 2. High Voltage Engineering 3. Advanced Digital Signal Processing	25	75	4	--	4
Core Elective II	1. Power Quality 2. Microcontrollers and applications 3. Distribution Automation	25	75	4	--	4
Open Elective I	1. Optimization Techniques 2. Digital control systems 3. Renewable energy systems 4. HVDC Transmission 5. Analysis of power converters 6. Embedded Systems	25	75	4	--	4
Laboratory I	Power Systems Lab-I	25	75	--	4	2
Seminar I	Seminar-I	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Power System Dynamics	25	75	4	--	4
Core Course V	Flexible AC Transmission Systems (FACTS)	25	75	4	--	4
Core Course VI	Power System Operation and Deregulation	25	75	4	--	4
Core Elective III	1. Gas Insulated Systems(GIS) 2. Programmable Logic Controllers and their applications 3. High frequency magnetic components	25	75	4	--	4
Core Elective IV	1. Reactive Power Compensation and Management 2. Power System Reliability 3. Voltage Stability	25	75	4	--	4
Open Elective II	1. Instrumentation & Control 2. Intelligent Control 3. Smart grid technologies 4. AI Techniques in Electrical Engineering 5. Reliability Engineering 6. Energy Auditing, Conservation & Management	25	75	4	--	4
Laboratory II	Power Systems Lab-II	25	75	--	4	2
Seminar II	Seminar-II	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C
Comprehensive Viva-Voce	--	100	--	--	4
Project work Review I	50	--	--	24	12
Total Credits			--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C
Project work Review II	50	--	--	8	4
Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits			--	24	16

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD



M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

ADVANCED POWER SYSTEM ANALYSIS (Core Course I)

Prerequisite: Computer Methods in Power Systems

Course Objectives:

- To analyze a Power System Network using graph theory.
- To interpret the formation of Network matrices.
- To construct the necessity of load flow studies and various methods of Analysis.
- To examine short circuit analysis using Z_{BUS} .

Course Outcomes:

Upon the completion of the subject, the student will be able to

- Remember proper mathematical models for analysis.
- Conclude methodologies of load flow studies for the power network.
- Apply contingency Analysis.
- Analyze power system studies.

UNIT-I:

Admittance Model and Network Calculations, Branch and Node Admittances, Mutually Coupled Branches in Y_{BUS} , An Equivalent Admittance Network, Modification of Y_{BUS} , Network Incidence Matrix and Y_{BUS} , Method of Successive Elimination, Node Elimination, Triangular Factorization, Sparsity and Near Optimal Ordering.

UNIT-II:

Impedance Model and Network Calculations, the BUS Admittance and Impedance Matrices, Thevenin's Theorem and Z_{BUS} , Algorithms for building Z_{BUS} , Modification of existing Z_{BUS} , Calculation of Z_{BUS} elements from Y_{BUS} , Power Invariant Transformations, Mutually Coupled Branches in Z_{BUS} .

UNIT-III:

Gauss Seidel method, N-R Method, Decoupled method, fast decoupled method, comparison between power flow solutions. DC load flow.

UNIT-IV:

Z_{BUS} Method in Contingency Analysis, Adding and Removing Multiple Lines, Piecewise Solution of Interconnected Systems, Analysis of Single Contingencies, Analysis of Multiple Contingencies, Contingency Analysis of DC Model, System Reduction for Contingency and Fault Studies.

UNIT-V:

Fault Analysis: Symmetrical faults-Fault calculations using Z_{BUS} - Fault calculations using Z_{BUS} equivalent circuits –Selection of circuit breakers- Unsymmetrical faults-Problems on various types of faults.

TEXT BOOKS:

1. John J.Grainger and W.D. Stevenson, "Power System Analysis"- T.M.H.Edition.
2. Modern Power System Analysis – by I.J.Nagrath & D.P.Kothari Tata M Graw – Hi Publishing Company Ltd, 2nd edition.

REFERENCE:

1. Power System Analysis and Design by J.Duncan Glover and M.S.Sarma., cengage 3rd Edition.
2. Olle. L.Elgard, "Electrical Energy Systems Theory"-T.M.H.Edition.
3. Power systems stability and control, Prabha Kundur, The Mc Graw – Hill companies.
4. Power System Operation and Control, Dr. K. Uma Rao, Wiley India Pvt. Ltd.
5. Operation and Control in Power Systems, PSR Murthy, Bs Publications.
6. Power System Operation, Robert H. Miller, James H. Malinowski, The Mc Graw – Hill companies.
7. Power Systems Analysis, operation and control by Abhijit Chakrabarti, Sunitha Halder, PHI 3/e, 2010



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

ADVANCED POWER SYSTEM PROTECTION
(Core Course II)

Prerequisite: Switch Gear and Protection

Course Objectives:

- To distinguish all kinds of circuit breakers and relays for protection of Generators, Transformers and feeder bus bars from Over voltages and other hazards.
- To generalize neutral grounding for overall protection.
- To illustrate the phenomenon of Over Voltages and its classification.

Course Outcomes:

Upon the completion of the subject, the student will be able to

- Understand the basic function of a circuit breaker, all kinds of circuit breakers and differentiate fuse and circuit breakers under fault condition.
- Describe the necessity for the protection of alternators, transformers and feeder bus bars from over voltages and other hazards
- Illustrate neutral grounding, and how over voltages can be generated and how system can be protected against lightning and switching transient over voltages with various protective means
- Identify operation and control of microprocessor based relays.

UNIT-I:

Static Relays: Advantages of static relays-Basic construction of static relays-Level detectors-Replica impedance –Mixing circuits-General equation for two input phase and amplitude comparators-Duality between amplitude and phase comparators.

Amplitude Comparators: Circulating current type and opposed voltage type- rectifier bridge comparators, Direct and Instantaneous comparators.

UNIT-II:

Phase Comparators: Coincidence circuit type- block spike phase comparator, techniques to measure the period of coincidence-Integrating type-Rectifier and Vector product type- Phase comparators.

Static Over Current Relays: Instantaneous over-current relay-Time over-current relays-basic principles –definite time and Inverse definite time over-current relays.

UNIT-III:

Static Differential Relays: Analysis of Static Differential Relays –Static Relay schemes –Duo bias transformer differential protection –Harmonic restraint relay.

Static Distance Relays: Static impedance-reactance-MHO and angle impedance relay-sampling comparator –realization of reactance and MHO relay using sampling comparator.

UNIT-IV:

Multi-Input Comparators: Conic section characteristics-Three input amplitude comparator –Hybrid comparator-switched distance schemes –Poly phase distance schemes- phase fault scheme –three phase scheme – combined and ground fault scheme.

Power Swings: Effect of power swings on the performance of distance relays –Power swing analysis-Principle of out of step tripping and blocking relays-effect of line and length and source impedance on distance relays.

UNIT-V:

Microprocessor based Protective Relays: (Block diagram and flowchart approach only)-Over current relays-impedance relays-directional relay-reactance relay .Generalized mathematical expressions for distance relays-measurement of resistance and reactance –MHO and offset MHO relays-Realization of MHO characteristics- Realization of offset MHO characteristics -Basic principle of Digital computer relaying.

TEXT BOOKS:

1. Badri Ram and D.N.Vishwakarma, "Power system protection and Switch gear ", TMH publication New Delhi 1995.



2. T.S.Madhava Rao , “Static relays”, TMH publication, second edition 1989.

REFERENCE:

1. Protection and Switchgear, Bhavesh Bhalja, R. P. Mahesheari, Nilesh G. Chothani, Oxford University Press.
2. Electrical Power System Protection, C. Christopoulos and A. Wright, Springer International.

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

MODERN CONTROL THEORY
(Core Course III)

Prerequisite: Control Systems

Course Objectives

- To explain the concepts of basic and modern control system for the real time analysis and design of control systems.
- To Explain and apply concepts of state variables analysis.
- To study and analyze non linear systems.
- To analyze the concept of stability of nonlinear systems and categorization.
- To apply the comprehensive knowledge of optimal theory for Control Systems.

Course Outcomes

Upon completion of this course, students should be able to:

- Apply the knowledge of basic and modern control system for the real time analysis and design of control systems.
- Understand the concepts of state variables analysis.
- Analyze the concept of stability of nonlinear systems and optimal control.

UNIT-I:

Mathematical Preliminaries: Fields, Vectors and Vector Spaces – Linear combinations and Bases – Linear Transformations and Matrices – Scalar Product and Norms – Eigen-values, Eigen Vectors and a Canonical form representation of Linear operators – The concept of state – State Equations for Dynamic systems – Time invariance and Linearity – Non-uniqueness of state model – State diagrams for Continuous-Time State models.

UNIT-II:

State Variable Analysis: Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and its properties. General concept of controllability – General concept of Observability – Controllability tests for Continuous-Time Invariant Systems – Observability tests for Continuous-Time Invariant Systems – Controllability and Observability of State Model in Jordan Canonical form – Controllability and Observability Canonical forms of State model.

UNIT-III:

Non Linear Systems: Introduction – Non Linear Systems - Types of Non-Linearities – Saturation – Dead-Zone - Backlash – Jump Phenomenon etc;– Singular Points – Introduction to Linearization of nonlinear systems, Properties of Non-Linear systems – Describing function–describing function analysis of nonlinear systems – Stability analysis of Non-Linear systems through describing functions. Introduction to phase-plane analysis, Method of Isoclines for Constructing Trajectories, singular points, phase-plane analysis of nonlinear control systems.

UNIT-IV:

Stability Analysis: Stability in the sense of Lyapunov, Lyapunov's stability and Lyapunov's instability theorems - Stability Analysis of the Linear continuous time invariant systems by Lyapunov second method – Generation of Lyapunov functions – Variable gradient method – Krasovskii's method. State feedback controller design through Pole Assignment – State observers: Full order and Reduced order.

UNIT-V:

Optimal Control: Introduction to optimal control - Formulation of optimal control problems – calculus of variations – fundamental concepts, functional, variation of functional – fundamental theorem of theorem of Calculus of variations – boundary conditions – constrained minimization – formulation using Hamiltonian method – Linear Quadratic regulator.



TEXT BOOKS:

1. modern control system theory by m.gopal – new age international -1984
2. Control System Engineering, Nagrath and Gopal - New Age International – Fourth Edition

REFERENCES:

1. Optimal control by Kirck , Dover Publications
2. Advanced Control Theory A. Nagoor Kani, RBA Publications, 1999
3. Modern Control Engineering by Ogata.K – Prentice Hall - 1997

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

EHV AC TRANSMISSION
(Core Elective- I)

Prerequisite: Power Systems -II

Course objectives:

- To identify the different aspects of Extra High Voltage A.C and D.C Transmission design and Analysis
- To understand the importance of modern developments of E.H.V and U.H.V transmission systems.
- To demonstrate EHV ac transmission system components, protection and insulation level for over voltages.

Course Outcomes:

Upon the completion of the subject, the student will be able to

- List the necessity of EHV AC transmission, choice of voltage for transmission, line losses and power handling capability.
- Estimate the Statistical procedures for line designs, scientific and engineering principles in power systems.

Construct commercial transmission system

UNIT- I:

E.H.V.A.C. Transmission line trends and preliminary aspect standard transmission voltages – Estimation at line and ground parameters-Bundle conductor systems-Inductance and Capacitance of E.H.V. lines – positive, negative and zero sequence impedance – Line Parameters for Modes of Propagation.

UNIT- II:

Electrostatic field and voltage gradients – calculations of electrostatic field of AC lines – effect of high electrostatic field on biological organisms and human beings - surface voltage gradients and maximum gradients of actual transmission lines – voltage gradients on sub conductor.

UNIT- III:

Electrostatic induction in unenergized lines – measurement of field and voltage gradients for three phase single and double circuit lines – un energized lines. Power Frequency Voltage control and over-voltages in EHV lines: No load voltage – charging currents at power frequency-voltage control – shunt and series compensation – static VAR compensation.

UNIT - IV:

Corona in E.H.V. lines – Corona loss formulae- attention of traveling waves due to Corona – Audio noise due to Corona, its generation, characteristic and limits. Measurements of audio noise radio interference due to Corona - properties of radio noise – frequency spectrum of RI fields – Measurements of RI and RIV.

UNIT- V:

Design of EHV lines based on steady state and transient limits - EHV cables and their characteristics.

TEXT BOOKS:

1. R. D. Begamudre, "EHVAC Transmission Engineering", New Age International (p) Ltd. 3rd Edition.
2. K.R. Padiyar, "HVDC Power Transmission Systems" New Age International (p) Ltd. 2nd revised Edition, 2012.

REFERENCES:

1. S. Rao "EHVAC and HVDC Transmission Engg. Practice" Khanna publishers.
2. Arrillaga.J " High Voltage Direct Current Transmission" 2nd Edition (London) peter Peregrines, IEE, 1998.
3. Padiyar.K.R, " FACTS Controllers in Power Transmission and Distribution" New Age Int. Publishers, 2007.
4. Hingorani H G and Gyugyi. L " Understanding FACTS-Concepts and Technology of Flexible AC Transmission Systems" New York, IEEE Press, 2000.



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)**

**HIGH VOLTAGE ENGINEERING
(Core Elective I)**

Prerequisite: Power Systems and Electrical & Electronics Instrumentation

Course Objectives:

- To distinguish the Gaseous, liquid and solid dielectric behavior under High Voltage.
- To understand the generation methods of High A.C, DC & Impulse Voltages required for various application.
- To apply the measuring techniques of High A.C., D.C & Impulse voltages and currents.
- To identify the testing techniques for High Voltage Equipment.

Course Outcomes:

Upon the completion of the subject, the student will be able to

- Know conduction and breakdown will occur in gases, liquids and solids dielectrics and different applications of the insulating materials in electrical power apparatus.
- Explain the insulation testing of various components in power systems for different types of voltages, namely power frequency A.C, high frequency, switching or lightning impulses, for which generation of high voltages in laboratories is essential
- Interpret the necessity to measure the voltages and currents accurately, ensuring perfect safety to the personnel and equipment.
- Detect the necessary condition for all the electrical equipment which are capable of withstanding the over voltages which met in service like natural causes lightning or system originated ones switching or power frequency transient voltages.

UNIT- I:

Introduction To High Voltage Engineering: Electric Field Stresses, Gas / Vacuum as Insulator, Liquid Dielectrics, Solids and Composites, Estimation and Control of Electric Stress, Numerical methods for electric field computation, Surge voltages, their distribution and control, Applications of insulating materials in transformers, rotating machines, circuit breakers, cable power capacitors and bushings.

UNIT- II:

Break Down In Dielectric Materials: Gases as insulating media, collision process, Ionization process, Townsend's criteria of breakdown in gases, Paschen's law. Liquid as Insulator, pure and commercial liquids, breakdown in pure and commercial liquids. Intrinsic breakdown, electromechanical breakdown, thermal breakdown, breakdown of solid dielectrics in practice, Breakdown in composite dielectrics, solid dielectrics used in practice.

UNIT-III:

Generation & Measurement of High Voltages & Currents : Generation of High Direct Current Voltages, Generation of High alternating voltages, Generation of Impulse Voltages, Generation of Impulse currents, Tripping and control of impulse generators. Measurement of High Direct Current voltages, Measurement of High Voltages alternating and impulse, Measurement of High Currents- direct, alternating and Impulse, Oscilloscope for impulse voltage and current measurements.

UNIT-IV:

Over Voltages & Insulation Co-Ordination: Natural causes for over voltages – Lightning phenomenon, Overvoltage due to switching surges, system faults and other abnormal conditions, Principles of Insulation Coordination on High voltage and Extra High Voltage power systems.

UNIT- V:

Testing of Materials & Electrical Apparatus: Measurement of D.C Resistivity, Measurement of Dielectric Constant and loss factor, Partial discharge measurements. Testing of Insulators and bushings, Testing of Isolators and circuit breakers, testing of cables, Testing of Transformers, Testing of Surge Arresters, and Radio Interference measurements.

TEXT BOOKS:

1. High Voltage Engineering by M.S.Naidu and V. Kamaraju – TMH Publications, 3rd Edition



2. High Voltage Engineering: Fundamentals by E.Kuffel, W.S.Zaengl, J.Kuffel by Elsevier, 2nd Edition.

REFERENCES:

1. High Voltage Engineering by C.L.Wadhwa, New Age International (P) Limited, 1997.
2. High Voltage Insulation Engineering by Ravindra Arora, Wolfgang Mosch, New Age International (P) Limited, 1995.
3. High Voltage Engineering, Theory and Practice by Mazen Abdel Salam, Hussein Anis, Ahdan El-Morshedy, Roshdy Radwan, Marcel Dekker

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (PE/PEED/PID)

ADVANCED DIGITAL SIGNAL PROCESSING
(Core Elective-I)

Prerequisite: Digital Signal Processing

Course Learning Objectives

- To Comprehend characteristics of discrete time signals and systems
- To analyze and process signals using various transform techniques
- To identify various factors involved in design of digital filters
- To illustrate the effects of finite word length implementation.

Course Outcomes

- Analyze and process signals in the discrete domain
- Design filters to suit specific requirements for specific applications
- Perform statistical analysis and inferences on various types of signals
- Design multi rate signal processing of signals through systems.

Analyze binary fixed point and floating-point representation of numbers and arithmetic operations

UNIT-I:

Digital Filter Structures: Block diagram representation – Equivalent Structures – FIR and IIR digital filter Structures All pass Filters-tunable IIR Digital Sine-cosine generator- Computational complexity of digital filter structures.

UNIT-II:

Digital Filter Design :

Preliminary considerations- Bilinear transformation method of IIR filter design –design of Low pass high-pass – Band-pass, and Band stop- IIR digital filters – Spectral transformations of IIR filters – FIR filter design –based on Windowed Fourier series – design of FIR digital filters with least – mean square-error – constrained Least –square design of FIR digital filters.

UNIT-III:

DSP Algorithm Implementation : Computation of the discrete Fourier transform- Number representation – Arithmetic operations – handling of overflow – Tunable digital filters – function approximation.

UNIT-IV:

Analysis Of Finite Word Length Effects: The Quantization process and errors-Quantization of fixed –point and floating –point Numbers – Analysis of coefficient Quantization effects – Analysis of Arithmetic Round-off errors- Dynamic range scaling – signal –to- noise in Low –order IIR filters- Low – Sensitivity Digital filter – Reduction of Product round-off errors feedback – Limit cycles in IIR digital filter – Round – off errors in FFT Algorithms.

UNIT-V:

Power Spectrum Estimation : Estimation of spectra from Finite Duration Observations signals- Non-parametric methods for power spectrum Estimation- parametric method for power spectrum Estimation- Estimation of spectral form-Finite duration observation of signals- Non-parametric methods for power spectrum estimation – Walsh methods – Blackman and torchy method.

TEXT BOOKS:

1. Digital Signal Processing principles –algorithms and Applications- John G. Proakis –PHI – 3rd edition 2002.
2. Digital Time Signal Processing: Alan V.Oppenheim,Ronald W ,Shafer – PHI 1996 1st Edition reprint
3. Advanced Digital Signal Processing – Theory and Applications – Glenn Zelniker, Fred J. Taiylor.

REFERENCE BOOKS

1. Digital Signal Processing – S Salivahanan . A Vallavaraj C. Gnanapriya –TMH – 2nd reprint 2001.



2. Digital Signal Processing – sanjit K.Mitra – TMH second edition.
- 3 Theory and Applications of Digital Signal Processing – Lourens R RebinarandBernold.
4. Digital Filter Analysis and Design Auntoniam - TMH
5. Digital Signal Processing – J.S.Chitode – First Edition, 2008, Technical Publications.

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

POWER QUALITY
(Core Elective II)

Prerequisite: Power Systems and Power Electronics

Course Objectives

- To know different terms of power quality.
- To Illustrate of voltage power quality issue - short and long interruption
- To construct study of characterization of voltage sag magnitude and three phase unbalanced voltage sag.
- To know the behavior of power electronics loads; induction motors, synchronous motor etc by the power quality issues
- To prepare mitigation of power quality issues by the VSI converters.

Course Outcomes:

Upon the completion of the subject, the student will be able to

- Know the severity of power quality problems in distribution system;
- Understand the concept of voltage sag transformation from up-stream (higher voltages) to down-stream (lower voltage)
- compute the concept of improving the power quality to sensitive load by various mitigating custom power devices

UNIT-I:

Introduction : Introduction of the Power Quality (PQ) problem, Terms used in PQ: Voltage, Sag, Swell, Surges, Harmonics, over voltages, spikes, Voltage fluctuations, Transients, Interruption, overview of power quality phenomenon, Remedies to improve power quality, power quality monitoring.

UNIT-II:

Long & Short Interruptions: Interruptions – Definition – Difference between failures, outage, Interruptions – causes of Long Interruptions – Origin of Interruptions – Limits for the Interruption frequency – Limits for the interruption duration – costs of Interruption – Overview of Reliability evaluation to power quality, comparison of observations and reliability evaluation.

Short interruptions: definition, origin of short interruptions, basic principle, fuse saving, voltage magnitude events due to re-closing, voltage during the interruption, monitoring of short interruptions, difference between medium and low voltage systems. Multiple events, single phase tripping – voltage and current during fault period, voltage and current at post fault period, stochastic prediction of short interruptions.

UNIT III:

1 & 3-Phase Voltage SAG Characterization : Voltage sag – definition, causes of voltage sag, voltage sag magnitude, and monitoring, theoretical calculation of voltage sag magnitude, voltage sag calculation in non-radial systems, meshed systems, and voltage sag duration.

Three phase faults, phase angle jumps, magnitude and phase angle jumps for three phase unbalanced sags, load influence on voltage sags.

UNIT-IV:

Power Quality Considerations in Industrial Power Systems: Voltage sag – equipment behavior of Power electronic loads, induction motors, synchronous motors, computers, consumer electronics, adjustable speed AC drives and its operation. Mitigation of AC Drives, adjustable speed DC drives and its operation, mitigation methods of DC drives.

UNIT-V:

Mitigation of Interruptions & Voltage Sags: Overview of mitigation methods – from fault to trip, reducing the number of faults, reducing the fault clearing time changing the power system, installing mitigation equipment, improving equipment immunity, different events and mitigation methods. System equipment interface – voltage source converter, series voltage controller, shunt controller, combined shunt and series controller.



Power Quality and EMC Standards: Introduction to standardization, IEC Electromagnetic compatibility standards, European voltage characteristics standards, PQ surveys.

TEXTBOOKS:

1. Math H J Bollen "Understanding Power Quality Problems", IEEE Press.
2. R.C. Dugan, M.F. McGranaghan and H.W. Beaty, "Electric Power Systems Quality." New York: McGraw-Hill.1996

REFERENCES:

1. G.T. Heydt, 'Electric Power Quality', 2nd Edition. (West Lafayette, IN, Stars in a Circle Publications, 1994).
2. Power Quality VAR Compensation in Power Systems, R. SastryVedamMulukutla S. Sarma,CRC Press.
3. A Ghosh, G. Ledwich, Power Quality Enhancement Using Custom Power Devices. Kluwer Academic, 2002



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)**

**MICROCONTROLLERS AND APPLICATIONS
(Core Elective II)**

Prerequisite: Microprocessors and Interfacing Devices

Course Objectives:

- To relate the basic architecture and addressing modes of a microcontroller.
- To summarize the principles of top down design to microcontroller software development
- To demonstrate assembly language programs for the advanced Microcontroller , assembly language code for high-level language structures such as IF-THEN-ELSE and DO-WHILE
- To analyze a typical I/O interface and to discuss timing issues
- To identify different types of memory used in microcontroller systems

Course Outcomes:

Upon the completion of the subject, the student will be able to

- Distinguish Types of computers & microcontrollers,
- Generalize 8-Bit, 16- Bit & 32 Bit advanced Microcontrollers.
- Construct Real time Applications of Microcontrollers.
- Demonstrate RTOS for Microcontrollers.
- Translate Hardware applications using Microcontrollers.

UNIT-I:

Overview of Architecture & Microcontroller Resources: Architecture of a microcontroller – Microcontroller resources – Resources in advanced and next generation microcontrollers – 8051 microcontroller – Internal and External memories – Counters and Timers – Synchronous serial-cum asynchronous serial communication - Interrupts.

UNIT-II:

8051- Microcontrollers Instruction Set : Basic assembly language programming – Data transfer instructions – Data and Bit-manipulation instructions – Arithmetic instructions – Instructions for Logical operations on the test among the Registers, Internal RAM, and SFRs – Program flow control instructions – Interrupt control flow.

UNIT-III:

Real Time Control: Interrupts: Interrupt handling structure of an MCU – Interrupt Latency and Interrupt deadline – Multiple sources of the interrupts – Non-maskable interrupt sources – Enabling or disabling of the sources – Polling to determine the interrupt source and assignment of the priorities among them – Interrupt structure in Intel 8051.

Timers: Programmable Timers in the MCU's – Free running counter and real time control – Interrupt interval and density constraints.

UNIT-IV:

Systems Design: Digital and Analog Interfacing Methods: Switch, Keypad and Keyboard interfacing – LED and Array of LEDs – Keyboard-cum-Display controller (8279) – Alphanumeric Devices – Display Systems and its interfaces – Printer interfaces – Programmable instruments interface using IEEE 488 Bus – Interfacing with the Flash Memory – Interfaces – Interfacing to High Power Devices – Analog input interfacing – Analog output interfacing – Optical motor shaft encoders – Industrial control – Industrial process control system – Prototype MCU based Measuring instruments – Robotics and Embedded control – Digital Signal Processing and digital filters.

UNIT-V:

Real Time Operating System for Microcontrollers: Real Time operating system – RTOS of Keil (RTX51) – Use of RTOS in Design – Software development tools for Microcontrollers.

16-Bit Microcontrollers: Hardware – Memory map in Intel 80196 family MCU system – IO ports – Programmable Timers and High-speed outputs and input captures – Interrupts – instructions. ARM 32 Bit MCUs: Introduction to 16/32 Bit processors – ARM architecture and organization – ARM / Thumb programming model – ARM / Thumb instruction set – Development tools.



TEXT BOOKS:

1. Raj Kamal," Microcontrollers Architecture, Programming, Interfacing and System Design"– Pearson Education, 2005.
2. Mazidi and Mazidi, "The 8051 Microcontroller and Embedded Systems" – PHI, 2000.

REFERENCES:

1. A.V. Deshmuk, "Microcontrollers (Theory & Applications)" – WTMH, 2005.
2. John B. Peatman, "Design with PIC Microcontrollers" – Pearson Education, 2005.
3. Microcontroller Programming, Julio Sanchez, Maria P. Canton, CRC Press.
4. The 8051 Microcontroller, Ayala, Cengage Learning.
5. Microprocessors and Microcontrollers, Architecture, Programming and System Design, Krishna Kant, PHI Learning PVT. Ltd.
6. Microprocessors, Nilesh B. Bahadure, PHI Learning PVT. Ltd.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

DISTRIBUTION AUTOMATION
(Core Elective II)

Prerequisite: Electrical Distribution Systems

Course objectives:

- To list the distribution systems for load modeling
- To understand the design & working of substations.
- To compute system protection
- To give a comprehensive idea on communication systems.

Course Outcomes:

Upon the completion of the subject, the student will be able to

- Find the transfer of electrical data in distribution system through Digital Communication.
- Predict load forecasting and reliability in economic point of view
- Apply Distribution Automation objectives and SCADA
- To have a knowledge on management of different electrical parameters.

UNIT-I:

Distribution Automation and The Utility System: Introduction to Distribution Automation (DA), control system interfaces, control and data requirements, centralized (Vs) decentralized control, DA System (DAS), DA Hardware, DAS software.

UNIT-II:

Distribution Automation Functions : DA capabilities, Automation system computer facilities, management processes, Information management, system reliability management, system efficiency management, voltage management, Load management.

UNIT-III:

Communication Systems for DA: DA communication requirements, Communication reliability, Cost effectiveness, Data rate Requirements, Two way capability, Ability to communicate during outages and faults, Ease of operation and maintenance, Conforming to the architecture of data flow

Communication systems used in DA : Distribution line carrier (Power line carrier), Ripple control, Zero crossing technique, telephone, cable TV, Radio, AM broadcast, FM SCA, VHF Radio, UHF Radio, Microwave satellite. Fiber optics, Hybrid Communication systems, Communication systems used in field tests.

UNIT-IV:

Technical Benefits: DA benefit categories, Capital deferred savings, Operation and Maintenance savings, Interruption related savings, Customer related savings, Operational savings, improved operation, Function benefits, Potential benefits for functions, and function shared benefits, Guidelines for formulation of estimating equations Parameters required, economic impact areas, Resources for determining benefits impact on distribution system, integration of benefits into economic evaluation.

UNIT-V:

Economic Evaluation Methods: Development and evaluation of alternate plans, Select study area, Select study period, Project load growth, Develop Alternatives, Calculate operating and maintenance costs, Evaluate alternatives. Economic comparison of alternate plans, Classification of expenses and capital expenditures, Comparison of revenue requirements of alternative plans, Book Life and Continuing plant analysis, Year by year revenue requirement analysis, short term analysis, end of study adjustment, Break even analysis, Sensitivity analysis computational aids.

TEXT BOOKS:

1. Control and Automation of Electrical Distribution Systems, James. Northcote – Green Robert Wilson, CRC Press.
2. Electric Power Distribution Automation, Dr. M. K. Khedkar, Dr. G.M.Dhole, University Science press.

REFERENCES:

1. IEEE Tutorial Course “Distribution Automation”
2. IEEE Working Group on “Distribution Automation”



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

OPTIMIZATION TECHNIQUES
(Open Elective I)

Course Objectives

- To understand the theory of optimization methods and algorithms developed for solving various types of optimization problems.
- To develop an interest in applying optimization techniques in problems of Engineering and Technology
- To apply the mathematical results and numerical techniques of optimization theory to concrete Engineering problems.

Course Outcomes

Upon the completion of the subject, the student will be able to

- Know basic theoretical principles in optimization
- formulation of optimization models
- solution methods in optimization;
- methods of sensitivity analysis and post processing of results
- applications to a wide range of engineering problems

UNIT – I

Introduction and Classical Optimization Techniques: Statement of an Optimization problem – design vector – design constraints – constraint surface – objective function – objective function surfaces – classification of Optimization problems.

Classical Optimization Techniques: Single variable Optimization – multi variable Optimization without constraints – necessary and sufficient conditions for minimum/maximum – multivariable Optimization with equality constraints.

Solution by method of Lagrange multipliers – multivariable Optimization with inequality constraints – Kuhn – Tucker conditions.

UNIT – II

Linear Programming: Standard form of a linear programming problem – geometry of linear programming problems – definitions and theorems – solution of a system of linear simultaneous equations – pivotal reduction of a general system of equations – motivation to the simplex method – simplex algorithm.

UNIT – III

Transportation Problem: Finding initial basic feasible solution by north – west corner rule, least cost method and Vogel's approximation method – testing for optimality of balanced transportation problems.

Unconstrained Nonlinear Programming: One – dimensional minimization methods: Classification, Fibonacci method and Quadratic interpolation method

UNIT – IV

Unconstrained Optimization Techniques: Univariate method, Powell's method and steepest descent method.

Constrained Nonlinear Programming: Characteristics of a constrained problem, Classification, Basic approach of Penalty Function method; Basic approach of Penalty Function method; Basic approaches of Interior and Exterior penalty function methods. Introduction to convex Programming Problem.

UNIT – V

Dynamic Programming: Dynamic programming multistage decision processes – types – concept of sub optimization and the principle of optimality – computational procedure in dynamic programming – examples illustrating the calculus method of solution - examples illustrating the tabular method of solution.

TEXT BOOKS:

1. "Engineering optimization: Theory and practice"-by S. S.Rao, New Age International (P) Limited, 3rd edition, 1998.
2. "Introductory Operations Research" by H.S. Kasene & K.D. Kumar, Springer(India), Pvt .LTd.



REFERENCE BOOKS:

1. “ Optimization Methods in Operations Research and systems Analysis” – by K.V. Mital and C. Mohan, New Age International (P) Limited, Publishers, 3rd edition, 1996.
2. Operations Research – by Dr. S.D.Sharma.
3. “Operations Research : An Introduction” – by H.A. Taha, PHI Pvt. Ltd., 6th edition
4. Linear Programming – by G. Hadley

University Updates



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

DIGITAL CONTROL SYSTEMS
(Open Elective I)

Prerequisite: Control Systems

Course Objectives

- To explain basic and digital control system for the real time analysis and design of control systems.
- To apply the knowledge state variable analysis in the design of discrete systems.
 - To explain the concept of stability analysis and design of discrete time systems.

Course Outcomes

Upon the completion of the subject, the student will be able to

- To illustrate the concepts of Digital control systems.
- Analysis and design of discrete systems in state variable analysis.
- To relate the concepts of stability analysis and design of discrete time systems.

UNIT – I:

Concept & Representation of Discrete time Systems: Block Diagram of typical control system- advantages of sampling in control systems – examples of discrete data and digital systems – data conversion and quantization – sample and hold devices – D/A and A/D conversion – sampling theorem – reconstruction of sampled signals.

Z-transform: Definition of Z-transforms – mapping between s-plane and z-plane –inverse z- transform – properties of z-transforms - ROC of z-transforms –pulse transfer function –relation between $G(s)$ and $G(z)$ – signal flow graph method applied to digital control systems.

UNIT- II:

State Space Analysis: State space modeling of discrete time systems – state transition equation of discrete time invariant systems – solution of time invariant discrete state equations: recursive method and the Z-Transformation method – conversion of pulse transfer function to the state model & vice-versa – Eigen values – Eigen vectors of discrete time system-matrix (A) – Realization of pulse transformation in state space form, discretization of continuous time systems, Computation of state transition matrix and its properties. Response of sample data system between sampling instants.

UNIT – III :

Controllability, Observability & Stability Tests: Concept of controllability, stabilizability, observability and reachability - Controllability and observability tests, Transformation of discrete time systems into controllable and observable forms.

Stability: Definition of stability – stability tests – The second method of Liapunov.

UNIT- IV:

Design of Discrete Time Controllers and Observers: Design of discrete time controller with bilinear transformation – Realization of digital PID controller-Design of deadbeat controller; Pole placement through state feedback.

UNIT-V:

State Observers: Design of - Full order and reduced order observers. Study of observer based control design

TEXT BOOKS:

1. K. Ogata , Discrete-Time Control systems, Pearson Education/PHI, 2nd Edition.
2. V. I. George, C. P. Kurian, Digital Control Systems, Cengage Learning.
3. M.Gopal, Digital Control Engineering, New Age Int. Pvt. Ltd., 2014

REFERENCE BOOKS:

1. Kuo, Digital Control Systems, Oxford University Press, 2nd Edition, 2003.
2. M.Gopal , Digital Control and State Variable Methods, TMH.
3. M. Sami Fadali Antonio Visioli, Digital Control Engineering Analysis and Design, Academic Press



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

RENEWABLE ENERGY SYSTEMS
(Open Elective I)

Prerequisite: No Prerequisite

Course Objectives:

- To recognize the awareness of energy conservation in students
- To identify the use of renewable energy sources for electrical power generation
- To collect different energy storage methods
- To detect about environmental effects of energy conversion

Course Outcomes:

Upon the completion of the subject, the student will be able to

- find different renewable energy sources to produce electrical power
- estimate the use of conventional energy sources to produce electrical energy
- role-play the fact that the conventional energy resources are depleted
- arrange Store energy and to avoid the environmental pollution

UNIT-I:

Photo voltaic power generation ,spectral distribution of energy in solar radiation, solar cell configurations, voltage developed by solar cell, photo current and load current, practical solar cell performance, commercial photo voltaic systems, test specifications for PV systems, applications of super conducting materials in electrical equipment systems.

UNIT-II:

Principles of MHD power generation, ideal MHD generator performance, practical MHD generator, MHD technology.

Wind Energy conversion: Power from wind, properties of air and wind, types of wind Turbines, operating characteristics.

UNIT-III:

Tides and tidal power stations, modes of operation, tidal project examples, turbines and generators for tidal power generation.

Wave energy conversion: properties of waves and power content, vertex motion of Waves, device applications. Types of ocean thermal energy conversion systems Application of OTEC systems examples,

UNIT-IV:

Miscellaneous energy conversion systems: coal gasification and liquefaction, biomass conversion, geothermal energy, thermo electric energy conversion, principles of EMF generation, description of fuel cells, Co-generation and energy storage, combined cycle co-generation, energy storage.

Global energy position and environmental effects: energy units, global energy position.

UNIT-V:

Types of fuel cells, H₂-O₂ Fuel cells, Application of fuel cells – Batteries, Description of batteries, Battery application for large power. Environmental effects of energy conversion systems, pollution from coal and preventive measures steam stations and pollution, pollution free energy systems.

TEXT BOOKS:

1. "Energy conversion systems" by Rakosh das Begamudre, New age International publishers, New Delhi - 2000.
2. "Renewable Energy Resources" by John Twidell and Tony Weir, 2nd Edition, Fspan & Co.

REFERENCES:

1. "Understanding Renewable Energy Systems" by Volker Quaschnig, 2005, UK.
2. "Renewable Energy Systems-Advanced Conversion, Technologies & Applications" by Faner Lin Luo Honer Ye, CRC press, Taylor & Francies group.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

HVDC TRANSMISSION
(Open Elective I)

Prerequisite: Power Systems-II

Course Objectives:

- To Comprehend the conversion principles of HVDC Transmission
- Analysis of 3,6 , 12 pulse converters, rectifier and inverter operations of HVDC converters
- To identify the different types of Harmonics and reduction by using Filters
- To Comprehend Interaction between HVAC and DC systems in various aspects
- To appreciate the reliable MTDC systems and protection of HVDC system

Course Outcomes:

Upon the completion of the subject, the student will be able to

- To find the applications of HVDC transmission in the power system with the acquired knowledge.
- To analyze different converter topologies viz. 3,6 and 12 Pulse converters and understand it's control aspects.
- To understand the filter configuration for Harmonics in HVDC systems.
- To appreciate the reliable Multi terminal HVDC system.
- To have knowledge on the Protection of HVDC systems against Transient over voltages and over currents.

UNIT-I:

Introduction: General consideration, Power Handling Capabilities of HVDC Lines Basic Conversion principles, static converter configuration.

UNIT-II:

Static Power Converters: 3-pulse, 6-pulse, and 12-pulse converters, converter station and Terminal equipment, commutation process, Rectifier and inverter operation, equivalent circuit for converter – special features of converter transformers. Harmonics in HVDC Systems, Harmonic elimination, AC and DC filters.

UNIT-III:

Control of HVDC Converters and Systems: Constant current, constant extinction angle and constant ignition angle control Individual phase control and equidistant firing angle control DC power flow control. Interaction between HV AC and DC systems – Voltage interaction Harmonic instability problems and DC power modulation.

UNIT-IV:

MTDC Systems & Over Voltages: Series parallel and series parallel systems their operation and control. Over voltages due to disturbances on DC side, over voltages due to DC and AC side line faults.

UNIT-V:

Converter Faults & Protection: Converter faults, over current protection – valve group, and DC line protection over voltage protection of converters, surge arresters.

TEXT BOOKS:

1. E.W. Kimbark: Direct current Transmission, Wiley Inter Science – New York
2. KR Padiyar : High Voltage Direct current Transmission Wiley Esatern Ltd New Delhi – 1992.

REFERENCES:

- J. Arillaga HVDC Transmission Peter Peregrinus Ltd. London UK 1983
1. E. Uhlman : Power Transmission by Direct Current , Springer Verlag, Berlin Helberg. 1985.
 2. S. Rao "EHVAC and HVDC Transmission Engg. Practice" Khanna publishers.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

ANALYSIS OF POWER ELECTRONIC CONVERTERS
(Open Elective I)

Prerequisite: Power Electronics

Course Objectives

- To comprehend the concepts of converters
- Students will be able to relate to the applications of phase controlled rectifiers
- Students will be able to describe the importance of AC voltage controllers and cyclo converters for various industrial applications
- Students will be able to analyze and design switch mode power electronic converters for various applications including microprocessor power supplies, renewable energy systems, and motor drives.
- Students will be able to analyze pulse width modulated inverters which are used in variable speed drives

Course Outcomes

- Students will have good understanding of the basic principles of switch mode power conversion
- Students will understand the operating principles and models of different types of power electronic converters including dc-dc converters, PWM rectifiers and inverters
- Students will be able to choose appropriate power converter topologies and design the power stage and feedback controllers for various applications
- Students will be able to use power electronic simulation packages for analyzing and designing power converters

Unit I

Single Phase AC Voltage Controllers: Single phase AC voltage controllers with Resistive, Resistive-inductive and Resistive-inductive-induced e.m.f. loads - ac voltage controllers with PW Control - Effects of source and load inductances - Synchronous tap changers-Applications - numerical problems.

Unit II

Three Phase AC Voltage Controllers: Three phase AC voltage controllers - Analysis of controllers with star and delta Connected Resistive, Resistive-inductive loads - Effects of source and load Inductances - applications - numerical problems.

Cycloconverters: Single phase to single phase cycloconverters - analysis of midpoint and bridge Configurations - Three phase to three phase cycloconverters - analysis of Midpoint and bridge configurations - Limitations - Advantages - Applications- numerical problems.

Unit III

Single Phase Converters: Single phase converters - Half controlled and Fully controlled converters -Evaluation of input power factor and harmonic factor - continuous and Discontinuous load current - single phase dual converters - power factor Improvements - Extinction angle control - symmetrical angle control - PWM -single phase sinusoidal PWM - single phase series converters - Applications -Numerical problems.

Three Phase Converters: Three phase converters - Half controlled and fully controlled converters - Evaluation of input power factor and harmonic factor - continuous and Discontinuous load current - three phase dual converters - power factor Improvements - three phase PWM - twelve pulse converters - applications -Numerical problems.

Unit VI

D.C. to D.C. Converters: Analysis of step-down and step-up dc to dc converters with resistive and Resistive-inductive loads - Switched mode regulators - Analysis of Buck Regulators - Boost regulators - buck and boost regulators - Cuk regulators - Condition for continuous inductor current and capacitor voltage - comparison of regulators -Multioutput boost converters - advantages - applications - Numerical problems.



Unit V

Pulse Width Modulated Inverters(single phase): Principle of operation - performance parameters - single phase bridge inverter -evaluation of output voltage and current with resistive, inductive and Capacitive loads - Voltage control of single phase inverters - single PWM - Multiple PWM - sinusoidal PWM - modified PWM - phase displacement Control - Advanced modulation techniques for improved performance - Trapezoidal, staircase, stepped, harmonic injection and delta modulation - Advantage - application - numerical problems.

Pulse Width Modulated Inverters(three phase): Three phase inverters - analysis of 180 degree condition for output voltage And current with resistive, inductive loads - analysis of 120 degree Conduction - voltage control of three phase inverters - sinusoidal PWM - Third Harmonic PWM – 60 degree PWM - space vector modulation - Comparison of PWM techniques
- harmonic reductions - Current Source Inverter - variable d.c. link inverter - boost inverter - buck and boost inverter - inverter circuit design - advantages -applications - numerical problems.

TEXT BOOKS:

1. Power Electronics - Mohammed H. Rashid - Pearson Education -Third Edition - First Indian reprint 2004.
2. Power Electronics - Ned Mohan, Tore M. Undeland and William P. Robbins –John Wiley and Sons - Second Edition

REFERENCES:

1. Power Electronics Daniel W. Hart
2. Fundamentals of Power Electronics, 2nd Edition. R.W. Erickson
3. The power electronics Hand Book Timothy, L. Skvarenina, Purdue University



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

EMBEDDED SYSTEMS
(Open Elective I)

Prerequisite: Microprocessors and Interfacing Devices

Course Learning Objectives

- To Comprehend the general embedded system concepts , design of embedded hardware and software development tools
- To explain the basics of real time operating and embedded systems
- To Describe key issues such as CPU scheduling, memory management, task synchronization, and file system in the context of real-time embedded systems.

Course Outcomes:

- To analyze and design embedded systems and real-time systems
- Define the unique design problems and challenges of real-time systems
- Identify the unique characteristics of real-time operating systems and evaluate the need for real-time operating system
- Explain the general structure of a real-time system and Understand and use RTOS to build an embedded real-time system
- Gain knowledge and skills necessary to design and develop embedded applications based on real-time operating systems.

UNIT- I:

Overview of Embedded System: Embedded System, types of Embedded System, Requirements of Embedded System, and Issues in Embedded software development, Applications.

UNIT-II:

Processor & Memory Organization: Structural units in a processor, Processor selection, Memory devices, Memory selection, Memory Allocation & Map, Interfacing.

UNIT-III:

Devices, Device Drivers & Buses for Device Networks: I/O devices, Timer & Counter devices, Serial Communication, Communication between devices using different buses. Device drives, Parallel and serial port device drives in a system, Interrupt servicing mechanism, context and periods for context switching, Deadline and Interrupt Latency.

UNIT-IV:

Programming & Modeling Concepts : Program elements, Modeling Processes for Software Analysis, Programming Models, Modeling of Multiprocessor Systems, Software algorithm Concepts, design, implementation, testing, validating, debugging, Management and maintenance, Necessity of RTOS.

UNIT-V:

Hardware and Software Co-Design: Embedded system design and co design issues in software development, design cycle in development phase for Embedded System, Use of ICE & Software tools for development of ES, Issues in embedded system design.

TEXTBOOK

1. Embedded systems: Architecture, programming and design by Rajkamal, TMH
2. Embedded system design by Arnold S Burger, CMP

REFERENCES

1. An embedded software primer by David Simon, PEA
2. Embedded systems design:Real world design be Steve Heath; Butterworth Heinenann, Newton mass USA 2002
3. Data communication by Hayt.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM LAB – I

1. Develop Program for Y_{BUS} formation.
2. Develop Program for G-S Load Flow Analysis.
3. Develop Program for N-R Load Flow Analysis.
4. Develop Program for FDLF Load Flow Analysis.
5. Develop Program for Short Circuit Analysis.
6. Develop Program for Transient Stability Analysis for Single Machine connected to Infinite Bus by Point by Point Method.
7. Develop Program for Generation System Reliability Analysis.
8. Develop Program for Distribution System Reliability Analysis.
9. Develop Simulation of RLC Circuit
10. Develop Simulation of Single Phase Full Converter with RLE Load
11. Develop Program model for Closed Loop Speed Control of Separately Excited D.C Motor.
12. Develop Program model for Sinusoidal Pulse Width Modulation.

Note: From the above list minimum 10 experiments are to be conducted using suitable software.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. (ELECTRICAL POWER SYSTEMS/POWER ENGINEERING AND ENERGY SYSTEMS/
POWER SYSTEM CONTROL AND AUTOMATION/ ELECTRICAL POWER ENGINEERING)
COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Power System Dynamics	25	75	4	--	4
Core Course V	Flexible AC Transmission Systems (FACTS)	25	75	4	--	4
Core Course VI	Power System Operation and Deregulation	25	75	4	--	4
Core Elective III	1. Gas Insulated Systems(GIS) 2. Programmable Logic Controllers and their applications 3. High frequency magnetic components	25	75	4	--	4
Core Elective IV	1. Reactive Power Compensation and Management 2. Power System Reliability 3. Voltage Stability	25	75	4	--	4
Open Elective II	1. Instrumentation & Control 2. Intelligent Control 3. Smart grid technologies 4. AI Techniques in Electrical Engineering 5. Reliability Engineering 6. Energy Auditing, Conservation & Management	25	75	4	--	4
Laboratory II	Power Systems Lab-II	25	75	--	4	2
Seminar II	Seminar-II	50	--	--	4	2
Total Credits				24	8	28



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM DYNAMICS

Prerequisite: Computer Methods in Power Systems

Course objectives:

- To remember the dynamic characteristics of power system equipment,
- To recognize dynamic performance of power systems
- To illustrate the system stability and controls.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Choose the fundamental dynamic behavior and controls of power systems to perform basic stability analysis.
- Comprehend concepts in modeling and simulating the dynamic phenomena of power systems
- Interpret results of system stability studies
- Analyze theory and practice of modeling main power system components, such as synchronous machines, excitation systems and governors

UNIT-I:

Basic Concepts

Power system stability states of operation and system security - system dynamics - problems system model analysis of steady State stability and transient stability - simplified representation of Excitation control.

UNIT-II:

Modeling of Synchronous Machine:

Synchronous machine - park's Transformation-analysis of steady state performance per - unit quantities-Equivalent circuits of synchronous machine-determination of parameters of equivalent circuits.

UNIT-III:

Excitation System

Excitation system modeling-excitation systems block Diagram - system representation by state equations- Dynamics of a synchronous generator connected to infinite bus - system model Synchronous machine model-stator equations rotor equations - Synchronous machine model with field circuit - one equivalent damper winding on q axis (model 1.1) - calculation of Initial conditions.

UNIT-IV:

Analysis of Single Machine System

Small signal analysis with block diagram - Representation Characteristic equation and application of Routh Hurwitz criterion- synchronizing and damping torque analysis-small signal model - State equations.

UNIT-V:

Application of Power System Stabilizers

Basic concepts in applying PSS - Control signals - Structure and tuning of PSS - Washout circuit - Dynamic compensator analysis of single machine infinite bus system with and without PSS.

TEXT BOOKS:

1. K.R. PADIYAR," Power system dynamics "- B.S. Publications.
2. P.M. Anderson and A.A. Fouad, "Power system control and stability",IEEE Press

REFERENCES:

1. R. Ramanujam, "Power Systems Dynamics"- PHI Publications.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)

Prerequisite: Power Electronics and Power Systems-II

Course Objectives:

- To understand the fundamentals of FACTS Controllers, Importance of controllable parameters and types of FACTS controllers & their benefits
- To recall the objectives of Shunt and Series compensation
- To explain control of STATCOM and SVC and their comparison And the regulation of STATCOM
- To analyze the functioning and control of GCSC, TSSC and TCSC

Course Outcomes: Upon the completion of the subject, the student will be able to

- Choose proper controller for the specific application based on system requirements
- Understand various systems thoroughly and their requirements
- Interpret the control circuits of Shunt Controllers SVC & STATCOM for various functions viz. Transient stability Enhancement, voltage instability prevention and power oscillation damping
- Detect the Power and control circuits of Series Controllers GCSC, TSSC and TCSC

UNIT-I:

Facts Concepts

Transmission interconnections power flow in an AC system, loading capability limits, Dynamic stability considerations, importance of controllable parameters basic types of FACTS controllers, benefits from FACTS controllers.

UNIT-II:

Voltage Source Converters

Single phase three phase full wave bridge converters transformer connections for 12 pulse 24 and 48 pulse operation. Three level voltage source converter, pulse width modulation converter, basic concept of current source Converters, and comparison of current source converters with voltage source converters.

UNIT-III:

Static Shunt Compensation

Objectives of shunt compensation, mid-point voltage regulation voltage instability prevention, improvement of transient stability, Power oscillation damping, Methods of controllable VAR generation, variable impedance type static VAR generators switching converter type VAR generators hybrid VAR generators.

UNIT-IV:

SVC And STATCOM

The regulation and slope transfer function and dynamic performance, transient stability enhancement and power oscillation damping operating point control and summary of compensator control.

UNIT-V:

Static Series Compensators

Concept of series capacitive compensation, improvement of transient stability, power oscillation damping, and functional requirements of GTO thyristor controlled series capacitor (GSC), thyristor switched series capacitor (TSSC), and thyristor controlled series capacitor (TCSC)
Control schemes for GSC TSSC and TCSC.

TEXT BOOKS:

1. Hingorani H G and Gyugyi. L “ Understanding FACTS-Concepts and Technology of Flexible AC Transmission Systems” New York, IEEE Press, 2000.
2. Padiyar.K.R, “ FACTS Controllers in Power Transmission and Distribution” New Age Int. Publishers, 2007

REFERENCES:

1. Zhang, Xiao-Ping, Rehtanz, Christian, Pal, Bikash “Flexible AC Transmission Systems: Modeling and Control”, Springer, 2012
2. Yong-Hua Song, Allan Johns, “Flexible AC Transmission Systems”, IET,1999



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM OPERATION AND DEREGULATION

Prerequisite: Power System Operation and Control

Course objectives:

- To find OPF with security constraints.
- To generalize modeling of load frequency control of a power system
- To compute reactive power control of a power system.
- To apply the concept of deregulation and ATC.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Know the optimal scheduling of power plants
- Outline the modeling of turbine and generator
- Compute the steady state behavior of the power system for voltage and frequency fluctuations.
- Analyze ATC and the cost of transmission

UNIT- I:

Optimal Power Flow

Introduction- Solution to the optimal power flow-gradient method-Newton's method-Linear sensitivity analysis- Linear programming methods- Security constrained OPF-Interior point algorithm- Bus incremental costs

UNIT-II:

Power System Security

Introduction –Factors affecting power system security-Contingency analysis-Detection of network problems-Linear sensitivity analysis-AC power flow methods-contingency selection-concentric relaxation-Bounding area method

UNIT-III:

State Estimation in Power Systems

Introduction- Power system state estimation- Maximum likelihood Weighted Least squares estimation- Matrix formulation- State estimation of AC network- State estimation by orthogonal decomposition- detection and identification of Bad measurements- Estimation of quantities not being measured- Network observability and pseudo measurements

UNIT-IV:

Power System Deregulation

Introduction- motivation for restructuring of power systems- Electricity market entities model-benefits of deregulation- terminology-deregulation in Indian power sector-Operations in power markets-power pools-transmission networks and electricity markets.

UNIT-V:

Available Transfer Capability

Introduction methods: of determination of ATC - ATC calculation considering the effect of contingency analysis- Transmission open access and pricing-cost components of transmission system- transmission pricing methods-Incremental cost based transmission pricing.

TEXT BOOKS:

1. A.J.Wood & B.F.Woolenberg- John Wiley Power Generation, "Operation and Control"-2nd edition.
2. P.Venkatesh. B.V.Manikandan, S.Charles Raja- A.Srinivasan, "Electrical power systems: Analysis, security, Deregulation"– PHI 2012

REFERENCES:

1. Bhattacharya, Kankar, Bollen, Math, Daalder, Jaap E. "Operation of Restructured Power System", 2001, Springer.
2. Venkatesh P. , Manikandan B. V., Raja S. Charles , Srinivasan A. Electrical Power Systems: Analysis, Security And Deregulation, Phi Learning Pvt Ltd



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

GAS INSULATED SYSTEMS (GIS)
(Core Elective–III)

Prerequisite: Switch Gear and Protection

Course objectives:

- To know the GIS concepts and principles
- To choose Air Insulated Substation and GIS
- To demonstrate the design and constructional aspects of GIS
- To analyze transient phenomenon, problems and diagnostic methods in GIS

Course Outcomes: Upon the completion of the subject, the student will be able to

- Know the advantages of GIS systems over air insulated systems
- Observe constructional design features of GIS design
- Discriminate the Problems and design diagnostic methods of GIS

UNIT–I:

Introduction to GIS and Properties Of SF_6

Characteristics of GIS- Introduction to SF_6 - Physical properties-Chemical properties - Electrical properties-Specification of SF_6 gas for GIS application - Handling of SF_6 gas before use - Safe handling of SF_6 gas in electrical equipment - Equipment for handling the SF_6 Gas - SF_6 and environment.

UNIT–II:

Layout of GIS Stations

Advancement of GIS station - Comparison with Air Insulated Substation - Economics of GIS - User Requirements for GIS - Main Features for GIS - Planning and Installation components of a GIS station.

UNIT–III:

Design and Construction of GIS Station

Introduction - Rating of GIS components - Design Features - Estimation of different types of Electrical Stresses -Design Aspects of GIS components - Insulation Design for Components - Insulation Design for GIS - Thermal Considerations in the Design of GIS - Effect of very Fast Transient Over-voltages (VFTO) on the GIS design - Insulation Coordination systems - Gas handling and Monitoring System Design.

UNIT-IV:

Fast Transient Phenomena in GIS

Introduction- Disconnect Switching in Relation to Very fast Transients-Origin of VFTO-Propagation and Mechanism of VFTO-VFTO Characteristics- Effects of VFTO-Testing of GIS for VFTO.

UNIT–V:

Special Problems in GIS and GIS Diagnostics

Introduction - particles their effects and their control- Insulating Spacers and their Reliability - SF_6 Gas Decomposition - Characteristics of imperfections in insulation - Insulation Diagnostic methods - PD Measurement and UHF Method.

TEXT BOOKS:

1. M. S. Naidu, "Gas Insulated Substations"- IK International Publishing House.
2. Hermann J. Koch, "Gas Insulated Substations", June 2014, Wiley-IEEE Press

REFERENCES:

1. Olivier Gallot-Lavellee, "Dielectric materials and Electrostatics" , Wiley-IEEE Press
2. Jaun Martinez, "Dielectric Materials for Electrical Engineering", Wiley-IEEE Press



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPs/PEES/PSC&A)

PROGRAMMABLE LOGIC CONTROLLERS AND THEIR APPLICATIONS
(Core Elective-III)

Prerequisite: No Prerequisite

Course Objectives

- It is to provide and ensure a comprehensive understanding of using advanced controllers in measurement and control instrumentation.
- To illustrate about data acquisition - process of collecting information from field instruments.
- To analyze Programmable Logic Controller (PLC), IO Modules and internal features.
- To Comprehend Programming in Ladder Logic, addressing of IO.
- To apply PID and its Tuning.

Course Outcomes

- Describe the main functional units in a PLC and be able to explain how they interact.
- They should know different bus types used in automation industries.
- Development of ladder logic programming for simple process.
- At the end of each chapter, review question, problems given to reinforce their understanding of the concepts.

UNIT-I:

PLC Basics PLC system, I/O modules and interfacing CPU processor programming equipment programming formats, construction of PLC ladder diagrams, devices connected to I/O modules.

UNIT-II:

PLC Programming input instructions, outputs, operational procedures, programming examples using contacts and coils. Drill-press operation.

Digital logic gates programming in the Boolean algebra system, conversion examples Ladder diagrams for process control Ladder diagrams and sequence listings, ladder diagram construction and flow chart for spray process system.

UNIT-III:

PLC Registers: Characteristics of Registers module addressing holding registers input registers, output registers. PLC Functions Timer functions and industrial applications counters counter function industrial applications, Architecture functions, Number comparison functions, number conversion functions.

UNIT-IV:

Data handling functions: SKIP, Master control Relay Jump Move FIFO, FAL, ONS, CLR and Sweep functions and their applications. Bit Pattern and changing a bit shift register, sequence functions and applications, controlling of two axes and three axis Robots with PLC, Matrix functions.

UNIT-V:

Analog PLC operation: Analog modules and systems Analog signal processing multi bit data processing , analog output application examples, PID principles position indicator with PID control, PID modules, PID tuning, PID functions

TEXT BOOKS:

- 1) Programmable Logic Controllers – Principle and Applications by John W. Webb & Ronald A. Reiss, Fifth Edition, PHI
- 2) Digital Design by Morris Mano, PHI, 3rd Edition 2006.

REFERENCE BOOKS:

1. Programmable logic Controllers, Frank D. Petruzella, 4th Edition, McGraw Hill Publishers.
2. Programmable Logic Controllers – Programming Method and Applications by JR. Hackworth & F.D Hackworth Jr. – Pearson, 2004.
3. Programmable logic controllers and their Engineering Applications, 2nd Edition, Alan J. Crispin.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

HIGH FREQUENCY MAGNETIC COMPONENTS
(Core Elective –III)

Course Objectives:

- To have a knowledge on magnetic circuits
- To know the skin effect and proximity effect

Course Outcome:

- Design of magnetic components (i.e., inductor and transformer) in a converter,
- Steady-state analysis of switched mode power supply
- define core loss in an electromagnetic device, and recognise & describe its effect
- describe the engineering uses of electromagnetic waves, by frequency band, and the respective hazards associated with them

UNIT-I:

Fundamentals of Magnetic Devices: Introduction, Magnetic Relationships, Magnetic Circuits, Magnetic Laws, Eddy Currents, Core Saturation, Volt-Second Balance, Inductance, Inductance Factor, Magnetic Energy, Self-Resonant Frequency, Classification of Power Losses in Magnetic Components, Non-inductive Coils.

Magnetic Cores: Introduction, Properties of Core Materials, Magnetic Dipoles, Magnetic Domains, Curie Temperature, Magnetization, Magnetic Materials, Hysteresis, Core Permeability, Core Geometries, Iron Alloy Cores, Amorphous Alloy Cores, Nickel–Iron and Cobalt–Iron Cores, Ferrite Cores, Powder Cores, Nano-crystalline Cores, Superconductors, Hysteresis Core Loss, Eddy-Current Core Loss, Total Core Loss, Complex Permeability.

UNIT-II:

Skin Effect & Proximity Effect: Introduction, Magnet Wire, Wire Insulation, Skin Depth, Ratio of AC-to-DC Winding Resistance, Skin Effect in Long Single Round Conductor, Current Density in Single Round Conductor, Impedance of Round Conductor, Magnetic Field Intensity for Round Wire, Other Methods of Determining the Round Wire Inductance, Power Density in Round Conductor, Skin Effect on Single Rectangular Plate. Proximity and Skin Effects in Two Parallel Plates, Anti-proximity and Skin Effects in Two Parallel Plates, Proximity Effect in Multiple-Layer Inductor, Appendix: Derivation of Proximity Power Loss.

Winding Resistance at High Frequencies: Introduction, Winding Resistance, Square and Round Conductors, Winding Resistance of Rectangular Conductor, Winding Resistance of Square Wire, Winding Resistance of Round Wire, Leakage Inductance, Solution for Round Conductor Winding in Cylindrical Coordinates, Litz Wire, Winding Power Loss for Inductor Current with Harmonics, Effective Winding Resistance for Non-sinusoidal Inductor Current, Thermal Model of Inductors.

UNIT-III:

Transformers: Introduction, Neumann's Formula for Mutual Inductance, Mutual Inductance, Energy Stored in Coupled Inductors, Magnetizing Inductance, Leakage Inductance, Measurement of Transformer Inductances, Stray Capacitance, High-Frequency Transformer Model, Non-interleaved Windings, Interleaved Windings, AC Current Transformers, Winding Power Losses with Harmonics, Thermal Model of Transformers.

Design of Transformers: Introduction, Area Product Method, Optimum Flux Density, Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM, Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM.

UNIT-IV:

Integrated Inductors: Introduction, Resistance of Rectangular Trace, Inductance of Straight Rectangular Trace, Construction of Integrated Inductors, Meander Inductors, Inductance of Straight Round Conductor, Inductance of Circular Round Wire Loop, Inductance of Two-Parallel Wire Loop, Inductance of Rectangle of Round Wire, Inductance of Polygon Round Wire Loop, Bond-wire Inductors, Single-Turn Planar Inductor, Inductance of Planar Square Loop, Planar Spiral Inductors, Multi-metal Spiral Inductors, Planar Transformers, MEMS Inductors, Inductance of Coaxial Cable, Inductance of Two-Wire Transmission Line, Eddy Currents in Integrated Inductors, Model of RF Integrated Inductors, PCB Inductors.



Design of Inductors: Introduction, Restrictions on Inductors, Window Utilization Factor, Temperature Rise of Inductors, Mean Turn Length of Inductors, Area Product Method, AC Inductor Design, Inductor Design for Buck Converter in CCM, Inductor Design for Buck Converter in DCM method.

UNIT-V:

Self-Capacitance: Introduction, High-Frequency Inductor Model, Self-Capacitance Components, Capacitance of Parallel-Plate Capacitor, Self-Capacitance of Foil Winding Inductors, Capacitance of Two Parallel Round Conductors, Capacitance of Round Conductor and Conducting Plane, Self-Capacitance of Single-Layer Inductors, Self-Capacitance of Multi-layer Inductors, Capacitance of Coaxial Cable.

TEXT BOOK:

1. Design of Magnetic Components for Switched Mode Power Converters, Umanand L., Bhat, S.R., ISBN: 978-81-224-0339-8, Wiley Eastern Publication, 1992.

REFERENCES:

1. High-Frequency Magnetic Components, Marian K. Kazimierczuk, ISBN: 978-0-470-71453-9 John Wiley & Sons, Inc.
2. G.C. Chryssis, High frequency switching power supplies, McGraw Hill, 1989 (2nd Edn.)
3. Eric Lowdon, Practical Transformer Design Handbook, Howard W. Sams & Co., Inc., 1980
4. "Thompson --- Electrodynamic Magnetic Suspension.pdf"
5. Witulski --- "Introduction to modeling of transformers and coupled inductors" Beattie --- "Inductance 101.pdf"
6. P. L. Dowell, "Effects of eddy currents in transformer windings.pdf"
7. Dixon--- "Eddy current losses in transformer windings.pdf"
8. J J Ding, J S Buckkeridge, "Design Considerations For A Sustainable Hybrid Energy System" IPENZ Transactions, 2000, Vol. 27, No. 1/EMCh.
9. Texas Instruments --- "Windings.pdf"
10. Texas Instruments --- "Magnetic core characteristics.pdf"
11. Ferroxcube --- "3f3 ferrite datasheet.pdf"
12. Ferroxcube --- "Ferrite selection guide.pdf"
13. Magnetics, Inc., Ferrite Cores (www.mag-inc.com).



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

REACTIVE POWER COMPENSATION AND MANAGEMENT
(Core Elective –IV)

Prerequisite: Power Systems-II

Course Objectives:

- To identify the necessity of reactive power compensation
- To describe load compensation
- To select various types of reactive power compensation in transmission systems
- To contrast reactive power coordination system
- To characterize distribution side and utility side reactive power management.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Distinguish the importance of load compensation in symmetrical as well as un symmetrical loads
- Observe various compensation methods in transmission lines
- Construct model for reactive power coordination
- Distinguish demand side reactive power management & user side reactive power management

UNIT-I:

Load Compensation: Objectives and specifications – reactive power characteristics – inductive and capacitive approximate biasing – Load compensator as a voltage regulator – phase balancing and power factor correction of unsymmetrical loads- examples.

UNIT-II:

Steady – State Reactive Power Compensation in Transmission System: Uncompensated line – types of compensation – Passive shunt and series and dynamic shunt compensation –examples

Transient state reactive power compensation in transmission systems: Characteristic time periods – passive shunt compensation – static compensations- series capacitor compensation – compensation using synchronous condensers – examples

UNIT-III:

Reactive Power Coordination: Objective – Mathematical modeling – Operation planning – transmission benefits – Basic concepts of quality of power supply – disturbances- steady –state variations – effects of under voltages – frequency –Harmonics, radio frequency and electromagnetic interferences

UNIT-IV:

Demand Side Management: Load patterns – basic methods load shaping – power tariffs- KVAR based tariffs penalties for voltage flickers and Harmonic voltage levels

Distribution side Reactive power Management: System losses –loss reduction methods – examples – Reactive power planning – objectives – Economics Planning capacitor placement – retrofitting of capacitor banks

UNIT-V:

User Side Reactive Power Management: KVAR requirements for domestic appliances – Purpose of using capacitors – selection of capacitors – deciding factors – types of available capacitor, characteristics and Limitations

Reactive power management in electric traction systems and arc furnaces: Typical layout of traction systems – reactive power control requirements – distribution transformers- Electric arc furnaces – basic operations- furnaces transformer –filter requirements – remedial measures –power factor of an arc furnace

TEXT BOOKS:

1. Reactive power control in Electric power systems by T.J.E.Miller, John Wiley and sons, 1982.
2. Reactive power Management by D.M.Tagare, Tata McGraw Hill,2004.

REFERENCES:

1. Wolfgang Hofmann, Jurgen Schlabbach, Wolfgang Just “Reactive Power Compensation: A Practical Guide, April, 2012, Wiely publication.





JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM RELIABILITY
(Core Elective-IV)

Prerequisite: Reliability Engineering

Course Objectives:

- To identify the generation system model and recursive relation for capacitive model building
- To calculate the equivalent transitional rates, cumulative probability and cumulative frequency
- To classify the risk, system and load point reliability indices
- To evaluate the basic reliability indices

Course Outcomes: Upon the completion of the subject, the student will be able to

- Find loss of load and energy indices for generation systems model
- Describe merging generation and load models
- Apply various indices for distribution systems

UNIT-I:

Generating System Reliability Analysis – I: Generation system model – capacity outage probability tables – Recursive relation for capacitive model building – sequential addition method – unit removal – Evaluation of loss of load and energy indices – Examples.

UNIT-II:

Generating System Reliability Analysis – II: Frequency and Duration methods – Evaluation of equivalent transitional rates of identical and non-identical units – Evaluation of cumulative probability and cumulative frequency of non-identical generating units – 2- level daily load representation - merging generation and load models – Examples.

UNIT-III:

Operating Reserve Evaluation: Basic concepts - risk indices – PJM methods – security function approach – rapid start and hot reserve units – Modelling using STPM approach.

Bulk Power System Reliability Evaluation: Basic configuration – conditional probability approach – system and load point reliability indices – weather effects on transmission lines – Weighted average rate and Markov model – Common mode failures.

UNIT-IV:

Inter Connected System Reliability Analysis: Probability array method – Two inter connected systems with independent loads – effects of limited and unlimited tie capacity - imperfect tie – Two connected Systems with correlated loads – Expression for cumulative probability and cumulative frequency.

Distribution System Reliability Analysis – I (Radial configuration): Basic Techniques – Radial networks –Evaluation of Basic reliability indices, performance indices – load point and system reliability indices – customer oriented, loss and energy oriented indices – Examples.

UNIT-V:

Distribution System Reliability Analysis - II (Parallel Configuration): Basic techniques – inclusion of bus bar failures, scheduled maintenance – temporary and transient failures – weather effects – common mode failures –Evaluation of various indices – Examples

Substations and Switching Stations: Effects of short-circuits - breaker operation – Open and Short-circuit failures – Active and Passive failures – switching after faults – circuit breaker model – preventive maintenance – exponential maintenance times.

TEXT BOOKS:

1. Reliability Evaluation of Power systems by R. Billinton, R.N.Allan, BS Publications, 2007.
2. Reliability Modeling in Electric Power Systems by J. Endrenyi, John Wiley and Sons, 1978

REFERENCES:

1. Reliability Engineering: Theory and Practice by Alessandro Birolini, Springer Publications.
2. An Introduction to Reliability and Maintainability Engineering by Charles Ebeling, TMH Publications.
3. Reliability Engineering by E. Balaguruswamy, TMH Publications.
4. Reliability Engineering by Elsayed A. Elsayed, Prentice Hall Publications.





JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

VOLTAGE STABILITY
(Core Elective-IV)

Prerequisite: Computers Methods in Power Systems

Course Objectives:

- To choose SEC Planning and Operational Standards of Security
- To estimate Reactive Power Control in Generation/Transmission Interconnected Networks
- To apply sstability/Instability in Generation/Transmission Interconnected Networks
- To analyze design and Operational Solutions
- To characterize voltage Control in Distribution Networks

Course Outcomes: Upon the completion of the subject, the student will be able to

- Understand issues related to power system stability and control.
- Demonstrate various load models in voltage stability analysis.
- Detect reactive power compensation techniques & their practical importance

UNIT-I:

Introduction to Voltage Stability

Definitions: Voltage Stability, Voltage Collapse, Voltage Security; Physical relation indicating dependency of voltage on reactive power flow; Factors affecting Voltage collapse and instability; Previous cases of voltage collapse incidences.

UNIT-II:

Graphical Analysis of Voltage Stability

Comparison of Voltage and angular stability of the system; Graphical Methods describing voltage collapse phenomenon: P-V and Q-V curves; detailed description of voltage collapse phenomenon with the help of Q-V curves.

UNIT-III:

Analysis of Voltage Stability

Analysis of voltage stability on SMLB system: Analytical treatment and analysis.

Voltage Stability Indices:

Voltage collapse proximity indicator; Determinant of Jacobin as proximity indicators; Voltage stability margin.

UNIT-IV:

Power System Loads

Loads that influences voltage stability: Discharge lights, Induction Motor, Air-conditioning, heat pumps, electronic power supplies, OH lines and cables.

Reactive Power Compensation:

Generation and Absorption of reactive power; Series and Shunt compensation; Synchronous condensers, SVC s; OLTC s; Booster Transformers.

UNIT-V:

Voltage Stability Margin

Stability Margin: Compensated and un-compensated systems.

Voltage Security

Definition; Voltage security; Methods to improve voltage stability and its practical aspects.

TEXT BOOKS:

1. "Performance, operation and control of EHV power transmission system"-A.CHAKRABARTHY, D.P.KOTARI and A.K.MUKOPADYAY, A.H.Wheeler Publishing, I Edition, 1995.
2. "Power System Dynamics: Stability and Control" – K.R.PADIYAR, II Edition, B.S.Publications.

REFERENCES:

1. "Power System Voltage Stability"- C.W.TAYLOR, Mc Graw Hill, 1994.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

INSTRUMENTATION AND CONTROL
(Open Elective – II)

Course Objectives:

- To provide good knowledge of Instrumentation systems and their applications.
- To provide knowledge of advanced control theory and its applications to engineering problems.
- To have a comprehensive idea about P,PI,PD,PID controllers

Course Outcomes:

- Ability to understand and analyze Instrumentation systems and their applications to various industries.
- Ability to apply advanced control theory to practical engineering problems.

Unit-I

Instrumentation: Introduction to mechanical systems and their structure and function, Performance Characteristics – Static and Dynamic, Fundamentals of signals acquisition, conditioning and processing,

Unit-II

Measurement of temperature, pressure, flow, position, velocity, acceleration, force, torque etc.

Unit-III

Control: Introduction to control systems, mathematical model of physical systems in transfer function and state space forms, response of dynamic systems, concept of pole & zero of a system,

Unit-IV

Realization of transfer functions, stability analysis. Introduction of discrete time system. Controllers: P, PI, PD, PID, Feed forward etc., tuning of controller parameters, disturbance rejection, implementation of controller using digital computer.

Unit-V

Control components: Actuator (ac & dc servomotors, valve), AC, DC tacho-generators, servo amplifier.

TEXT BOOKS:

1. Jhon P Bently, "Principles of Measurement Systems" 3rd. Edition, Pearson
2. Alok Barua, "Fundamentals of Industrial Instrumentation" Wiely India, 2011
3. William Bolton, "Instrumentation and Control Systems"Elsevier, 2015

REFERENCES:

1. William Bolton, "Industrial Control and Instrumentation" University Press, 1991
2. Norman A Anderson, " Instrumentation for Process Measurement and Control" CRC, 1997
3. A. K, Ghosh, " Introduction to Instrumentation and Control" Prentice Hall of India, 2005



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

INTELLIGENT CONTROL
(Open Elective-II)

Course Objectives:

- Gaining an understanding of the functional operation of a variety of intelligent control techniques and their bio-foundations
- the study of control-theoretic foundations
- learning analytical approaches to study properties

Course Outcome:

- To give a solid understanding of Basic Neural Network, Fuzzy Logic and Genetic algorithms.
- To know how to use Soft Computing to solve real-world problems mainly pertaining to Control system applications

Unit-I

Introduction and motivation. Approaches to intelligent control. Architecture for intelligent control. Symbolic reasoning system, rule-based systems, the AI approach. Knowledge representation. Expert systems.

Unit-II

Concept of Artificial Neural Networks and its basic mathematical model, McCulloch-Pitts neuron model, simple perceptron, Adaline and Madaline, Feedforward Multilayer Perceptron. Learning and Training the neural network. Data Processing: Scaling, Fourier transformation, principal-component analysis.

Unit-III

Networks: Hopfield network, Self-organizing network and Recurrent network. Neural Network based controller Case studies: Identification and control of linear and nonlinear dynamic systems using Matlab-Neural Network toolbox. Stability analysis of Neural-Network interconnection systems.

Unit-IV

Genetic Algorithm: Basic concept of Genetic algorithm and detail algorithmic steps, adjustment of free parameters. Solution of typical control problems using genetic algorithm. Concept on some other search techniques like tabu search and ant-colony search techniques for solving optimization problems.

Unit-V

Introduction to crisp sets and fuzzy sets, basic fuzzy set operation and approximate reasoning. Introduction to fuzzy logic modeling and control. Fuzzification, inferencing and defuzzification. Fuzzy knowledge and rule bases. Fuzzy modeling and control schemes for nonlinear systems. Fuzzy logic control for nonlinear time-delay system. Implementation of fuzzy logic controller using Matlab fuzzy-logic toolbox. Stability analysis of fuzzy control systems.

TEXT BOOKS

1. Simon Haykins, Neural Networks: A comprehensive Foundation, Pearson Edition, 2003.
2. T.J.Ross, Fuzzy logic with Fuzzy Applications, Mc Graw Hill Inc, 1997.
3. David E Goldberg, Genetic Algorithms.
4. John Yen and Reza Langari, Fuzzy logic Intelligence, Control, and Information, Pearson Education, Indian Edition, 2003.

REFERENCES

1. M.T.Hagan, H. B. Demuth and M. Beale, Neural Network Design, Indian reprint, 2008.
2. Fredric M.Ham and Ivica Kostanic, Principles of Neurocomputing for science and Engineering, McGraw Hill, 2001.
3. N.K. Bose and P.Liang, Neural Network Fundamentals with Graphs, Algorithms and Applications, Mc - Graw Hill, Inc. 1996.
4. Yung C. Shin and Chengying Xu, Intelligent System - Modeling, Optimization and Control, CRC Press, 2009.
5. N.K.Sinha and Madan M Gupta, Soft computing & Intelligent Systems - Theory & Applications, Indian Edition, Elsevier, 2007.
6. Witold Pedrycz, Fuzzy Control and Fuzzy Systms, Overseas Press, Indian Edition, 2008.





JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

SMART GRID TECHNOLOGIES
(Open Elective-II)

Prerequisite: Electrical and Electronic Instrumentation

Course Objectives:

- To group various aspects of the smart grid,
- To defend smart grid design to meet the needs of a utility
- To select issues and challenges that remain to be solved
- To analyze basics of electricity, electricity generation, economics of supply and demand, and the various aspects of electricity market operations in both regulated and deregulated environment.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Recite the structure of an electricity market in either regulated or deregulated market conditions.
- Understand the advantages of DC distribution and developing technologies in distribution
- Discriminate the trade-off between economics and reliability of an electric power system.
- Differentiate various investment options (e.g. generation capacities, transmission, renewable, demand-side resources, etc) in electricity markets.
- Analyze the development of smart and intelligent domestic systems.

UNIT-I:

Introduction: Introduction to smart grid- Electricity network-Local energy networks- Electric transportation- Low carbon central generation-Attributes of the smart grid- Alternate views of a smart grid.

Smart Grid to Evolve a Perfect Power System: Introduction- Overview of the perfect power system configurations- Device level power system- Building integrated power systems- Distributed power systems- Fully integrated power system-Nodes of innovation.

UNIT-II:

DC Distribution and Smart Grid: AC vs DC sources-Benefits of and drives of DC power delivery systems-Powering equipment and appliances with DC-Data centers and information technology loads-Future neighborhood-Potential future work and research.

Intelligrid Architecture for the Smartgrid: Introduction- Launching intelligrid-Intelligrid today- Smart grid vision based on the intelligrid architecture-Barriers and enabling technologies.

UNIT-III:

Dynamic Energy Systems Concept: Smart energy efficient end use devices-Smart distributed energy resources-Advanced whole building control systems- Integrated communications architecture-Energy management-Role of technology in demand response- Current limitations to dynamic energy management-Distributed energy resources-Overview of a dynamic energy management-Key characteristics of smart devices- Key characteristics of advanced whole building control systems-Key characteristics of dynamic energy management system.

UNIT-IV:

Energy Port as Part of the Smart Grid: Concept of energy -Port, generic features of the energy port.

Policies and Programs to Encourage End – Use Energy Efficiency: Policies and programs in action -multinational - national-state-city and corporate levels.

Market Implementation: Framework-factors influencing customer acceptance and response - program planning-monitoring and evaluation.

UNIT-V:

Efficient Electric End – Use Technology Alternatives: Existing technologies – lighting - Space conditioning - Indoor air quality - Domestic water heating - hyper efficient appliances - Ductless residential heat pumps and air conditioners - Variable refrigerant flow air conditioning-Heat pump water heating - Hyper efficient residential appliances - Data center energy efficiency- LED street and area lighting - Industrial motors and drives - Equipment retrofit and replacement - Process heating - Cogeneration, Thermal energy storage - Industrial energy management programs - Manufacturing process-Electro-technologies, Residential, Commercial and industrial sectors.

**TEXT BOOKS:**

1. Clark W Gellings, "The Smart Grid, Enabling Energy Efficiency and Demand Side Response"- CRC Press, 2009.
2. Jean Claude Sabonnadière, Nouredine Hadjsaid, "Smart Grids", Wiley-ISTE, IEEE Press, May 2012

REFERENCES:

1. Janaka Ekanayake, Kithsiri Liyanage, Jianzhong Wu, Akihiko Yokoyama, Nick Jenkins, "Smart Grid: Technology and Applications"- Wiley, 2012.
2. James Momoh, "Smart Grid : Fundamentals of Design and Analysis"-Wiley, IEEE Press, 2012.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

AI TECHNIQUES IN ELECTRICAL ENGINEERING
(Open Elective-II)

Course Objectives:

- To locate soft commanding methodologies, such as artificial neural networks, Fuzzy logic and genetic Algorithms.
- To observe the concepts of feed forward neural networks and about feedback neural networks.
- To practice the concept of fuzziness involved in various systems and comprehensive knowledge of fuzzy logic control and to design the fuzzy control
- To analyze genetic algorithm, genetic operations and genetic mutations.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Quote feed forward neural networks and learning and understanding of feedback neural networks.
- Generalize fuzziness involved in various systems and fuzzy set theory.
- Select fuzzy logic control and design
- Examine genetic algorithm and applications in electrical engineering.

UNIT – I:

Artificial Neural Networks: Introduction-Models of Neural Network - Architectures – Knowledge representation – Artificial Intelligence and Neural networks–Learning process – Error correction learning – Hebbian learning –Competitive learning –Boltzman learning –Supervised learning – Unsupervised learning – Reinforcement learning- learning tasks.

UNIT- II:

ANN Paradigms : Multi – layer perceptron using Back propagation Algorithm-Self – organizing Map – Radial Basis Function Network – Functional link, network – Hopfield Network.

UNIT – III:

Fuzzy Logic: Introduction – Fuzzy versus crisp – Fuzzy sets - Membership function – Basic Fuzzy set operations – Properties of Fuzzy sets – Fuzzy cartesian Product –Operations on Fuzzy relations – Fuzzy logic – Fuzzy Quantifiers-Fuzzy Inference-Fuzzy Rule based system-Defuzzification methods.

UNIT – IV:

Genetic Algorithms: Introduction-Encoding –Fitness Function-Reproduction operators-Genetic Modeling –Genetic operators-Crossover-Single – site crossover-Two point crossover –Multi point crossover-Uniform crossover – Matrix crossover-Crossover Rate-Inversion & Deletion –Mutation operator –Mutation –Mutation Rate-Bit-wise operators-Generational cycle-convergence of Genetic Algorithm.

UNIT-V:

Applications of AI Techniques: Load forecasting – Load flow studies – Economic load dispatch – Load frequency control – Single area system and two area system – Small Signal Stability (Dynamic stability) Reactive power control – speed control of DC and AC Motors.

TEXT BOOK:

1. S.Rajasekaran and G.A.V.Pai, "Neural Networks, Fuzzy Logic & Genetic Algorithms"- PHI, New Delhi, 2003.

REFERENCE BOOKS:

1. P.D.Wasserman,Van Nostrand Reinhold,"Neural Computing Theory & Practice"- New York,1989.
2. Bart Kosko,"Neural Network & Fuzzy System" Prentice Hall, 1992.
3. G.J.Klir and T.A.Folger,"Fuzzy sets,Uncertainty and Information"-PHI, Pvt.Ltd,1994.
4. D.E.Goldberg," Genetic Algorithms"- Addison Wesley 1999.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

RELIABILITY ENGINEERING
(Open Elective –II)

Course Objectives

- To comprehend the concept of Reliability and Unreliability
- Derive the expressions for probability of failure, Expected value and standard deviation of Binominal distribution, Poisson distribution, normal distribution and weibull distributions.
- Formulating expressions for Reliability analysis of series-parallel and Non-series parallel systems
- Deriving expressions for Time dependent and Limiting State Probabilities using Markov models.

Learning Outcomes

- Apply fundamental knowledge of Reliability to modeling and analysis of series-parallel and Non-series parallel systems.
- Solve some practical problems related with Generation, Transmission and Utilization of Electrical Energy.
- Understand or become aware of various failures, causes of failures and remedies for failures in practical systems.

Unit I:

Rules for combining probabilities of events, Definition of Reliability. Significance of the terms appearing in the definition. Probability distributions: Random variables, probability density and distribution functions. Mathematical expectation, Binominal distribution, Poisson distribution, normal distribution, weibull distribution.

Unit II:

Hazard rate, derivation of the reliability function in terms of the hazard rate. Failures: Causes of failures, types of failures (early failures, chance failures and wear-out failures). Bath tub curve. Preventive and corrective maintenance. Modes of failure. Measures of reliability: mean time to failure and mean time between failures.

Unit III:

Classification of engineering systems: series, parallel and series-parallel systems- Expressions for the reliability of the basic configurations.
Reliability evaluation of Non-series-parallel configurations: Decomposition, Path based and cutset based methods, Deduction of the Paths and cutsets from Event tree.

Unit IV:

Discrete Markov Chains: General modelling concepts, stochastic transitional probability matrix, time dependent probability evaluation and limiting state probability evaluation of one component repairable model. Absorbing states.
Continuous Markov Processes: Modelling concepts, State space diagrams, Stochastic Transitional Probability Matrix, Evaluating time dependent and limiting state Probabilities of one component repairable model. Evaluation of Limiting state probabilities of two component repairable model.

UNIT-V:

Approximate system Reliability analysis of Series systems, parallel systems with two and more than two components, Network reduction techniques. Minimal cutset/failure mode approach.

TEXT BOOKS:

1. "Reliability evaluation of Engineering systems", Roy Billinton and Ronald N Allan, BS Publications.
2. "Reliability Engineering", Elsayed A. Elsayed, Prentice Hall Publications.

REFERENCE BOOKS:

1. "Reliability Engineering: Theory and Practice", By Alessandro Birolini, Springer Publications.
2. "An Introduction to Reliability and Maintainability Engineering", Charles Ebeling, TMH Publications.
3. "Reliability Engineering", E. Balaguruswamy, TMH Publications.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

ENERGY AUDITING, CONSERVATION AND MANAGEMENT
(Open Elective –II)

Prerequisite: Electrical Distribution Systems

Course Objectives:

- To know the necessity of conservation of energy
- To generalize the methods of energy management
- To illustrate the factors to increase the efficiency of electrical equipment
- To detect the benefits of carrying out energy audits.

Course Outcomes: Upon the completion of the subject, the student will be able to

- Tell energy audit of industries
- Predict management of energy systems
- Sequence the methods of improving efficiency of electric motor
- Analyze the power factor and to design a good illumination system
- Determine pay back periods for energy saving equipment

UNIT-I:

Basic Principles of Energy Audit: Energy audit- definitions, concept , types of audit, energy index, cost index ,pie charts, Sankey diagrams, load profiles, Energy conservation schemes- Energy audit of industries- energy saving potential, energy audit of process industry, thermal power station, building energy audit.

UNIT-II:

Energy Management: Principles of energy management, organizing energy management program, initiating, planning, controlling, promoting, monitoring, reporting- Energy manger, Qualities and functions, language, Questionnaire – check list for top management.

UNIT-III:

Energy Efficient Motors: Energy efficient motors , factors affecting efficiency, loss distribution , constructional details , characteristics - variable speed , variable duty cycle systems, RMS hp- voltage variation-voltage unbalance- over motoring- motor energy audit

UNIT-IV:

Power Factor Improvement, Lighting and Energy Instruments: Power factor – methods of improvement, location of capacitors, Pf with non linear loads, effect of harmonics on power factor, power factor motor controllers - Good lighting system design and practice, lighting control ,lighting energy audit - Energy Instruments- wattmeter, data loggers, thermocouples, pyrometers,lux meters, tongue testers ,application of PLC's.

UNIT-V:

Economic Aspects and Analysis: Economics Analysis-Depreciation Methods, time value of money, rate of return , present worth method , replacement analysis, life cycle costing analysis- Energy efficient motors- calculation of simple payback method, net present worth method- Power factor correction, lighting - Applications of life cycle costing analysis, return on investment .

TEXT BOOKS:

1. Energy management by W.R. Murphy AND G. McKay Butter worth, Heinemann publications.
2. Energy management by Paul o' Callaghan, Mc-graw Hill Book company-1st edition, 1998

REFERENCES:

1. Energy efficient electric motors by John .C. Andreas, Marcel Dekker Inc Ltd-2nd edition, 1995-
2. Energy management hand book by W.C.Turner, John wiley and sons
3. Energy management and good lighting practice : fuel efficiency- booklet12-EEO



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM LAB – II

1. Determination of Equivalent circuit of a 3-Winding Transformer.
2. Determination of Sequence Impedances of a Cylindrical Rotor Synchronous Machine.
3. Fault Analysis:
 - i. Single Line to Ground fault (L-G).
 - ii. Line to Line fault (L-L).
 - iii. Double Line to Ground fault (L-L-G).
 - iv. Triple Line to Ground fault (L-L-L-G).
4. Determination of Sub-transient reactance's of a Salient Pole Synchronous Machine.
5. Determination of Sequence Impedances of Three Phase Transformer
6. Characteristics of Over Current Relays
 - i. IDMT Electromagnetic Relay (7051 A).
 - ii. Microprocessor based Relay (7051 B)
7. Characteristics of Percentage biased Differential Relay.
 - i. Electromagnetic Relay (7054 A).
 - ii. Static Relay (7054 B).
8. Characteristics of Over Voltage Relay.
 - i. Electromagnetic Relay (7053 A).
 - ii. Microprocessor based Relay (7053 B).
9. Characteristics of Under Voltage (UV) and Negative sequence Relays
 - i. Uv Electromagnetic Relay (7052 A).
 - ii. Uv Microprocessor Based Relay (7052 B).
 - iii. Static Negative Sequence Relay (7055 B).
10. Performance and Testing of Generator Protection System.
11. Performance and Testing of Transformer Protection System.
12. Performance and Testing of Feeder Protection System.
13. Performance and Testing of Transmission Line Model.
14. Differential protection on Single Phase Transformer.

Note: From the above list minimum 10 experiments are to be conducted



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech. (VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN)

COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	VLSI Technology	25	75	4	--	4
Core Course II	CMOS Analog Integrated Circuit Design	25	75	4	--	4
Core Course III	CMOS Digital Integrated Circuit Design	25	75	4	--	4
Core Elective I	Digital System Design Hardware Software Co-Design CPLD and FPGA Architectures and Applications	25	75	4	--	4
Core Elective II	Algorithms for VLSI Design Automation Embedded System Design Device Modeling	25	75	4	--	4
Open Elective I	Soft Computing Techniques Image and Video processing Software Defined Radio	25	75	4	--	4
Laboratory I	VLSI Laboratory – I	25	75	--	4	2
Seminar I	Seminar	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Low Power VLSI Design	25	75	4	--	4
Core Course V	Design for Testability	25	75	4	--	4
Core Course VI	CMOS Mixed Signal Circuit Design	25	75	4	--	4
Core Elective III	VLSI and DSP Architectures Full custom IC Design Hardware Description Language	25	75	4	--	4
Core Elective IV	Optimization Techniques in VLSI Design System On Chip Architecture Semiconductor Memory Design and Testing	25	75	4	--	4
Open Elective II	Scripting Languages Coding Theory and Techniques Adhoc Wireless Networks	25	75	4	--	4
Laboratory II	VLSI Laboratory – II	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C
Comprehensive Viva-Voce	--	100	--	--	4
Project work Review I	50	--	--	24	12
Total Credits			--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C
Project work Review II	50	--	--	8	4
Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits			--	24	16



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI TECHNOLOGY

UNIT –I:

Review of Microelectronics and Introduction to MOS Technologies:

MOS, CMOS, BiCMOS Technology. Basic Electrical Properties of MOS, CMOS & BiCMOS Circuits: I_{ds} – V_{ds} relationships, Threshold Voltage V_T , G_m , G_{ds} and ω_o , Pass Transistor, MOS, CMOS & BiCMOS Inverters, Z_{pu}/Z_{pd} , MOS Transistor circuit model, Latch-up in CMOS circuits.

UNIT –II:

Layout Design and Tools:

Transistor structures, Wires and Vias, Scalable Design rules, Layout Design tools.

Logic Gates & Layouts:

Static Complementary Gates, Switch Logic, Alternative Gate circuits, Low power gates, Resistive and Inductive interconnect delays.

UNIT –III:

Overview of semiconductor industry, Stages of Manufacturing, Process and product trends, Crystal growth, Basic wafer fabrication operations, process yields, Semiconductor material preparation, Basic wafer fabrication operations, Yield measurement, Contamination sources, Clean room construction, Oxidation and Photolithography, Doping and Depositions, Metallization. Ten step patterning process, Photoresists, physical properties of photoresists, Storage and control of photoresists, photo masking process, Hard bake, develop inspect, Dry etching Wet etching, resist stripping

UNIT –IV

Doping and depositions: Diffusion process steps, deposition, Drive-in oxidation, Ion implantation-1, Ion implantation-2, CVD basics, CVD process steps, Low pressure CVD systems, Plasma enhanced CVD systems, Vapour phase epitaxy, molecular beam epitaxy.

UNIT –V

Design rules and Scaling, BiCMOS ICs: Choice of transistor types, pnp transistors, Resistors, capacitors,

Packaging: Chip characteristics, package functions, package operations

TEXT BOOKS:

1. Peter Van Zant, Microchip fabrication, McGraw Hill, 1997.
2. C.Y. Chang and S.M. Sze, ULSI technology, McGraw Hill, 2000

REFERENCE BOOKS:

1. Micro Electronics circuits Analysis and Design 2nd Edition, Muhammad H Rashid, CENAGE Learning 2011.
2. Eugene D. Fabricius, Introduction to VLSI design, McGraw Hill, 1999
3. Wani-Kai Chen (editor), The VLSI Hand book, CRI/IEEE press, 2000
4. S.K. Gandhi, VLSI Fabrication principles, John Wiley and Sons, NY, 1994



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

CMOS ANALOG INTEGRATED CIRCUIT DESIGN

UNIT -I:

MOS Devices and Modeling

The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

UNIT -II:

Analog CMOS Sub-Circuits:

MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

UNIT -III

CMOS Amplifiers:

Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

UNIT -IV

CMOS Operational Amplifiers:

Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power-Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.

UNIT -V

Comparators:

Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

TEXT BOOKS:

CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.

Analysis and Design of Analog Integrated Circuits- Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, Wiley India, Fifth Edition, 2010.

REFERENCE BOOKS:

Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edn, 2013.

Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition.

CMOS: Circuit Design, Layout and Simulation- Baker, Li and Boyce, PHI.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSi System Design)

CMOS DIGITAL INTEGRATED CIRCUIT DESIGN

UNIT –I:

MOS Design:

Pseudo NMOS Logic – Inverter, Inverter threshold voltage, Output high voltage, Output Low voltage, Gain at gate threshold voltage, Transient response, Rise time, Fall time, Pseudo NMOS logic gates, Transistor equivalency, CMOS Inverter logic.

UNIT –II:

Combinational MOS Logic Circuits:

MOS logic circuits with NMOS loads, Primitive CMOS logic gates – NOR & NAND gate, Complex Logic circuits design – Realizing Boolean expressions using NMOS gates and CMOS gates, AOI and OIA gates, CMOS full adder, CMOS transmission gates, Designing with Transmission gates.

UNIT –III:

Sequential MOS Logic Circuits:

Behavior of bistable elements, SR Latch, Clocked latch and flip flop circuits, CMOS D latch and edge triggered flipflop.

UNIT –IV:

Dynamic Logic Circuits:

Basic principle, Voltage Bootstrapping, Synchronous dynamic pass transistor circuits, Dynamic CMOS transmission gate logic, High performance Dynamic CMOS circuits.

UNIT –V:

Semiconductor Memories:

Types, RAM array organization, DRAM – Types, Operation, Leakage currents in DRAM cell and refresh operation, SRAM operation Leakage currents in SRAM cells, Flash Memory- NOR flash and NAND flash.

TEXT BOOKS:

1. Digital Integrated Circuit Design – Ken Martin, Oxford University Press, 2011.
2. CMOS Digital Integrated Circuits Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 3rd Ed., 2011.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011
2. Digital Integrated Circuits – A Design Perspective, Jan M. Rabaey, Anantha Chandrakasan, Borivoje Nikolic, 2nd Ed., PHI.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

DIGITAL SYSTEM DESIGN
(Core Elective –I)

UNIT -I:

Minimization and Transformation of Sequential Machines:

The Finite State Model – Capabilities and limitations of FSM – State equivalence and machine minimization – Simplification of incompletely specified machines.

Fundamental mode model – Flow table – State reduction – Minimal closed covers – Races, Cycles and Hazards.

UNIT -II:

Digital Design:

Digital Design Using ROMs, PALs and PLAs , BCD Adder, 32 – bit adder, State graphs for control circuits, Scoreboard and Controller, A shift and add multiplier, Array multiplier, Keypad Scanner, Binary divider.

UNIT -III:

SM Charts:

State machine charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, dice game controller.

UNIT -IV:

Fault Modeling & Test Pattern Generation:

Logic Fault model – Fault detection & Redundancy- Fault equivalence and fault location –Fault dominance – Single stuck at fault model – Multiple stuck at fault models –Bridging fault model.

Fault diagnosis of combinational circuits by conventional methods – Path sensitization techniques, Boolean Difference method – Kohavi algorithm – Test algorithms – D algorithm, PODEM, Random testing, Transition count testing, Signature analysis and test bridging faults.

UNIT -V:

Fault Diagnosis in Sequential Circuits:

Circuit Test Approach, Transition Check Approach – State identification and fault detection experiment, Machine identification, Design of fault detection experiment

TEXT BOOKS:

1. Fundamentals of Logic Design – Charles H. Roth, 5th Ed., Cengage Learning.
2. Digital Systems Testing and Testable Design – Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman- John Wiley & Sons Inc.
3. Logic Design Theory – N. N. Biswas, PHI

REFERENCE BOOKS:

1. Switching and Finite Automata Theory – Z. Kohavi , 2nd Ed., 2001, TMH
2. Digital Design – Morris Mano, M.D.Ciletti, 4th Edition, PHI.
3. Digital Circuits and Logic Design – Samuel C. Lee , PHI



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

HARDWARE - SOFTWARE CO-DESIGN
(Core Elective –I)

UNIT –I:

Co- Design Issues:

Co- Design Models, Architectures, Languages, A Generic Co-design Methodology.

Co- Synthesis Algorithms:

Hardware software synthesis algorithms: hardware – software partitioning distributed system co-synthesis.

UNIT –II:

Prototyping and Emulation:

Prototyping and emulation techniques, prototyping and emulation environments, future developments in emulation and prototyping architecture specialization techniques, system communication infrastructure

Target Architectures:

Architecture Specialization techniques, System Communication infrastructure, Target Architecture and Application System classes, Architecture for control dominated systems (8051-Architectures for High performance control), Architecture for Data dominated systems (ADSP21060, TMS320C60), Mixed Systems.

UNIT –III:

Compilation Techniques and Tools for Embedded Processor Architectures:

Modern embedded architectures, embedded software development needs, compilation technologies, practical consideration in a compiler development environment.

UNIT –IV:

Design Specification and Verification:

Design, co-design, the co-design computational model, concurrency coordinating concurrent computations, interfacing components, design verification, implementation verification, verification tools, interface verification

UNIT –V:

Languages for System – Level Specification and Design-I:

System – level specification, design representation for system level synthesis, system level specification languages,

Languages for System – Level Specification and Design-II:

Heterogeneous specifications and multi language co-simulation, the cosyma system and lycos system.

TEXT BOOKS:

1. Hardware / Software Co- Design Principles and Practice – Jorgen Staunstrup, Wayne Wolf – 2009, Springer.
2. Hardware / Software Co- Design - [Giovanni De Micheli](#), [Mariagiovanna Sami](#), 2002, Kluwer Academic Publishers

REFERENCE BOOKS:

1. A Practical Introduction to Hardware/Software Co-design -Patrick R. Schaumont - 2010 – Springer



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSISystem Design)

CPLD AND FPGA ARCHITECTURES AND APPLICATIONS
(Core Elective –I)

UNIT-I:

Introduction to Programmable Logic Devices:

Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation.

UNIT-II:

Field Programmable Gate Arrays:

Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, Applications of FPGAs.

UNIT -III:

SRAM Programmable FPGAs:

Introduction, Programming Technology, Device Architecture, The Xilinx XC2000, XC3000 and XC4000 Architectures.

UNIT -IV:

Anti-Fuse Programmed FPGAs:

Introduction, Programming Technology, Device Architecture, The Actel ACT1, ACT2 and ACT3 Architectures.

UNIT -V:

Design Applications:

General Design Issues, Counter Examples, A Fast Video Controller, A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.

TEXT BOOKS:

1. Field Programmable Gate Array Technology - Stephen M. Trimberger, Springer International Edition.
2. Digital Systems Design - Charles H. Roth Jr, Lizy Kurian John, Cengage Learning.

REFERENCE BOOKS:

1. Field Programmable Gate Arrays - John V. Oldfield, Richard C. Dorf, Wiley India.
2. Digital Design Using Field Programmable Gate Arrays - Pak K. Chan/Samiha Mourad, Pearson Low Price Edition.
3. Digital Systems Design with FPGAs and CPLDs - Ian Grout, Elsevier, Newnes.
4. FPGA based System Design - Wayne Wolf, Prentice Hall Modern Semiconductor Design Series.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSISystem Design)

ALGORITHMS FOR VLSI DESIGN AUTOMATION
(Core Elective –II)

UNIT I

PRELIMINARIES

Introduction to Design Methodologies, Design Automation tools, Algorithmic Graph Theory, Computational complexity, Tractable and Intractable problems.

UNIT II

GENERAL PURPOSE METHODS FOR COMBINATIONAL OPTIMIZATION

Backtracking, Branch and Bound, Dynamic Programming, Integer Linear Programming, Local Search, Simulated Annealing, Tabu search, Genetic Algorithms.

UNIT III

LAYOUT COMPACTION, PLACEMENT, FLOORPLANNING AND ROUTING

Problems, Concepts and Algorithms.

MODELLING AND SIMULATION

Gate Level Modelling and Simulation, Switch level Modelling and Simulation.

UNIT IV

LOGIC SYNTHESIS AND VERIFICATION

Basic issues and Terminology, Binary-Decision diagrams, Two-Level logic Synthesis

HIGH-LEVEL SYNTHESIS

Hardware Models, Internal representation of the input Algorithm, Allocation, Assignment and Scheduling, Some Scheduling Algorithms, Some aspects of Assignment problem, High-level Transformations.

UNIT V

PHYSICAL DESIGN AUTOMATION OF FPGAs

FPGA technologies, Physical Design cycle for FPGAs, partitioning and Routing for segmented and staggered Models.

PHYSICAL DESIGN AUTOMATION OF MCMs

MCM technologies, MCM physical design cycle, Partitioning, Placement - Chip Array based and Full Custom Approaches, Routing – Maze routing, Multiple stage routing, Topologic routing, Integrated Pin – Distribution and routing, Routing and Programmable MCMs.

TEXT BOOKS

1. Algorithms for VLSI Design Automation, S.H.Gerez, 1999, WILEY Student Edition, John wiley & Sons (Asia) Pvt. Ltd.
2. Algorithms for VLSI Physical Design Automation – Naveed Sherwani, 3rd Ed., 2005, Springer International Edition.

REFERENCE BOOKS

1. Computer Aided Logical Design with Emphasis on VLSI – Hill & Peterson, 1993, Wiley.
2. Modern VLSI Design:Systems on silicon – Wayne Wolf, 2nd ed., 1998, Pearson Education Asia.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

EMBEDDED SYSTEMS DESIGN
(Core Elective –II)

UNIT -I:

Introduction to Embedded Systems

Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT -II:

Typical Embedded System:

Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT -III:

Embedded Firmware:

Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT -IV:

RTOS Based Embedded System Design:

Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT -V:

Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOKS:

1. Introduction to Embedded Systems - Shibu K.V, Mc Graw Hill.

REFERENCE BOOKS:

1. Embedded Systems - Raj Kamal, TMH.
2. Embedded System Design - Frank Vahid, Tony Givargis, John Wiley.
3. Embedded Systems – Lyla, Pearson, 2013
4. An Embedded Software Primer - David E. Simon, Pearson Education.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

DEVICE MODELLING
(Core Elective –II)

UNIT -I:

Introduction to Semiconductor Physics:

Review of Quantum Mechanics, Boltzman transport equation, Continuity equation, Poisson equation.

Integrated Passive Devices:

Types and Structures of resistors and capacitors in monolithic technology, Dependence of model parameters on structures

UNIT -II:

Integrated Diodes:

Junction and Schottky diodes in monolithic technologies – Static and Dynamic behavior – Small and large signal models – SPICE models

Integrated Bipolar Transistor:

Types and structures in monolithic technologies – Basic model (Eber-Moll) – Gummel - Poon model- dynamic model, Parasitic effects – SPICE model –Parameter extraction

UNIT -III:

Integrated MOS Transistor:

NMOS and PMOS transistor – Threshold voltage – Threshold voltage equations – MOS device equations – Basic DC equations second order effects – MOS models – small signal AC characteristics – MOS FET SPICE model level 1, 2, 3 and 4

UNIT -IV:

VLSI Fabrication Techniques: An overview of wafer fabrication, Wafer Processing – Oxidation – Patterning – Diffusion – Ion Implantation – Deposition – Silicon gate nMOS process – CMOS processes – n-well- p-well- twin tub- Silicon on insulator – CMOS process enhancements – Interconnects circuit elements

UNIT -V:

Modeling of Hetero Junction Devices: Band gap Engineering, Band gap Offset at abrupt Hetero Junction, Modified current continuity equations, Hetero Junction bipolar transistors (HBTs), SiGe

TEXT BOOKS:

1. Introduction to Semiconductor Materials and Devices – Tyagi M. S, 2008, John Wiley Student Edition.
2. Solid State Circuits – Ben G. Streetman, Prentice Hall, 1997

REFERENCE BOOKS:

1. Physics of Semiconductor Devices – Sze S. M, 2nd Edition, Mcgraw Hill, New York, 1981.
2. Introduction to Device Modeling and Circuit Simulation – Tor A. Fijedly, Wiley-Interscience, 1997.
3. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

SOFT COMPUTING TECHNIQUES
(Open Elective - I)

UNIT – I: Fundamentals of Neural Networks & Feed Forward Networks

Basic Concept of Neural Networks, Human Brain, Models of an Artificial Neuron, Learning Methods, Neural Networks Architectures, Signal Layer Feed Forward Neural Network :The Perceptron Model, Multilayer Feed Forward Neural Network :Architecture of a Back Propagation Network(BPN), The Solution, Backpropagation Learning, Selection of various Parameters in BPN. Application of Back propagation Networks in Pattern Recognition & Image Processing.

UNIT – II: Associative Memories & ART Neural Networks

Basic concepts of Linear Associator, Basic concepts of Dynamical systems, Mathematical Foundation of Discrete-Time Hop field Networks(HPF), Mathematical Foundation of Gradient-Type Hopfield Networks, Transient response of Continuous Time Networks, Applications of HPF in Solution of Optimization Problem: Minimization of the Traveling salesman tour length, Summing networks with digital outputs, Solving Simultaneous Linear Equations, Bidirectional Associative Memory Networks; Cluster Structure, Vector Quantization, Classical ART Networks, Simplified ART Architecture.

UNIT – III: Fuzzy Logic & Systems

Fuzzy sets, Crisp Relations, Fuzzy Relations, Crisp Logic, Predicate Logic, Fuzzy Logic, Fuzzy Rule based system, Defuzzification Methods, Applications: Greg Viot's Fuzzy Cruise Controller, Air Conditioner Controller.

UNIT – IV: Genetic Algorithms

Basic Concepts of Genetic Algorithms (GA), Biological background, Creation of Offsprings, Working Principle, Encoding, Fitness Function, Reproduction, Inheritance Operators, Cross Over, Inversion and Deletion, Mutation Operator, Bit-wise Operators used in GA, Generational Cycle, Convergence of Genetic Algorithm.

UNIT – V: Hybrid Systems

Types of Hybrid Systems, Neural Networks, Fuzzy Logic, and Genetic Algorithms Hybrid, Genetic Algorithm based BPN: GA Based weight Determination, Fuzzy Back Propagation Networks: LR-type fuzzy numbers, Fuzzy Neuron, Fuzzy BP Architecture, Learning in Fuzzy BPN, Inference by fuzzy BPN.

TEXT BOOKS:

1. Introduction to Artificial Neural Systems - J.M.Zurada, Jaico Publishers
2. Neural Networks, Fuzzy Logic & Genetic Algorithms: Synthesis & Applications -S.Rajasekaran, G.A. Vijayalakshmi Pai, July 2011, PHI, New Delhi.
3. Genetic Algorithms by David E. Gold Berg, Pearson Education India, 2006.
4. Neural Networks & Fuzzy Sytems- Kosko.B., PHI, Delhi,1994.

REFERENCE BOOKS:

1. Artificial Neural Networks - Dr. B. Yagananarayana, 1999, PHI, New Delhi.
2. An introduction to Genetic Algorithms - Mitchell Melanie, MIT Press, 1998
3. Fuzzy Sets, Uncertainty and Information- Klir G.J. & Folger. T. A., PHI, Delhi, 1993.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

IMAGE AND VIDEO PROCESSING
(OPEN ELECTIVE-I)

UNIT –I:

Fundamentals of Image Processing and Image Transforms: Basic steps of Image Processing System Sampling and Quantization of an image, Basic relationship between pixels.

Image Segmentation: Segmentation concepts, Point, Line and Edge Detection, Thresholding, Region based segmentation.

UNIT –II:

Image Enhancement: Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters.

Frequency domain methods: Basics of filtering in frequency domain, Image smoothing, Image sharpening, Selective filtering.

UNIT –III:

Image Compression: Image compression fundamentals - Coding Redundancy, Spatial and Temporal redundancy, Compression models: Lossy & Lossless, Huffman coding, Bit plane coding, Transform coding, Predictive coding, Wavelet coding, Lossy Predictive coding, JPEG Standards.

UNIT -IV:

Basic Steps of Video Processing: Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.

UNIT –V:

2-D Motion Estimation: Optical flow, General Methodologies, Pixel Based Motion Estimation, Block-Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.

TEXT BOOKS:

1. Digital Image Processing – Gonzaleze and Woods, 3rd Ed., Pearson.
2. Video Processing and Communication – Yao Wang, Joem Ostermann and Ya-quin Zhang. 1st Ed., PH Int.

REFERENCE BOOKS:

1. Digital Image Processing using MATLAB– Gonzaleze and Woods, 2nd ed., Mc Graw Hill Education, 2010
2. Image Processing Analysis , and Machine Vision- Milan Sonka, Vaclan Hlavac, 3 ed., CENGAGE, 2008
3. Digital Video Processing – A Murat Tekalp, PERSON, 2010
4. Digital Image Processing – S.Jayaraman, S.Esakkirajan, T.Veera Kumar –TMH, 2009



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

SOFTWARE DEFINED RADIO
(Open Elective-I)

UNIT -I:

Introduction: The Need for Software Radios, What is Software Radio, Characteristics and benefits of software radio- Design Principles of Software Radio, RF Implementation issues- The Purpose of RF Front – End, Dynamic Range- The Principal Challenge of Receiver Design – RF Receiver Front-End Topologies- Enhanced Flexibility of the RF Chain with Software Radios- Importance of the Components to Overall Performance- Transmitter Architectures and Their Issues- Noise and Distortion in the RF Chain, ADC and DAC Distortion.

UNIT -II:

Profile and Radio Resource Management : Communication Profiles- Introduction, Communication Profiles, Terminal Profile, Service Profile , Network Profile, User Profile, Communication Profile Architecture, Profile Data Structure, XML Structure, Distribution of Profile Data, Access to Profile Data, Management of Communication Profiles, Communication Classmarks, Dynamic Classmarks for Reconfigurable Terminals, Compression and Coding, Meta Profile Data

UNIT -III:

Radio Resource Management in Heterogeneous Networks

Introduction, Definition of Radio Resource Management, Radio Resource Units over RRM Phases, RRM Challenges and Approaches, RRM Modelling and Investigation Approaches, Investigations of JRRM in Heterogeneous Networks, Measuring Gain in the Upper Bound Due to JRRM, Circuit-Switched System, Packet-Switched System, Functions and Principles of JRRM, General Architecture of JRRM, Detailed RRM Functions in Sub-Networks and Overall Systems

UNIT -IV:

Reconfiguration of the Network Elements : Introduction, Reconfiguration of Base Stations and Mobile Terminals, Abstract Modelling of Reconfigurable Devices, the Role of Local Intelligence in Reconfiguration, Performance Issues, Classification and Rating of Reconfigurable Hardware, Processing Elements, Connection Elements, Global Interconnect Networks, Hierarchical Interconnect Networks, Installing a New Configuration, Applying Reconfiguration Strategies, Reconfiguration Based on Comparison, Resource Recycling, Flexible Workload Management at the Physical Layer, Optimised Reconfiguration, Optimisation Parameters and Algorithms, Optimization Algorithms, Specific Reconfiguration Requirements, Reconfiguring Base Stations, Reconfiguring Mobile Terminals

UNIT -V:

Object – Oriented Representation of Radios and Network Resources:

Networks- Object Oriented Programming- Object Brokers- Mobile Application Environments- Joint Tactical Radio System.

Case Studies in Software Radio Design: Introduction and Historical Perspective, SPEAK easy-JTRS, Wireless Information Transfer System, SDR-3000 Digital Transceiver Subsystem, Spectrum Ware, CHARIOT.

TEXT BOOKS:

1. Software Defined Radio Architecture System and Functions- Markus Dillinger, Kambiz Madani, WILEY 2003
2. Software Defined Radio: Enabling Technologies- Walter Tuttle Bee, 2002, Wiley Publications.

REFERENCE BOOKS:

1. Software Radio: A Modern Approach to Radio Engineering - Jeffrey H. Reed, 2002, PEA Publication.
2. Software Defined Radio for 3G - Paul Burns, 2002, Artech House.
3. Software Defined Radio: Architectures, Systems and Functions - Markus Dillinger, Kambiz Madani, Nancy Alonistioti, 2003, Wiley.
4. Software Radio Architecture: Object Oriented Approaches to wireless System Engineering – Joseph Mitola, III, 2000, John Wiley & Sons.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI LABORATORY – I

Note:

- Minimum of 10 programs from Part –I and 2 programs from Part -II are to be conducted.

Design and implementation of the following CMOS digital/analog circuits using **Cadence / Mentor Graphics / Synopsys / Equivalent** CAD tools. The design shall include Gate-level design, Transistor-level design, Hierarchical design, Verilog HDL/VHDL design, Logic synthesis, Simulation and verification.

Part –I: VLSI Front End Design programs:

Programming can be done using any compiler. Down load the programs on FPGA/CPLD boards and performance testing may be done using pattern generator (32 channels) and logic analyzer apart from verification by simulation with any of the front end tools.

1. HDL code to realize all the logic gates
2. Design and Simulation of adder, Serial Binary Adder, Multi Precession Adder, Carry Look Ahead Adder.
3. Design of 2-to-4 decoder
4. Design of 8-to-3 encoder (without and with parity)
5. Design of 8-to-1 multiplexer
6. Design of 4 bit binary to gray converter
7. Design of Multiplexer/ Demultiplexer, comparator
8. Design of Full adder using 3 modeling styles
9. Design of flip flops: SR, D, JK, T
10. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset) or any sequence counter
11. Design of a N- bit Register of Serial- in Serial –out, Serial in parallel out, Parallel in Serial out and Parallel in Parallel Out.
12. Design of Sequence Detector (Finite State Machine- Mealy and Moore Machines).
13. Design of 4- Bit Multiplier, Divider.
14. Design of ALU to Perform – ADD, SUB, AND-OR, 1's and 2's Compliment, Multiplication, and Division.
15. Design of Finite State Machine.
16. Implementing the above designs on Xilinx/Altera/Cypress/equivalent based FPGA/CPLD kits .

Part –II: VLSI Back End Design programs:

Design and implementation of the following CMOS digital/analog circuits using **Cadence / Mentor Graphics / Synopsys / Equivalent** CAD tools. The design shall include Gate-level design/Transistor-level design/Hierarchical design/Verilog HDL or VHDL design, Logic synthesis, Simulation and verification, Scaling of CMOS Inverter for different technologies, study of secondary effects (temperature, power supply and process corners), Circuit optimization with respect to area, performance and/or power, Layout, Extraction of parasitics and back annotation, modifications in circuit parameters and layout consumption, DC/transient analysis, Verification of layouts (DRC, LVS).

1. Introduction to layout design rules
2. Layout, physical verification, placement & route for complex design, static timing analysis, IR drop analysis and crosstalk analysis of the following:
 - Basic logic gates
 - CMOS inverter
 - CMOS NOR/ NAND gates
 - CMOS XOR and MUX gates
 - CMOS 1-bit full adder
 - Static / Dynamic logic circuit (register cell)
 - Latch
 - Pass transistor

Layout of any combinational circuit (complex CMOS logic gate)- Learning about data paths



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech. (VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN)

COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Low Power VLSI Design	25	75	4	--	4
Core Course V	Design for Testability	25	75	4	--	4
Core Course VI	CMOS Mixed Signal Circuit Design	25	75	4	--	4
Core Elective III	VLSI and DSP Architectures Full custom Design Hardware Description Language	25	75	4	--	4
Core Elective IV	Optimization Techniques in VLSI Design System On Chip Architecture Semiconductor Memory Design and Testing	25	75	4	--	4
Open Elective II	Scripting Languages Coding Theory and Techniques Adhoc Wireless Networks	25	75	4	--	4
Laboratory II	VLSI Laboratory – II	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

LOW POWER VLSI DESIGN

UNIT –I:

Fundamentals:

Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects –Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.

UNIT –II:

Low-Power Design Approaches:

Low-Power Design through Voltage Scaling – VTCMOS circuits, MTCMOS circuits, Architectural Level Approach –Pipelining and Parallel Processing Approaches.

Switched Capacitance Minimization Approaches:

System Level Measures, Circuit Level Measures, Mask level Measures.

UNIT –III:

Low-Voltage Low-Power Adders:

Introduction, Standard Adder Cells, CMOS Adder's Architectures – Ripple Carry Adders, Carry Look-Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques –Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

UNIT –IV:

Low-Voltage Low-Power Multipliers:

Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh-Wooley Multiplier, Booth Multiplier, Introduction to Wallace Tree Multiplier.

UNIT –V:

Low-Voltage Low-Power Memories:

Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Precharge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

TEXT BOOKS:

1. CMOS Digital Integrated Circuits – Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 2011.
2. Low-Voltage, Low-Power VLSI Subsystems – Kiat-Seng Yeo, Kaushik Roy, TMH Professional Engineering.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011
2. Low Power CMOS Design – AnanthaChandrakasan, IEEE Press/Wiley International, 1998.
3. Low Power CMOS VLSI Circuit Design – Kaushik Roy, Sharat C. Prasad, John Wiley & Sons, 2000.
4. Practical Low Power Digital VLSI Design – Gary K. Yeap, Kluwer Academic Press, 2002.
5. Low Power CMOS VLSI Circuit Design – A. Bellamour, M. I. Elamasri, Kluwer Academic Press, 1995.
6. Leakage in Nanometer CMOS Technologies – Siva G. Narendran, AnathaChandrakasan, Springer, 2005.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

DESIGN FOR TESTABILITY

UNIT -I:

Introduction to Testing:

Testing Philosophy, Role of Testing, Digital and Analog VLSI Testing, VLSI Technology Trends affecting Testing, Types of Testing, Fault Modeling: Defects, Errors and Faults, Functional Versus Structural Testing, Levels of Fault Models, Single Stuck-at Fault.

UNIT -II:

Logic and Fault Simulation:

Simulation for Design Verification and Test Evaluation, Modeling Circuits for Simulation, Algorithms for True-value Simulation, Algorithms for Fault Simulation, ATPG.

UNIT -III:

Testability Measures:

SCOAP Controllability and Observability, High Level Testability Measures, Digital DFT and Scan Design: Ad-Hoc DFT Methods, Scan Design, Partial-Scan Design, Variations of Scan.

UNIT -IV:

Built-In Self-Test:

The Economic Case for BIST, Random Logic BIST: Definitions, BIST Process, Pattern Generation, Response Compaction, Built-In Logic Block Observers, Test-Per-Clock, Test-Per-Scan BIST Systems, Circular Self Test Path System, Memory BIST, Delay Fault BIST.

UNIT -V:

Boundary Scan Standard:

Motivation, System Configuration with Boundary Scan: TAP Controller and Port, Boundary Scan Test Instructions, Pin Constraints of the Standard, Boundary Scan Description Language: BSDL Description Components, Pin Descriptions.

TEXT BOOK:

1 Essentials of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits - M.L. Bushnell, V. D. Agrawal, Kluwer Academic Publishers.

REFERENCE BOOKS:

1. Digital Systems and Testable Design - M. Abramovici, M.A.Breuer and A.D Friedman, Jaico Publishing House.
2. Digital Circuits Testing and Testability - P.K. Lala, Academic Press.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

CMOS MIXED SIGNAL CIRCUIT DESIGN

UNIT -I:

Switched Capacitor Circuits:

Introduction to Switched Capacitor circuits- basic building blocks, Operation and Analysis, Non-ideal effects in switched capacitor circuits, Switched capacitor integrators first order filters, Switch sharing, biquad filters.

UNIT -II:

Phased Lock Loop (PLL):

Basic PLL topology, Dynamics of simple PLL, Charge pump PLLs-Lock acquisition, Phase/Frequency detector and charge pump, Basic charge pump PLL, Non-ideal effects in PLLs-PFD/CP non-idealities, Jitter in PLLs, Delay locked loops, applications

UNIT -III:

Data Converter Fundamentals:

DC and dynamic specifications, Quantization noise, Nyquist rate D/A converters- Decoder based converters, Binary-Scaled converters, Thermometer-code converters, Hybrid converters

UNIT -IV:

Nyquist Rate A/D Converters:

Successive approximation converters, Flash converter, Two-step A/D converters, Interpolating A/D converters, Folding A/D converters, Pipelined A/D converters, Time-interleaved converters.

UNIT -V:

Oversampling Converters:

Noise shaping modulators, Decimating filters and interpolating filters, Higher order modulators, Delta sigma modulators with multibit quantizers, Delta sigma D/A

TEXT BOOKS:

1. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition, 2002
2. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.
3. Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edition, 2013

REFERENCE BOOKS:

1. CMOS Integrated Analog-to- Digital and Digital-to-Analog converters-Rudy Van De Plassche, Kluwer Academic Publishers, 2003
2. Understanding Delta-Sigma Data converters-Richard Schreier, Wiley Interscience, 2005.
3. CMOS Mixed-Signal Circuit Design - R. Jacob Baker, Wiley Interscience, 2009.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI AND DSP ARCHITECTURES
(Core Elective –III)

UNIT I

Essential feature of Instruction set architectures of CISC, RISC and DSP processors and their implications for implementation as VLSI Chips, Micro programming approaches for implementation of control part of the processor. Assessing understanding performance, Introduction, CPU performance and its factors, evaluating performance, real stuff: Two spec bench marks and performance of recent INTEL processors, fallacies and pitfalls

UNIT II

Data Path and Control: Introduction, logic design conventions, building a data path, a simple implementation scheme, a multi cycle implementation, exceptions, micro programming: simplifying control design, an introduction to digital design using hardware description language, fallacies and pitfalls

UNIT III

Enhancing performance with pipeline: An overview of pipelining, a pipe lined data path. Pipe lined control, data hazards and forwarding, data hazards and stalls, branch hazards using a hard ware description language to describe and model a pipe line, exceptions, advanced pipelining: extracting more performance, fallacies and pitfalls

UNIT IV

Computational Accuracy in DSP implementations: Introduction, number formats for signals and coefficients in DSP system, dynamic range and precision, sources of errors in DSP implementations, A/D conversion errors, DSP computational errors, D/A conversion errors

UNIT V

Architectures for programmable digital signal processing devices: introduction, basic architectural features, DSP Computational building blocks, bus architecture and memory, data addressing capabilities, address generation unit, programmability and program execution, speed issues, features for external interfacing.

TEXT BOOKS:

1. Computer Organization and Design: Hard ware/ Software Interface-D.A, Patterson and J.L Hennessy, 4th ed., Elsevier, 2011
2. Structural Computer organization, A.S Tannenbaum, 4th ed., Prentice-Hall, 1999

REFERENCE BOOKS:

1. W. Wolf, Modern VLSI Design: System on Silicon, 2nd Ed., Person Education, 1998
2. Keshab Parhi, VLSI Digital Signal Processing system design and implementations, Wiley 1999
3. Avatar sign, Srinivasan S, Digital Signal Processing implementations using DSP microprocessors with examples, Thomson 4th reprint, 2004.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

FULL CUSTOM DESIGN
(Core Elective –III)

UNIT I

Introduction: Schematic fundamentals, Layout design, Introduction to CMOS VLSI manufacturing processes, Layers and connectivity, Process design rules Significance of full custom IC design, layout design flows.

UNIT II

Advanced techniques for specialized building blocks Standard cell libraries, Pad cells and Laser fuse cells, Advanced techniques for building blocks, Power grid Clock signals and

UNIT III

Interconnect routing. Interconnect layout design, Special electrical requirements, Layout design techniques to address electrical characteristics.

UNIT IV

Layout considerations due to process constraints Large metal via implementations, Step coverage rules, Special design rules, Latch-up and Guard rings, Constructing the pad ring, Minimizing Stress effects.

UNIT V

Proper layout CAD tools for layout, Planning tools, Layout generation tools, Support tools.

TEXT BOOKS:

1. CMOS IC Layout Concepts Methodologies and Tools, Dan Clein, Newnes, 2000.
2. The Art of Analog Layout, 2nd Edition, Ray Alan Hastings, Prentice Hall, 2006



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

HARDWARE DESCRIPTION LANGUAGE
(Core Elective –III)

UNIT I

Introduction: About VHDL, Design Flows & EDA Tools, Code Structure, Data types, Operators and Attributes: Operators, Attributes, User-Defined Attributes, Operator, overloading

UNIT II

Concurrent Code: Concurrent versus Sequential, Using Operators, WHEN, Generate and Block, Sequential Code: Process, Signals and Variables, IF, WAIT, CASE, Using Sequential, Code To Design Combinational Circuits

UNIT III

State Machines: Introduction, Design Style #1, Design Style #2 (Stored Output), Encoding Style: From Binary to OneHot

UNIT IV

Introduction to Verilog-AMS: Verilog Family of Languages, Mixed Signal Simulators, Applications of Verilog-AMS, Analog Modeling.

UNIT V

Language Reference: Basics, Data Types, Signals, Expressions, Analog Behavior

TEXT BOOKS

1. Circuit Design and Simulation with VHDL, Volnei A. Pedroni, 2nd Edition, MIT Press, 2010.
2. Designers Guide to Verilog AMS, Kenneth S Kundert, Olaf Zinke, Springer, 2004



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

OPTIMIZATION TECHNIQUES IN VLSI DESIGN
(Core Elective –IV)

UNIT –I:

Statistical Modeling:

Modeling sources of variations, Monte Carlo techniques, Process variation modeling- Pelgrom's model, Principle component based modeling, Quad tree based modeling, Performance modeling- Response surface methodology, delay modeling, interconnect delay models.

UNIT –II:

Statistical Performance, Power and Yield Analysis

Statistical timing analysis, parameter space techniques, Bayesian networks Leakage models, High level statistical analysis, Gate level statistical analysis, dynamic power, leakage power, temperature and power supply variations, High level yield estimation and gate level yield estimation.

UNIT –III:

Convex Optimization:

Convex sets, convex functions, geometric programming, trade-off and sensitivity analysis, Generalized geometric programming, geometric programming applied to digital circuit gate sizing, Floor planning, wire sizing, Approximation and fitting- Monomial fitting, Maxmonomial fitting, Posynomial fitting.

UNIT –IV:

Genetic Algorithm:

Introduction, GA Technology-Steady State Algorithm-Fitness Scaling-Inversion GA for VLSI Design, Layout and Test automation- partitioning-automatic placement, routing technology, Mapping for FPGA- Automatic test generation- Partitioning algorithm Taxonomy-Multi-way Partitioning Hybrid genetic-encoding-local improvement-WDFR Comparison of CAS-Standard cell placement-GASP algorithm-unified algorithm.

UNIT –V:

GA Routing Procedures and Power Estimation:

Global routing-FPGA technology mapping-circuit generation-test generation in a GA frame work-test generation procedures, Power estimation-application of GA-Standard cell placement-GA for ATG-problem encoding- fitness function-GA Vs Conventional algorithm.

TEXT BOOKS / REFERENCE BOOKS:

1. Statistical Analysis and Optimization for VLSI: Timing and Power - Ashish Srivastava, Dennis Sylvester, David Blaauw, Springer, 2005.
2. Genetic Algorithm for VLSI Design, Layout and Test Automation - Pinaki Mazumder, E.Mrudnick, Prentice Hall,1998.
3. Convex Optimization - Stephen Boyd, Lieven Vandenberghe, Cambridge University Press, 2004.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

SYSTEM ON CHIP ARCHITECTURE
(Core Elective –IV)

UNIT –I:

Introduction to the System Approach:

System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT –II:

Processors:

Introduction , Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT –III:

Memory Design for SOC:

Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation , SOC Memory System, Models of Simple Processor – memory interaction.

UNIT -IV:

Interconnect Customization and Configuration:

Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses , Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance- Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT –V:

Application Studies / Case Studies:

SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

TEXT BOOKS:

1. Computer System Design System-on-Chip - Michael J. Flynn and Wayne Luk, Wiley India Pvt. Ltd.
2. ARM System on Chip Architecture – Steve Furber –2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer
2. Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology) – Jason Andrews – Newnes, BK and CDROM.
3. System on Chip Verification – Methodologies and Techniques –Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

SEMICONDUCTOR MEMORY DESIGN AND TESTING
(Core Elective –IV)

UNIT -I:

Random Access Memory Technologies:

SRAM – SRAM Cell structures, MOS SRAM Architecture, MOS SRAM cell and peripheral circuit operation, Bipolar SRAM technologies, SOI technology, Advanced SRAM architectures and technologies, Application specific SRAMs, DRAM – DRAM technology development, CMOS DRAM, DRAM cell theory and advanced cell structures, BICMOS DRAM, soft error failure in DRAM, Advanced DRAM design and architecture, Application specific DRAM

UNIT -II:

Non-volatile Memories:

Masked ROMs, High density ROM, PROM, Bipolar ROM, CMOS PROMS, EPROM, Floating gate EPROM cell, One time programmable EPROM, EEPROM, EEPROM technology and architecture, Non-volatile SRAM, Flash Memories (EPROM or EEPROM), advanced Flash memory architecture

UNIT -III:

Memory Fault Modeling Testing and Memory Design for Testability and Fault Tolerance: RAM fault modeling, Electrical testing, Pseudo Random testing, Megabit DRAM Testing, non-volatile memory modeling and testing, IDDQ fault modeling and testing, Application specific memory testing, RAM fault modeling, BIST techniques for memory

UNIT -IV:

Semiconductor Memory Reliability and Radiation Effects:

General reliability issues RAM failure modes and mechanism, Non-volatile memory reliability, reliability modeling and failure rate prediction, Design for Reliability, Reliability Test Structures, Reliability Screening and qualification, Radiation effects, Single Event Phenomenon (SEP), Radiation Hardening techniques, Radiation Hardening Process and Design Issues, Radiation Hardened Memory characteristics, Radiation Hardness Assurance and Testing, Radiation Dosimetry, Water Level Radiation Testing and Test structures

UNIT -V:

Advanced Memory Technologies and High-density Memory Packing Technologies: Ferroelectric RAMs (FRAMs), GaAs FRAMs, Analog memories, magneto resistive RAMs (MRAMs), Experimental memory devices, Memory Hybrids and MCMs (2D), Memory Stacks and MCMs (3D), Memory MCM testing and reliability issues, Memory cards, High Density Memory Packaging Future Directions

TEXT BOOKS:

1. Semiconductor Memories Technology – Ashok K. Sharma, 2002, Wiley.
2. Advanced Semiconductor Memories – Architecture, Design and Applications - Ashok K. Sharma- 2002, Wiley.
3. Modern Semiconductor Devices for Integrated Circuits – Chenming C Hu, 1st Ed., Prentice Hall.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

SCRIPTING LANGUAGES
(Open Elective-II)

UNIT -I:

Introduction to Scripts and Scripting:

Characteristics and uses of scripting languages, Introduction to PERL, Names and values, Variables and assignment, Scalar expressions, Control structures, Built-in functions, Collections of Data, Working with arrays, Lists and hashes, Simple input and output, Strings, Patterns and regular expressions, Subroutines, Scripts with arguments.

UNIT -II:

Advanced PERL:

Finer points of Looping, Subroutines, Using Pack and Unpack, Working with files, Navigating the file system, Type globs, Eval, References, Data structures, Packages, Libraries and modules, Objects, Objects and modules in action, Tied variables, Interfacing to the operating systems, Security issues.

UNIT -III:

TCL:

The TCL phenomena, Philosophy, Structure, Syntax, Parser, Variables and data in TCL, Control flow, Data structures, Simple input/output, Procedures, Working with Strings, Patterns, Files and Pipes, Example code.

UNIT -IV:

Advanced TCL:

The eval, source, exec and up-level commands, Libraries and packages, Namespaces, Trapping errors, Event-driven programs, Making applications 'Internet-aware', 'Nuts-and-bolts' internet programming, Security issues, running untrusted code, The C interface.

UNIT -V:

TK and JavaScript:

Visual tool kits, Fundamental concepts of TK, TK by example, Events and bindings, Geometry managers, PERL-TK.

JavaScript – Object models, Design Philosophy, Versions of JavaScript, The Java Script core language, Basic concepts of Pythan.

Object Oriented Programming Concepts (Qualitative Concepts Only): Objects, Classes, Encapsulation, Data Hierarchy.

TEXT BOOKS:

1. The World of Scripting Languages- David Barron, Wiley Student Edition, 2010.
2. Practical Programming in Tcl and Tk - Brent Welch, Ken Jones and Jeff Hobbs., Fourth edition.
3. Java the Complete Reference - Herbert Schildt, 7th Edition, TMH.

REFERENCE BOOKS:

1. Tcl/Tk: A Developer's Guide- Clif Flynt, 2003, Morgan Kaufmann Series.
2. Tcl and the Tk Toolkit- John Ousterhout, 2nd Edition, 2009, Kindel Edition.
3. Tcl 8.5 Network Programming book- Wojciech Kocjan and Piotr Beltowski, Packt Publishing.
4. Tcl/Tk 8.5 Programming Cookbook- Bert Wheeler



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

CODING THEORY AND TECHNIQUES
(Open Elective-II)

UNIT – I:

Coding for Reliable Digital Transmission and storage

Mathematical model of Information, A Logarithmic Measure of Information, Average and Mutual Information and Entropy, Types of Errors, Error Control Strategies.

Linear Block Codes: Introduction to Linear Block Codes, Syndrome and Error Detection, Minimum Distance of a Block code, Error-Detecting and Error-correcting Capabilities of a Block code, Standard array and Syndrome Decoding, Probability of an undetected error for Linear Codes over a BSC, Hamming Codes. Applications of Block codes for Error control in data storage system

UNIT - II:

Cyclic Codes : Description, Generator and Parity-check Matrices, Encoding, Syndrome Computation and Error Detection, Decoding ,Cyclic Hamming Codes, Shortened cyclic codes, Error-trapping decoding for cyclic codes, Majority logic decoding for cyclic codes.

UNIT – III:

Convolutional Codes : Encoding of Convolutional Codes, Structural and Distance Properties, maximum likelihood decoding, Sequential decoding, Majority- logic decoding of Convolution codes. Application of Viterbi Decoding and Sequential Decoding, Applications of Convolutional codes in ARQ system.

UNIT – IV:

Turbo Codes

LDPC Codes- Codes based on sparse graphs, Decoding for binary erasure channel, Log-likelihood algebra, Brief propagation, Product codes, Iterative decoding of product codes, Concatenated convolutional codes- Parallel concatenation, The UMTS Turbo code, Serial concatenation, Parallel concatenation, Turbo decoding

UNIT - V:

Space-Time Codes

Introduction, Digital modulation schemes, Diversity, Orthogonal space- Time Block codes, Alamouti's schemes, Extension to more than Two Transmit Antennas, Simulation Results, Spatial Multiplexing : General Concept, Iterative APP Preprocessing and Per-layer Decoding, Linear Multilayer Detection, Original BLAST Detection, QL Decomposition and Interface Cancellation, Performance of Multi – Layer Detection Schemes, Unified Description by Linear Dispersion Codes.

TEXT BOOKS:

1. Error Control Coding- Fundamentals and Applications –Shu Lin, Daniel J.Costello,Jr, Prentice Hall, Inc.
2. Error Correcting Coding Theory-Man Young Rhee- 1989, McGraw-Hill

REFERENCE BOOKS:

1. Error Correcting Coding Theory-Man Young Rhee-1989,McGraw – Hill Publishing,19
2. Digital Communications-Fundamental and Application - Bernard Sklar, PE.
3. Digital Communications- John G. Proakis, 5th ed., 2008, TMH.
4. Introduction to Error Control Codes-Salvatore Gravano-oxford
5. Error Correction Coding – Mathematical Methods and Algorithms – Todd K.Moon, 2006, Wiley India.
6. Information Theory, Coding and Cryptography – Ranjan Bose, 2nd Edition, 2009, TMH.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

AD-HOC WIRELESS NETWORKS
(Open Elective-II)

UNIT - I:

Wireless Local Area Networks

Introduction, wireless LAN Topologies, Wireless LAN Requirements, Physical Layer- Infrared Physical Layer, Microwave based Physical Layer Alternatives, Medium Access Control Layer- HIPERLAN 1 Sublayer, IEEE 802.11 MAC Sublayer and Latest Developments-802.11a, 802.11b, 802.11g
Personal Area Networks: Introduction to PAN technology and Applications, Bluetooth -specifications, Radio Channel, Piconets and Scatternets, Inquiry, Paging and Link Establishment, Packet Format, Link Types, Power Management, Security, Home RF -Physical and MAC Layer

UNIT - II:

MAC Protocols

Introduction, Issues in Designing a MAC protocol for Ad Hoc Wireless Networks, Design goals of a MAC Protocol for Ad Hoc Wireless Networks, Classifications of MAC Protocols, Contention - Based Protocols, Contention - Based Protocols with reservation Mechanisms, Contention – Based MAC Protocols with Scheduling Mechanisms, MAC Protocols that use Directional Antennas, Other MAC Protocols.

UNIT - III:

Routing Protocols

Introduction, Issues in Designing a Routing Protocol for Ad Hoc Wireless Networks, Classification of Routing Protocols, Table –Driven Routing Protocols, On – Demand Routing Protocols, Hybrid Routing Protocols, Routing Protocols with Efficient Flooding Mechanisms, Hierarchical Routing Protocols, Power – Aware Routing Protocols.

UNIT – IV:

Transport Layer Protocols

Introduction, Issues in Designing a Transport Layer Protocol for Ad Hoc Wireless Networks, Design Goals of a Transport Layer Protocol for Ad Hoc Wireless Networks, Classification of Transport Layer Solutions, TCP Over Ad Hoc Wireless Networks, Other Transport Layer Protocol for Ad Hoc Wireless Networks.

UNIT – V:

Quality of Service in Ad Hoc Wireless Networks:

Introduction, Real Time Traffic Support in Ad Hoc Wireless Networks, QoS Parameters in Ad Hoc Wireless Network, Issues and Challenges in providing QoS in Ad Hoc Wireless Networks, Classification of QoS Solutions: MAC Layer Solutions, Cluster TDMA, IEEE 802.11e, DBASE, Network Layer Solutions, QoS Routing Protocols, Ticket Based QoS Routing Protocol, Predictive Location Based QoS routing protocol, Trigger Based Distributed QoS Routing Protocol, QoS enabled AODV Routing Protocol, Bandwidth QoS Routing Protocol, On Demand QoS Routing Protocol, On Demand Link-State Multipath QoS Routing Protocol, Asynchronous Slot Allocation Strategies. QoS Frameworks for Ad Hoc Wireless Networks.

TEXT BOOKS:

1. Ad Hoc Wireless Networks: Architectures and Protocols - C. Siva Ram Murthy and B.S.Manoj, 2004, PHI.
2. Wireless Networks -P Nicopolitidis and M S Obaidat, Wiley India Edition 2003.

REFERENCE BOOKS

1. Wireless Communication Technology- Roy Blake, CENGAGE,2012
2. Wireless Ad- hoc and Sensor Networks: Protocols, Performance and Control - Jagannathan Sarangapani, CRC Press.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI LABORATORY - II

Note: All the following digital/analog circuits are to be designed and implemented using Cadence / Mentor Graphics / Synopsys / Equivalent CAD tools.

The design shall include Gate-level design/Transistor-level design/Hierarchical design/Verilog HDL or VHDL design, Logic synthesis, Simulation and verification, Scaling of CMOS Inverter for different technologies, study of secondary effects (temperature, power supply and process corners), Circuit optimization with respect to area, performance and/or power, Layout, Extraction of parasitics and back annotation, modifications in circuit parameters and layout consumption, DC/transient analysis, Verification of layouts (DRC, LVS).

VLSI Back End Design programs:

1. Introduction to layout design rules
2. Layout, physical verification, placement & route for complex design, static timing analysis, IR drop analysis and crosstalk analysis of the following:
 - CMOS inverter
 - CMOS NOR/ NAND gates
 - CMOS XOR and MUX gates
 - CMOS half adder and full adder
 - Static / Dynamic logic circuits (register cell)
 - Latch
 - Pass transistor
3. Layout of any combinational circuit (complex CMOS logic gate)- Learning about data paths
4. Introduction to SPICE simulation and coding of NMOS/CMOS circuit
5. SPICE simulation of basic analog circuits: Inverter / Differential amplifier
6. Analog Circuit simulation (AC analysis) – CS & CD amplifier
7. System level design using PLL



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech (WIRELESS AND MOBILE COMMUNICATIONS)
COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Advanced Data Communications	25	75	4	--	4
Core Course II	Coding Theory and Techniques	25	75	4	--	4
Core Course III	Spread Spectrum Communication	25	75	4	--	4
Core Elective I	Detection and Estimation Theory Random Processes and Time Series Analysis RF Circuit Design	25	75	4	--	4
Core Elective II	Voice Over Internet Protocol Queueing Theory and Applications GPS Applications	25	75	4	--	4
Open Elective I	Image and Video Processing Internetworking Advanced Digital Signal Processing	25	75	4	--	4
Laboratory I	Advanced Communications Lab	25	75	--	4	2
Seminar I	Seminar	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Advanced communications & Networks	25	75	4	--	4
Core Course V	Wireless Communications & Networks	25	75	4	--	4
Core Course VI	Wireless MIMO Communications	25	75	4	--	4
Core Elective III	Optical Communications Technology Wireless LANs and PANs Adhoc and Wireless Sensor Networks	25	75	4	--	4
Core Elective IV	Network Security and Cryptography Software Defined Radio 4G Technologies	25	75	4	--	4
Open Elective II	Embedded system Design Mobile Computing Technologies Scripting Languages	25	75	4	--	4
Laboratory II	Wireless Communications and Networks Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C
Comprehensive Viva-Voce	--	100	--	--	4
Project work Review I	50	--	--	24	12
Total Credits			--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C
Project work Review II	50	--	--	8	4
Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits			--	24	16



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

ADVANCED DATA COMMUNICATIONS

Unit I

Data Communications, Networks and Network Types, Internet History, Standards and Administration, Protocol Layering, TCP/IP protocol suite, OSI Model. Digital Data Transmission, DTE-DCE interface.

Data Link Layer

Introduction, Data Link Layer, Nodes and Links, Services, Categories of Links, sub layers, Link Layer Addressing, Address Resolution Protocol.

Unit II

Error Detection and Correction: Types of Errors, Redundancy, detection versus correction, Coding Block Coding: Error Detection, Vertical redundancy checks, longitudinal redundancy checks, Error Correction, Error correction single bit, Hamming code.

Cyclic Codes: Cyclic Redundancy Check, Polynomials, Cyclic Code Encoder Using Polynomials, Cyclic Code Analysis, Advantage of Cyclic Codes, Checksum

Data Link Control: DLC Services, Data Link Layer Protocols, HDLC, Point to Point Protocol

Unit III

Switching: Introduction to Switching, Circuit Switched Networks, Packet Switching, Structure of switch

Multiplexing : Multiplexing, Frequency Division Multiplexing, Time Division Multiplexing.

Connecting devices: Passive Hubs, Repeaters, Active Hubs, Bridges, Two Layer Switches, Routers, Three Layer Switches, Gateway, Backbone Networks.

Wired LANS: Ethernet Protocol, Standard Ethernet, Fast Ethernet, Gigabit Ethernet, 10 Giga bit Ethernet

Unit IV

Media Access Control (MAC) Sub Layer

Random Access, ALOHA, Carrier Sense Multiple Access (CSMA), Carrier Sense Multiple Access with Collision Detection (CSMA/CD), Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA), Controlled Access- Reservation, Polling- Token Passing, Channelization - Frequency Division Multiple Access (FDMA), Time - Division Multiple Access (TDMA), Code - Division Multiple Access (CDMA).

Spectrum Spreading: Spread Spectrum-Frequency Hopping Spread Spectrum and Direct Sequence Spread Spectrum.

Unit V

Networks Layer: Packetizing, Routing and Forwarding, Packet Switching, Network Layer Performance, IPv4 Address, Address Space, Classful Addressing, Classless Addressing, Dynamic Host Configuration Protocol (DHCP), Network Address Resolution (NATF), Forwarding of IP Packets, Forwarding based on Destination Address, Forwarding based on Label, Routing as Packet Switches.

Unicast Routing : Introduction, **Routing Algorithms**-Distance Vector Routing, Link State Routing, Path Vector Routing, **Unicast Routing Protocols**- Routing Information Protocol (RIP), Open Short Path First Version 4.

TEXT BOOKS:

1. Data Communications and Networking - B. A. Forouzan, 5th, 2013, TMH.
2. Data and Computer Communications - William Stallings, 8th ed., 2007, PHI.

REFERENCE BOOKS:

1. Data Communications and Computer Networks - Prakash C. Gupta, 2006, PHI.
2. Data Communications and Networking - B. A. Forouzan, 2nd, 2013, TMH.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

CODING THEORY AND TECHNIQUES

UNIT – I:

Coding for Reliable Digital Transmission and storage

Mathematical model of Information, A Logarithmic Measure of Information, Average and Mutual Information and Entropy, Types of Errors, Error Control Strategies.

Linear Block Codes: Introduction to Linear Block Codes, Syndrome and Error Detection, Minimum Distance of a Block code, Error-Detecting and Error-correcting Capabilities of a Block code, Standard array and Syndrome Decoding, Probability of an undetected error for Linear Codes over a BSC, Hamming Codes. Applications of Block codes for Error control in data storage system

UNIT - II:

Cyclic Codes : Description, Generator and Parity-check Matrices, Encoding, Syndrome Computation and Error Detection, Decoding ,Cyclic Hamming Codes, Shortened cyclic codes, Error-trapping decoding for cyclic codes, Majority logic decoding for cyclic codes.

UNIT – III:

Convolutional Codes : Encoding of Convolutional Codes, Structural and Distance Properties, maximum likelihood decoding, Sequential decoding, Majority- logic decoding of Convolution codes. Application of Viterbi Decoding and Sequential Decoding, Applications of Convolutional codes in ARQ system.

UNIT – IV:

Turbo Codes

LDPC Codes- Codes based on sparse graphs, Decoding for binary erasure channel, Log-likelihood algebra, Brief propagation, Product codes, Iterative decoding of product codes, Concatenated convolutional codes- Parallel concatenation, The UMTS Turbo code, Serial concatenation, Parallel concatenation, Turbo decoding

UNIT - V:

Space-Time Codes

Introduction, Digital modulation schemes, Diversity, Orthogonal space- Time Block codes, Alamouti's schemes, Extension to more than Two Transmit Antennas, Simulation Results, Spatial Multiplexing : General Concept, Iterative APP Preprocessing and Per-layer Decoding, Linear Multilayer Detection, Original BLAST Detection, QL Decomposition and Interface Cancellation, Performance of Multi – Layer Detection Schemes, Unified Description by Linear Dispersion Codes.

TEXT BOOKS:

1. Error Control Coding- Fundamentals and Applications –Shu Lin, Daniel J.Costello,Jr, Prentice Hall, Inc.
2. Error Correcting Coding Theory-Man Young Rhee- 1989, McGraw-Hill

REFERENCE BOOKS:

1. Error Correcting Coding Theory-Man Young Rhee-1989,McGraw – Hill Publishing,19
2. Digital Communications-Fundamental and Application - Bernard Sklar, PE.
3. Digital Communications- John G. Proakis, 5th ed., 2008, TMH.
4. Introduction to Error Control Codes-Salvatore Gravano-oxford
5. Error Correction Coding – Mathematical Methods and Algorithms – Todd K.Moon, 2006, Wiley India.
6. Information Theory, Coding and Cryptography – Ranjan Bose, 2nd Edition, 2009, TMH.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

SPREAD SPECTRUM COMMUNICATION

UNIT -I:

Introduction to Spread Spectrum Systems: Fundamental Concepts of Spread Spectrum Systems, Pseudo Noise Sequences, Direct Sequence Spread Spectrum, Frequency Hop Spread Spectrum, Hybrid Direct Sequence Frequency Hop Spread Spectrum, Code Division Multiple Access.

Binary Shift Register Sequences for Spread Spectrum Systems:

Introduction, Definitions, Mathematical Background and Sequence Generator Fundamentals, Maximal Length Sequences, Gold Codes.

UNIT -II:

Code Tracking Loops: Introduction, Optimum Tracking of Wideband Signals, Base Band Delay-Lock Tracking Loop, Tau-Dither Non-Coherent Tracking Loop, Double Dither Non-Coherent Tracking Loop.

UNIT -III:

Initial Synchronization of the Receiver Spreading Code: Introduction, Problem Definition and the Optimum Synchronizer, Serial Search Synchronization Techniques, Synchronization using a Matched Filter, Synchronization by Estimated the Received Spreading Code.

UNIT -IV:

Cellular Code Division Multiple Access (CDMA) Principles: Introduction, Wide Band Mobile Channel, The Cellular CDMA System, Single User Receiver in a Multi User Channel, CDMA System Capacity,

Multi-User Detection in CDMA Cellular Radio: Optimal Multi-User Detection, Linear Suboptimal Detectors, Interference Combat Detection Schemes, Interference Cancellation Techniques.

UNIT -V:

Performance of Spread Spectrum Systems in Jamming Environments: Spread Spectrum Communication System Model, Performance of Spread Spectrum Systems without Coding.

Performance of Spread Spectrum Systems with Forward Error Correction: Elementary Block Coding Concepts, Optimum Decoding Rule, Calculation of Error Probability, Elementary Convolution Coding Concepts, Viterbi Algorithm, Decoding and Bit-Error Rate.

TEXT BOOKS:

1. Rodger E Ziemer, Roger L. Peterson and David E Borth - "Introduction to Spread Spectrum Communication- Pearson, 1st Edition, 1995.
2. Mosa Ali Abu-Rgheff – "Introduction to CDMA Wireless Communications." Elsevier Publications, 2008.

REFERENCE BOOKS:

1. George R. Cooper, Clare D. Mc Gillem - "Modern Communication and Spread Spectrum," McGraw Hill, 1986.
2. Andrew j. Viterbi - "CDMA: Principles of spread spectrum communication," Pearson Education, 1st Edition, 1995.
3. Kamillo Feher - "Wireless Digital Communications," PHI, 2009.
4. Andrew Richardson - "WCDMA Design Handbook," Cambridge University Press, 2005.
5. Steve Lee - Spread Spectrum CDMA, McGraw Hill, 2002.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

DETECTION AND ESTIMATION THEORY
(Core Elective –I)

UNIT –I:

Random Processes: Discrete Linear Models, Markov Sequences and Processes, Point Processes, and Gaussian Processes.

UNIT –II:

Detection Theory: Basic Detection Problem, Maximum A posteriori Decision Rule, Minimum Probability of Error Classifier, Bayes Decision Rule, Multiple-Class Problem (Bayes)- minimum probability error with and without equal a priori probabilities, Neyman-Pearson Classifier, General Calculation of Probability of Error, General Gaussian Problem, Composite Hypotheses.

UNIT –III:

Linear Minimum Mean-Square Error Filtering: Linear Minimum Mean Squared Error Estimators, Nonlinear Minimum Mean Squared Error Estimators. Innovations, Digital Wiener Filters with Stored Data, Real-time Digital Wiener Filters, Kalman Filters.

UNIT –IV:

Statistics: Measurements, Nonparametric Estimators of Probability Distribution and Density Functions, Point Estimators of Parameters, Measures of the Quality of Estimators, Introduction to Interval Estimates, Distribution of Estimators, Tests of Hypotheses, Simple Linear Regression, Multiple Linear Regression.

UNIT –V:

Estimating the Parameters of Random Processes from Data: Tests for Stationarity and Ergodicity, Model-free Estimation, Model-based Estimation of Autocorrelation Functions, Power Spectral Density Functions.

TEXT BOOKS:

1. Random Signals: Detection, Estimation and Data Analysis - K. Sam Shanmugan & A.M. Breipohl, Wiley India Pvt. Ltd, 2011.
2. Random Processes: Filtering, Estimation and Detection - Lonnie C. Ludeman, Wiley India Pvt. Ltd., 2010.

REFERENCE BOOKS:

1. Fundamentals of Statistical Signal Processing: Volume I Estimation Theory– Steven.M.Kay, Prentice Hall, USA, 1998.
2. Fundamentals of Statistical Signal Processing: Volume I Detection Theory– Steven.M.Kay, Prentice Hall, USA, 1998.
3. Introduction to Statistical Signal Processing with Applications - Srinath, Rajasekaran, Viswanathan, 2003, PHI.
4. Statistical Signal Processing: Detection, Estimation and Time Series Analysis – Louis L.Scharf, 1991, Addison Wesley.
5. Detection, Estimation and Modulation Theory: Part – I – Harry L. Van Trees, 2001, John Wiley & Sons, USA.
6. Signal Processing: Discrete Spectral Analysis – Detection & Estimation – Mischa Schwartz, Leonard Shaw, 1975, Mc Graw Hill.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

RANDOM PROCESSES AND TIME SERIES ANALYSIS
 (Core Elective –I)

UNIT -I:

Stationary Random Processes from a Probability Point of View: Probability Density and Probability Distribution Functions of a Random Variable, Expected Value of Random Variable, Markov and Chebyshev Inequalities, Computer Methods for Generating Random Variables, Multidimensional Random variables, Chi-square tests of hypotheses concerning distribution.

UNIT -II:

Random Processes Analyzed in the Time Domain: Continuous and Discrete Time, Stationarity, Auto Covariance and Auto Correlation functions, Continuity, differentiation, Integrals of Random Processes.

Some special cases: The Poisson process, the Normal (Gaussian) Process.

UNIT -III:

Random Processes Analyzed in the Frequency Domain: The Fourier Transform, Spectral Density, The Cross Power Spectral Density.

Linear Systems with random input: Impulse response, Transfer function, the relation between the spectral density for the input and for the output

UNIT -IV:

Markov Chains: Markov Processes: Discrete time Markov chains, state transition probability matrix, n-step state transition probability, transition diagrams, classification of states, limiting state probabilities, Continuous-time Markov chains, Gambler's ruin as a Markov chains

UNIT -V:

Basic Queuing Theory: Elements of a Queueing System, Little's Formula, M/M/1, Queue- Delay Distribution in M/M/1 System, M/M/1 System with Finite Capacity, M/G/1 Queueing system- Residual Service Time, Mean Delay in M/G/1 Systems.

TEXT BOOKS:

1. Probability, Random Variables, and Random Signal Principles – Peebles, P. Z (1993)- Third edition or later – New York – McGraw-Hill
2. Fundamentals of Applied Probability and Random Processes – Oliver C. Ibe, Elsevier, 2009
3. Probability and Random Processes for Electrical Engineering - Alberto Leon-Garcia, 2nd Ed, Pearson

REFERENCE BOOKS:

1. Probability, Random Variables and Stochastic Processes – Athanasios Papoulis, S. Unnikrishna Pillai – TMH, 2008
2. Probability and Random Processes with Applications to Signal Processing – Henry Stark, John W. Woods, 3rd Edition, Pearson
3. Probability and Stochastic Processes – A Friendly Introduction for Electrical and Computer Engineers – Roy D. Yates, David J. Goodman



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

RF CIRCUIT DESIGN
(Core Elective –I)

UNIT -I:

Introduction to RF Electronics: The Electromagnetic Spectrum, units and Physical Constants, Microwave bands – RF behavior of Passive components: Tuned resonant circuits, Vectors, Inductors and Capacitors - Voltage and Current in capacitor circuits – Tuned RF / IF Transformers.

UNIT -II:

Transmission Line Analysis: Examples of transmission lines- Transmission line equations and Biasing- Micro Strip Transmission Lines- Special Termination Conditions- sourced and Loaded Transmission Lines.

Single And Multiport Networks: The Smith Chart, Interconnectivity networks, Network properties and Applications, Scattering Parameters.

UNIT- III:

Matching and Biasing Networks: Impedance matching using discrete components – Micro strip line matching networks, Amplifier classes of Operation and Biasing networks.

RF Passive & Active Components: Filter Basics – Lumped filter design – Distributed Filter Design – Diplexer Filters- Crystal and Saw filters- Active Filters - Tunable filters – Power Combiners / Dividers – Directional Couplers – Hybrid Couplers – Isolators. RF Diodes – BJTs- FETs- HEMTs and Models.

UNIT -IV:

RF Transistor Amplifier Design: Characteristics of Amplifiers - Amplifier Circuit Configurations, Amplifier Matching Basics, Distortion and noise products, Stability Considerations, Small Signal amplifier design, Power amplifier design, MMIC amplifiers, Broadband High Power multistage amplifiers, Low noise amplifiers, VGA Amplifiers.

UNIT -V:

Oscillators: Oscillator basics, Low phase noise oscillator design, High frequency Oscillator configuration, LC Oscillators, VCOs, Crystal Oscillators, PLL Synthesizer, and Direct Digital Synthesizer.

RF Mixers: Basic characteristics of a mixer - Active mixers- Image Reject and Harmonic mixers, Frequency domain considerations.

TEXT BOOKS:

1. RF Circuit design: Theory and applications by Reinhold Ludwig, Pavel Bretchko. Pearson Education Asia Publication, New Delhi 2001.
2. Radio Frequency and Microwave Communication Circuits – Analysis and Design – Devendra K. Misra, Wiley Student Edition, John Wiley & Sons

REFERENCE BOOKS:

1. Radio frequency and microwave electronics illustrated Mathew M.Radmangh, 2001, PE Asia Publication.
2. RF Circuit Design – Christopher Bowick, Cheryl Aljuni and John Biyler, Elsevier Science, 2008.
3. Secrets of RF Design by Joseph Carr., 3rd Edition, Tab Electronics.
4. Complete Wireless Design by Cotter W. Sawyer, 2nd Edition, Mc-Graw Hill.
5. Practical RF Circuit Design for Modern Wireless Systems Vol.2 by Less Besser and Rowan Gilmore.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

VOICE OVER INTERNET PROTOCOL (VoIP)
(Core Elective –II)

UNIT –I:

Overview of IP Protocol Suite: The Internet Protocol, The Transmission Control Protocol(TCP), The User Datagram Protocol (UDP), The Real-time Transport Protocol (RTP), IP multicast, IP version 6 (IP v6), Interworking IPv4 and IPv6, The VoIP Market, VoIP Challenges.

UNIT -II:

H.323 and H.245 Standards: The H.323 Architecture, Call Signaling-Call Scenarios, H.245 Control Signaling Conference calls- The Decomposed Gateway.

UNIT –III:

The Session Initiation Protocol (SIP): SIP architecture- Overview of SIP Messaging Syntax- Examples of SIP Message sequences- Redirect Servers- Proxy Servers. The Session Description Protocol (SDP)- Usage of SDP With SIP.

UNIT -IV:

Quality of Service (QoS): Need for QOS – End-to-end QoS, Overview of QOS solutions- The Resource reservation Protocol (RSVP)- Diffserv- The Diffserv Architecture- Multi-protocol Label Switching (MPLS)- The MPLS Architecture- MPLS Traffic Engineering- Label Distribution Protocols and Constraint- Based Routing.

UNIT -V:

VoIP and SS7: The SS7 Protocol Suite- The Message Transfer Part (MTP), ISDN User Part (ISUP) and Signaling Connection Control Part (SCCP), SS7 Network Architecture- Signaling Points(SPs)- Single Transfer Point (STP), - Service Control Point(SCP)- Message Signal Units (MSUs)- SS7 Addressing, ISUP, Performance Requirements for SS7, Sigtran- Sigtran Architecture- SCTP- M3UA Operation- M2UA Operation- M2PA Operation- Interworking SS7 and VoIP Architectures- Interworking Soft switch and SS7- Interworking H.323 and SS7.

TEXT BOOK:

Carrier Grade Voice over IP – Daniel Collins, 2nd ed., TMH.

REFERENCE BOOKS:

- 1 Understanding Voice over IP Technology – Nicholas Wittenberg – Cengage, 1st Ed., 2010.
- 2 Voice Over WLANS – The Complete Guide – Michael, F. Finnevan, Elsevier, 2008.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

QUEUEING THEORY AND APPLICATIONS

(Core Elective –II)

Unit I

Review of probability, Stochastic Processes, random variables, distributions, generating functions; Poisson, Markov, renewal and semi-Markov processes, and Markov Chains, Birth-Death Process

Unit II

Basic Queueing Theory

An Introduction to Queues and Queueing Theory, Characteristics of queueing systems, M/M/1 queueing system, Little's law, Reversibility and Burke's theorem, Markovian and non-Markovian queueing systems, embedded Markov chain applications to M/G/1, G/M/1 and related queueing systems;

Unit III

Queueing Networks

Fundamentals of Queueing Networks, Networks of queues, Open and Closed Queueing Networks, Open Networks of M/M/m type queues and Jackson's Theorem, MVA and Convolution Algorithm for Closed Networks, Approximate Models for Open and Closed Queueing Networks, Queues with vacations, priority queues, queues with modulated arrival process,

Unit IV

Discrete time queueing Systems-Introduction, Discrete time queueing systems, discrete time arrival process, Geom/Geom/m/N queueing system, Queueing on a Space division packet switch, Queueing on a single buffered banyan network

Unit V

Network traffic Modeling - Introduction, Continuous time models, Discrete time Models Solution methods, Burstiness, self similar traffic

Text books:

1. D. Gross and C. Harris, *Fundamentals of Queueing Theory*, 3rd Edition, Wiley, 1998. (WSE Edition, 2004).
2. T.G. Robertazzi, *Computer Networks and Systems - Queueing Theory and Performance Evaluation*, Springer 2000.

Reference Books:

1. L. Kleinrock, *Queueing Systems, Vol. 1: Theory*, Wiley, 1975.
2. E. Gelenbe and G. Pujolle, *Introduction to Queueing Networks*, 2nd Edition, Wiley, 1998.
3. J. Medhi, *Stochastic Models in Queueing Theory*, 2nd Edition, Academic Press, 2003. (Elsevier India Edition, 2006).
4. L. Kleinrock, *Queueing Systems Volume 1: Theory*, Wiley 1975.
5. R. Nelson, *Probability, Stochastic Processes, and Queueing Theory: The Mathematics of Computer Performance Modelling*, Springer, 1995.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

GPS AND APPLICATIONS
 (Core Elective –II)

UNIT I
INTRODUCTION

Basic concept, system architecture, GPS and GLONASS Overview, Satellite Navigation, Time and GPS, User position and velocity calculations, GPS, Satellite Constellation, Operation Segment, User receiving Equipment, Space Segment Phased development, GPS aided Geoaugmented navigation (GAGAN) architecture.

UNIT II
SIGNAL CHARACTERISTICS

GPS signal components, purpose, properties and power level, signal acquisition and tracking , Navigation information extraction, pseudorange estimation, frequency estimation, GPS satellite position calculation, Signal structure, anti spoofing (AS), selective availability, Difference between GPS and GALILEO satellite construction.

UNIT III,
GPS RECEIVERS & DATA ERRORS

Receiver Architecture, receiver design options, Antenna design, GPS error sources, SA errors, propagation errors, ionospheric error, tropospheric error, multipath, ionospheric error, estimation using dual frequency GPS receiver, Methods of multipath mitigation, Ephemeris data errors, clock errors.

UNIT IV
DIFFERENTIAL GPS

Introduction, LADGPS, WADGPS, Wide Area Augmentation systems , GEO Uplink subsystem , GEO downlink systems , Geo Orbit determination , Geometric analysis , covariance analysis , GPS /INS Integration Architectures

UNIT V
GPS APPLICATIONS

GPS in surveying, Mapping and Geographical Information System, Precision approach Aircraft landing system, Military and Space application, intelligent transportation system. GPS orbital parameters, description of receiver independent exchange format (RINEX) , Observation data and navigation message data parameters, GPS position determination, least squares method

TEXT BOOKS

1. Mohinder S.Grewal, Lawrence R.Weill, Angus P.Andrews, "*Global positioning systems , Inertial Navigation and Integration*", John Wiley & sons, 2007.
2. *Global Navigation Satellite System*, Gottapu Sasibhuhsana Rao , McGraw-Hill Education, 2010.

REFERENCES

1. E.D.Kaplan, Christopher J. Hegarty, "*Understanding GPS Principles and Applications*", Artech House Boston 2005.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

IMAGE AND VIDEO PROCESSING
(OPEN ELECTIVE - I)

UNIT –I:

Fundamentals of Image Processing and Image Transforms: Basic steps of Image Processing System Sampling and Quantization of an image, Basic relationship between pixels.

Image Segmentation: Segmentation concepts, Point, Line and Edge Detection, Thresholding, Region based segmentation.

UNIT –II:

Image Enhancement: Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters.

Frequency domain methods: Basics of filtering in frequency domain, image smoothing, image sharpening, Selective filtering.

UNIT –III:

Image Compression: Image compression fundamentals - Coding Redundancy, Spatial and Temporal redundancy, Compression models: Lossy & Lossless, Huffman coding, , Bit plane coding, Transform coding, Predictive coding, Wavelet coding, Lossy Predictive coding, JPEG Standards.

UNIT -IV:

Basic Steps of Video Processing: Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.

UNIT –V:

2-D Motion Estimation: Optical flow, General Methodologies, Pixel Based Motion Estimation, Block-Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.

TEXT BOOKS:

1. Digital Image Processing – Gonzaleze and Woods, 3rd Ed., Pearson.
2. Video Processing and Communication – Yao Wang, Joem Ostermann and Ya–quin Zhang. 1st Ed., PH Int.

REFERENCE BOOKS:

1. Digital Image Processing and Analysis-Human and Computer Vision Application with CVIP Tools – Scotte Umbaugh, 2nd Ed, CRC Press, 2011.
2. Digital Video Processing – M. Tekalp, Prentice Hall International.
3. Digital Image Processing – S.Jayaraman, S.Esakkirajan, T.Veera Kumar – TMH, 2009.
4. Multidimentional Signal, Image and Video Processing and Coding – John Woods, 2nd Ed, Elsevier.
5. Digital Image Processing with MATLAB and Labview – Vipula Singh, Elsevier.
6. Video Demystified – A Hand Book for the Digital Engineer – Keith Jack, 5th Ed., Elsevier.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

INTERNETWORKING
(Open Elective –I)

UNIT -I:

Internetworking Concepts: Principles of Internetworking, Connectionless Internetworking, Application level Interconnections, Network level Interconnection, Properties of the Internet, Internet Architecture, Wired LANS, Wireless LANS, Point-to-Point WANs, Switched WANs, Connecting Devices, TCP/IP Protocol Suite.

IP Address: Classful Addressing: Introduction, Classful Addressing, Other Issues, Sub-netting and Super-netting

Classless Addressing: Variable length Blocks, Sub-netting, Address Allocation. Delivery, Forwarding, and Routing of IP Packets: Delivery, Forwarding, Routing, Structure of Router.

ARP and RARP: ARP, ARP Package, RARP.

UNIT -II:

Internet Protocol (IP): Datagram, Fragmentation, Options, Checksum, IP V.6.

Transmission Control Protocol (TCP): TCP Services, TCP Features, Segment, A TCP Connection, State Transition Diagram, Flow Control, Error Control, Congestion Control, TCP Times.

Stream Control Transmission Protocol (SCTP): SCTP Services, SCTP Features, Packet Format, Flow Control, Error Control, Congestion Control.

Mobile IP: Addressing, Agents, Three Phases, Inefficiency in Mobile IP.

Classical TCP Improvements: Indirect TCP, Snooping TCP, Mobile TCP, Fast Retransmit/ Fast Recovery, Transmission/ Time Out Freezing, Selective Retransmission, Transaction Oriented TCP.

UNIT -III:

Unicast Routing Protocols (RIP, OSPF, and BGP: Intra and Inter-domain Routing, Distance Vector Routing, RIP, Link State Routing, OSPF, Path Vector Routing, BGP.

Multicasting and Multicast Routing Protocols: Unicast - Multicast- Broadcast, Multicast Applications, Multicast Routing, Multicast Link State Routing: MOSPF, Multicast Distance Vector: DVMRP.

UNIT -IV:

Domain Name System (DNS): Name Space, Domain Name Space, Distribution of Name Space, and DNS in the internet.

Remote Login TELNET: Concept, Network Virtual Terminal (NVT).

File Transfer FTP and TFTP: File Transfer Protocol (FTP).

Electronic Mail: SMTP and POP.

Network Management-SNMP: Concept, Management Components, World Wide Web- HTTP Architecture.

UNIT -V:

Multimedia: Digitizing Audio and Video, Network security, security in the internet firewalls. Audio and Video Compression, Streaming Stored Audio/Video, Streaming Live Audio/Video, Real-Time Interactive Audio/Video, RTP, RTCP, Voice Over IP. Network Security, Security in the Internet, Firewalls.

TEXT BOOKS:

1. TCP/IP Protocol Suite- Behrouz A. Forouzan, Third Edition, TMH
2. Internetworking with TCP/IP Comer 6th Edition PHI, Volume -1.

REFERENCE BOOKS:

1. Mobile Communications, Jochen Schiller, 2nd edition, Pearson Education 2003.
2. Data Communications & Networking – B.A. Forouzan – 4nd Edition – TMH
3. High Speed Networks and Internets- William Stallings, Pearson Education, 2002.
4. Data and Computer Communications, William Stallings, 7th Edition., PEI.
5. The Internet and Its Protocols – Adrin Farrel, Elsevier, 2005.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

ADVANCED DIGITAL SIGNAL PROCESSING
(OPEN ELECTIVE - I)

UNIT –I:

Review of DFT, FFT, IIR Filters and FIR Filters:

Multi Rate Signal Processing: Introduction, Decimation by a factor D , Interpolation by a factor I , Sampling rate conversion by a rational factor I/D , Multistage Implementation of Sampling Rate Conversion, Filter design & Implementation for sampling rate conversion.

UNIT –II:

Applications of Multi Rate Signal Processing: Design of Phase Shifters, Interfacing of Digital Systems with Different Sampling Rates, Implementation of Narrow Band Low Pass Filters, Implementation of Digital Filter Banks, Subband Coding of Speech Signals, Quadrature Mirror Filters, Transmultiplexers, Over Sampling A/D and D/A Conversion.

UNIT -III:

Non-Parametric Methods of Power Spectral Estimation: of spectra from finite duration observation of signals, Non-parametric Methods: Bartlett, Welch & Blackman-Tukey methods, Comparison of all Non-Parametric methods

UNIT –IV:

Implementation of Digital Filters: Introduction to filter structures (IIR & FIR), Frequency sampling structures of FIR, Lattice structures, Forward prediction error, Backward prediction error, Reflection coefficients for lattice realization, Implementation of lattice structures for IIR filters, Advantages of lattice structures.

UNIT –V:

Parametric Methods of Power Spectrum Estimation: Autocorrelation & Its Properties, Relation between auto correlation & model parameters, AR Models - Yule-Walker & Burg Methods, MA & ARMA models for power spectrum estimation, Finite word length effect in IIR digital Filters – Finite word-length effects in FFT algorithms.

TEXT BOOKS:

1. Digital Signal Processing: Principles, Algorithms & Applications - J.G.Proakis & D. G. Manolakis, 4th Ed., PHI.
2. Discrete Time Signal Processing - Alan V Oppenheim & R. W Schaffer, PHI.
3. DSP – A Practical Approach – Emmanuel C. Ifeachor, Barrie. W. Jervis, 2 ed., Pearson Education.

REFERENCE BOOKS:

1. Modern Spectral Estimation: Theory & Application – S. M .Kay, 1988, PHI.
2. Multi Rate Systems and Filter Banks – P.P.Vaidyanathan – Pearson Education.
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj, C.Gnanapriya, 2000,TMH.
4. Digital Spectral Analysis – Jr. Marple



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – I Sem. (WMC)

ADVANCED COMMUNICATIONS LAB

Note:

- A. Minimum of 10 Experiments have to be conducted
- B. All Experiments may be Simulated using MATLAB and to be verified using related training kits.

1. Determination of output of convolutional Encoder for a given sequence
2. Determination of output of convolutional Decoder for a given sequence
3. Efficiency of DS Spread- Spectrum Technique
4. Simulation of Frequency Hopping (FH) Spread- Spectrum
5. Implementation of Matched Filters.
6. Optimum receiver for the AWGN channel.
7. Measurement of effect of Inter Symbol Interference..
8. Simulation of PSK system with M=4
9. Simulation of DPSK system with M=4
10. Design of FSK system
11. BPSK Modulation and Demodulation techniques
12. QPSK Modulation and Demodulation techniques
13. DQPSK Modulation and Demodulation techniques
14. 8-QAM Modulation and Demodulation techniques
15. OFDM Transceiver design
16. Performance evaluation of simulated CDMA system



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech (WIRELESS AND MOBILE COMMUNICATIONS)
COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Advanced communications & Networks	25	75	4	--	4
Core Course V	Wireless Communications & Networks	25	75	4	--	4
Core Course VI	Wireless MIMO Communications	25	75	4	--	4
Core Elective III	Optical Communications Technology Wireless LANs and PANs Adhoc and Wireless Sensor Networks	25	75	4	--	4
Core Elective IV	Network Security and Cryptography Software Defined Radio 4G Technologies	25	75	4	--	4
Open Elective II	Embedded system Design Mobile Computing Technologies Scripting Languages	25	75	4	--	4
Laboratory II	Wireless Communications and Networks Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

ADVANCED COMMUNICATIONS AND NETWORKS

UNIT I

Spread Spectrum Communications

Spreading sequences- Properties of Spreading Sequences, Pseudo- noise sequence, Gold sequences, Kasami sequences, Walsh Sequences, Orthogonal Variable Spreading Factor Sequences, Barker Sequence, Complementary Codes
Direct sequence spread spectrum – DS-CDMA Model, Conventional receiver, Rake Receiver, Synchronization in CDMA, Power Control, Soft handoff, Multiuser detection – Optimum multiuser detector, Linear multiuser detection.

UNIT II

Orthogonal Frequency Division Multiplexing

Basic Principles of Orthogonality, Single vs Multicarrier Systems, OFDM Block Diagram and Its Explanation, OFDM Signal Mathematical Representation, Selection parameter for Modulation, Pulse shaping in OFDM Signal and Spectral Efficiency, Window in OFDM Signal and Spectrum, Synchronization in OFDM, Pilot Insert in OFDM Transmission and Channel Estimation, Amplitude Limitations in OFDM, FFT Point Selection Constraints in OFDM, CDMA vs OFDM, Hybrid OFDM.

UNIT III

MIMO Systems

Introduction, Space Diversity and System Based on Space Diversity, Smart Antenna system and MIMO, MIMO Based System Architecture, MIMO Exploits Multipath, Space – Time Processing, Antenna Consideration for MIMO, MIMO Channel Modelling, MIMO Channel Measurement, MIMO Channel Capacity, Cyclic Delay Diversity (CDD), Space Time Coding, Advantages and Applications of MIMO in Present Context, MIMO Applications in 3G Wireless System and Beyond, MIMO-OFDM

UNIT IV

Wireless LANs/IEEE 802.11x: Introduction to IEEE802.11x Technologies, Evolution of wireless LANs, IEEE 802.11 Design Issues, IEEE 802.11 Services, IEEE 802.11 MAC Layer operations, IEEE 802.11 Layer1, IEEE 802.11 a/b/g Higher Rate Standards, Wireless LAN Security, Computing Wireless Technologies, Typical WLAN Hardware

UNIT V

Wireless PANs/IEEE 802.15x: Introduction to IEEE 802.15x Technologies,

Wireless PAN Applications and Architecture, IEEE 802.15.1 Physical Layer Details, Bluetooth Link Controllers Basics, Bluetooth Link Controllers Operational States, IEEE 802.15.1 Protocols and Host Control Interface. Evaluation of IEEE 802.15 Standards

Broad Band Wireless MANs/IEEE 802.16x

Introduction to WMAN/IEEE 802.16x Technology, IEEE 802.16Wireless MANs, IEEE 802.16 MAC Layer Details, IEEE 802.16 Physical Layer Details, IEEE 802.16 Physical Layer Details for 2-11 GHz, IEEE 802.16 Common System Operations.

TEXT BOOKS

- 1.Introduction to Wireless Telecommunications Systems and Networks – Gary J. Mullett, CENGAGE
- 2.Wireless Communication-Upena Dalal, Oxford University Press, 2009

REFERENCES

- 1.Wireless Communication System –Ke-Lin Du & M N S Swamy, Cambridge University Press, 2010
- 2.Mobile Cellular Communication - Gottapu Sasibhusan Rao, PEARSON



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

WIRELESS COMMUNICATIONS AND NETWORKS

UNIT -I:

The Cellular Concept-System Design Fundamentals: Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference , Power Control for Reducing interference, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring .

UNIT –II:

Mobile Radio Propagation: Large-Scale Path Loss: Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, The Three Basic Propagation Mechanisms, Reflection-Reflection from Dielectrics, Brewster Angle, Reflection from perfect conductors, Ground Reflection (Two-Ray) Model, Diffraction-Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- Longley-Rice Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models-Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT –III:

Mobile Radio Propagation: Small –Scale Fading and Multipath: Small Scale Multipath propagation-Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel- Relationship between Bandwidth and Received power, Small-Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small-Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler spread in Clarke's model, Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT -IV:

Equalization and Diversity: Introduction, Fundamentals of Equalization, Training A Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity Techniques-Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT -V:

Wireless Networks: Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11, IEEE 802.11 Medium Access Control, Comparison of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, Hiper Lan, WLL.

TEXT BOOKS:

1. Wireless Communications, Principles, Practice – Theodore, S. Rappaport, 2nd Ed., 2002, PHI.
2. Wireless Communications-Andrea Goldsmith, 2005 Cambridge University Press.
3. Mobile Cellular Communication – Gottapu Sasibhushana Rao, Pearson Education, 2012.

REFERENCE BOOKS:

1. Principles of Wireless Networks – Kaveh Pah Laven and P. Krishna Murthy, 2002, PE.
2. Wireless Digital Communications – Kamilo Feher, 1999, PHI.
3. Wireless Communication and Networking – William Stallings, 2003, PHI.
4. Wireless Communication – Open Dalal, Oxford Univ. Press.
5. Wireless Communications and Networking – Vijay K. Gary, Elsevier.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

WIRELESS MIMO COMMUNICATIONS

UNIT I :

Fading Channels and Diversity Techniques

Wireless channels – Error/Outage probability over fading channels – Diversity techniques – Channel coding as a means of time diversity – Multiple antennas in wireless communications.

UNIT II

Capacity and Information Rates of MIMO Channels

Capacity and Information rates of noisy, AWGN and fading channels – Capacity of MIMO channels – Capacity of non-coherent MIMO channels – Constrained signaling for MIMO communications.

UNIT III

Space-Time Block and Trellis Codes

Transmit diversity with two antennas: The Alamouti scheme – Orthogonal and Quasi-orthogonal space-time block codes – Linear dispersion codes – Generic space-time trellis codes – Basic space-time code design principles – Representation of space-time trellis codes for PSK constellation – Performance analysis for space-time trellis codes – Comparison of space-time block and trellis codes.

UNIT IV

Concatenated Codes and Iterative Decoding

Development of concatenated codes – Concatenated codes for AWGN and MIMO channels – Turbo coded modulation for MIMO channels – Concatenated space-time block coding.

UNIT V

Space-Time Coding for Frequency Selective Fading Channels

MIMO frequency-selective channels – Capacity and Information rates of MIMO FS fading channels – Space-time coding and Channel detection for MIMO FS channels – MIMO OFDM systems.

TEXT BOOKS

1. Tolga M. Duman and Ali Ghrayeb, *“Coding for MIMO Communication systems”*, John Wiley & Sons, West Sussex, England, 2007.
2. A.B. Gershman and N.D. Sidiropoulos, *“Space-time processing for MIMO communications”*, Wiley, Hoboken, NJ, USA, 2005.

REFERENCES

1. E.G. Larsson and P. Stoica, *“Space-time block coding for Wireless communications”*, Cambridge University Press, 2003.
2. M. Janakiraman, *“Space-time codes and MIMO systems”*, Artech House, 2004.
3. H. Jafarkhani, *“Space-time coding: Theory & Practice”*, Cambridge University Press, 2005.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

OPTICAL COMMUNICATIONS TECHNOLOGY
(Core Elective –III)

UNIT –I:

Signal Propagation in Optical Fibers: Geometrical Optics approach and Wave Theory approach, Loss and Bandwidth, Chromatic Dispersion, Non Linear effects- Stimulated Brillouin and Stimulated Raman Scattering, Propagation in a Non-Linear Medium, Self Phase Modulation and Cross Phase Modulation, Four Wave Mixing, Principle of Solitons.

UNIT –II:

Fiber Optic Components for Communication & Networking: Couplers, Isolators and Circulators, Multiplexers, Bragg Gratings, Fabry-Perot Filters, Mach Zender Interferometers, Arrayed Waveguide Grating, Tunable Filters, High Channel Count Multiplexer Architectures, Optical Amplifiers, Direct and External Modulation Transmitters, Pump Sources for Amplifiers, Optical Switches and Wavelength Converters.

UNIT –III:

Modulation and Demodulation: Signal formats for Modulation, Subcarrier Modulation and Multiplexing, Optical Modulations – Duobinary, Single Side Band and Multilevel Schemes, Ideal and Practical receivers for Demodulation, Bit Error Rates, Timing Recovery and Equalization, Reed-Solomon Codes for Error Detection and Correction.

UNIT -IV:

Transmission System Engineering: System Model, Power Penalty in Transmitter and Receiver, Optical Amplifiers, Crosstalk and Reduction of Crosstalk, Cascaded Filters, Dispersion Limitations and Compensation Techniques.

UNIT –V:

Fiber Non-linearities and System Design Considerations: Limitation in High Speed and WDM Systems due to Non-linearities in Fibers, Wavelength Stabilization against Temperature Variations, Overall System Design considerations – Fiber Dispersion, Modulation, Non-Linear Effects, Wavelengths, All Optical Networks.

TEXT BOOKS:

1. Optical Networks: A Practical Perspective - Rajiv Ramaswami and Kumar N. Sivarajan, 2nd Ed., 2004, Elsevier Morgan Kaufmann Publishers.
2. Optical Fiber Communications – Gerd Keiser, 3rd Ed., 2000, McGraw Hill.

REFERENCE BOOKS:

1. Optical Fiber Communications: Principles and Practice – John.M.Senior, 2nd Ed., 2000, PE.
2. Fiber Optics Communication – Harold Kolimbris, 2nd Ed., 2004, PEI
3. Optical Networks: Third Generation Transport Systems – Uyles Black, 2nd Ed., 2009, PEI.
4. Optical Fiber Communications – Govind Agarwal, 2nd Ed., 2004, TMH.
5. Optical Fiber Communications and Its Applications – S.C.Gupta, 2004, PHI.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

WIRELESS LANS AND PANS
(Core Elective –III)

UNIT –I:

Wireless System & Random Access Protocols: Introduction, First and Second Generation Cellular Systems, Cellular Communications from 1G to 3G, Wireless 4G systems, The Wireless Spectrum; Random Access Methods: Pure ALOHA, Slotted ALOHA, Carrier Sense Multiple Access (CSMA), Carrier Sense Multiple Access with Collision Detection (CSMA/CD), Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA).

UNIT –II:

Wireless LANs: Introduction, importance of Wireless LANs, WLAN Topologies, Transmission Techniques: Wired Networks, Wireless Networks, comparison of wired and Wireless LANs; WLAN Technologies: Infrared technology, UHF narrowband technology, Spread Spectrum technology

UNIT –III:

The IEEE 802.11 Standard for Wireless LANs: Network Architecture, Physical layer, The Medium Access Control Layer; MAC Layer issues: Hidden Terminal Problem, Reliability, Collision avoidance, Congestion avoidance, Congestion control, Security, The IEEE 802.11e MAC protocol

UNIT –IV:

Wireless PANs: Introduction, importance of Wireless PANs, The Bluetooth technology: history and applications, technical overview, the Bluetooth specifications, piconet synchronization and Bluetooth clocks, Master-Slave Switch; Bluetooth security; Enhancements to Bluetooth: Bluetooth interference issues, Intra and Inter Piconet scheduling, Bridge selection, Traffic Engineering, QoS and Dynamics Slot Assignment, Scatternet formation.

UNIT –V:

The IEEE 802.15 working Group for WPANs: The IEEE 802.15.3, The IEEE 802.15.4, ZigBee Technology, ZigBee components and network topologies, The IEEE 802.15.4 LR-WPAN Device architecture: Physical Layer, Data Link Layer, The Network Layer, Applications; IEEE 802.15.3a Ultra wideband.

TEXT BOOKS:

1. Ad Hoc and Sensor Networks - Carlos de Morais Cordeiro and Dharma Prakash Agrawal, World Scientific, 2011.
2. Wireless Communications and Networking - Vijay K.Garg, Morgan Kaufmann Publishers, 2009.

REFERENCE BOOKS

1. Wireless Networks - Kaveh Pahlaram, Prashant Krishnamurthy, PHI, 2002.
2. Wireless Communication- Marks Ciampor, George Olenewa, Cengage Learning, 2007.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

ADHOC AND WIRELESS SENSOR NETWORKS
(Core Elective –III)

UNIT –I:

Wireless Local Area Networks

Introduction, wireless LAN Topologies, Wireless LAN Requirements, Physical Layer- Infrared Physical Layer, Microwave based Physical Layer Alternatives, Medium Access Control Layer- HIPERLAN 1 Sublayer, IEEE 802.11 MAC Sublayer and Latest Developments-802.11a, 802.11b, 802.11g

Personal Area Networks: Introduction to PAN technology and Applications, Bluetooth - specifications, Radio Channel, Piconets and Scatternets, Inquiry, Paging and Link Establishment, Packet Format, Link Types, Power Management, Security, Home RF -Physical and MAC Layer

UNIT -II:

Ad-Hoc Wireless Networks: Introduction; Cellular and Ad-Hoc Wireless Networks; Issues in Ad-Hoc Wireless Networks: Medium Access Scheme, Routing, Multicasting, Transport Layer Protocols, Quality of Service Provisioning, Energy Management, Scalability

UNIT -III:

Medium Access Control Protocols: Introduction; Issues in Designing a MAC protocol: Bandwidth efficiency, Quality of Service support, Synchronization, Hidden and exposed terminal problems, Error prone shared broadcast channel, mobility nodes; Design goals of a MAC protocol; Classification of MAC protocols; Contention-based protocols: MACAW, Floor acquisition multiple access protocol, Busy tone multiple access protocols, MACA by invitation, Media access with reduced handshake; Contention-based protocols with reservation mechanisms; Contention-based MAC protocols with scheduling mechanisms; MAC protocols that use directional antennas

UNIT -IV:

Routing and Transport Layer Protocols: Introduction, issues in designing a routing protocol, Classification of routing protocols, Table-driven protocols, On-demand routing protocols, Hybrid routing protocols, Routing with efficient flooding mechanisms, Hierarchical routing protocols, Power-aware routing protocols; Introduction to transport layer protocols, design issues and goals of transport layer protocol, Classification of transport layer solutions

UNIT -V:

Wireless Sensor Networks: Introduction, Sensor Network Architecture, Data Dissemination, Data gathering, MAC protocols for Sensor Networks, Location Discovery, Quality of a Sensor Network

TEXT BOOKS:

1. Ad Hoc Wireless Networks - C. Siva Ram Murthy, 2004, Pearson Education.
2. Wireless Networks -P Nicopolitidis and M S Obaidat, Wiley India Edition 2003.

REFERENCE BOOKS:

1. Ad-Hoc Mobile Wireless Networks: Protocols and Systems - C.K. Toh, 1st Edn, Pearson Education.
2. Ad Hoc and Sensor Networks - Carlos de Moraes Cordeiro and Dharma Prakash Agrawal, 2011, World Scientific.
3. Wireless Sensor Networks - Kazen Sohraby, Daniel Minoli, Taieb Znati, 1991, Wiley Student Edition
4. Wireless Sensor Networks - C.S. Raghavendra, Krishna M. SivaLingam, 2004, Springer.
5. Wireless Ad- hoc and Sensor Networks: Protocols, Performance and Control - Jagannathan Sarangapani, CRC Press



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

NETWORK SECURITY AND CRYPTOGRAPHY
(Core Elective –IV)

UNIT -I:

Introduction: Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security.

Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Differential and Linear Cryptanalysis, Block Cipher Design Principles and Modes of operations.

UNIT -II:

Encryption Algorithms: Triple DES, International Data Encryption algorithm, Blowfish, RC5, CAST-128, RC2, Characteristics of Advanced Symmetric block ciphers.

Conventional Encryption: Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT -III:

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography.

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT -IV:

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD File, Message digest Algorithm, Secure Hash Algorithm, RIPEMD-160, HMAC.

Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, X.509 directory Authentication service.

Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT –V:

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Combining security Associations, Key Management. Web Security: Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. Cryptography and Network Security: Principles and Practice - William Stallings, Pearson Education.
2. Network Security Essentials (Applications and Standards) - William Stallings Pearson Education.

REFERENCE BOOKS:

1. Fundamentals of Network Security - Eric Maiwald (Dreamtech press)
2. Network Security - Private Communication in a Public World - Charlie Kaufman, Radia Perlman and Mike Speciner, Pearson/PHI.
3. Principles of Information Security - Whitman, Thomson.
4. Network Security: The complete reference - Robert Bragg, Mark Rhodes, TMH
5. Introduction to Cryptography - Buchmann, Springer.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

SOFTWARE DEFINED RADIO
(Core Elective –IV)

UNIT -I:

Introduction: The Need for Software Radios, What is Software Radio, Characteristics and benefits of software radio- Design Principles of Software Radio, RF Implementation issues- The Purpose of RF Front – End, Dynamic Range- The Principal Challenge of Receiver Design – RF Receiver Front- End Topologies- Enhanced Flexibility of the RF Chain with Software Radios- Importance of the Components to Overall Performance- Transmitter Architectures and Their Issues- Noise and Distortion in the RF Chain, ADC and DAC Distortion.

UNIT -II:

Profile and Radio Resource Management : Communication Profiles- Introduction, Communication Profiles, Terminal Profile, Service Profile , Network Profile, User Profile, Communication Profile Architecture, Profile Data Structure, XML Structure, Distribution of Profile Data, Access to Profile Data, Management of Communication Profiles, Communication Classmarks, Dynamic Classmarks for Reconfigurable Terminals, Compression and Coding, Meta Profile Data

UNIT -III:

Radio Resource Management in Heterogeneous Networks : Introduction, Definition of Radio Resource Management, Radio Resource Units over RRM Phases, RRM Challenges and Approaches, RRM Modelling and Investigation Approaches, Investigations of JRRM in Heterogeneous Networks, Measuring Gain in the Upper Bound Due to JRRM, Circuit-Switched System, Packet-Switched System, Functions and Principles of JRRM, General Architecture of JRRM, Detailed RRM Functions in Sub-Networks and Overall Systems

UNIT -IV:

Reconfiguration of the Network Elements : Introduction, Reconfiguration of Base Stations and Mobile Terminals, Abstract Modelling of Reconfigurable Devices, the Role of Local Intelligence in Reconfiguration, Performance Issues, Classification and Rating of Reconfigurable Hardware, Processing Elements, Connection Elements, Global Interconnect Networks, Hierarchical Interconnect Networks, Installing a New Configuration, Applying Reconfiguration Strategies, Reconfiguration Based on Comparison, Resource Recycling, Flexible Workload Management at the Physical Layer, Optimised Reconfiguration, Optimisation Parameters and Algorithms, Optimization Algorithms, Specific Reconfiguration Requirements, Reconfiguring Base Stations, Reconfiguring Mobile Terminals

UNIT -V:

Object – Oriented Representation of Radios and Network Resources: Networks- Object Oriented Programming- Object Brokers- Mobile Application Environments- Joint Tactical Radio System.

Case Studies in Software Radio Design: Introduction and Historical Perspective, SPEAK easy-JTRS, Wireless Information Transfer System, SDR-3000 Digital Transceiver Subsystem, Spectrum Ware, CHARIOT.

TEXT BOOKS:

1. Software Defined Radio Architecture System and Functions- Markus Dillinger, Kambiz Madani, WILEY 2003
2. Software Defined Radio: Enabling Technologies- Walter Tuttle Bee, 2002, Wiley Publications.

REFERENCE BOOKS:

1. Software Radio: A Modern Approach to Radio Engineering - Jeffrey H. Reed, 2002, PEA Publication.
2. Software Defined Radio for 3G - Paul Burns, 2002, Artech House.
3. Software Defined Radio: Architectures, Systems and Functions - Markus Dillinger, Kambiz Madani, Nancy Alonistioti, 2003, Wiley.
4. Software Radio Architecture: Object Oriented Approaches to wireless System Engineering – Joseph Mitola, III, 2000, John Wiley & Sons.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

4G TECHNOLOGIES
(Core Elective –IV)

UNIT I:

2G and 3G technology

Second Generation (2G) - Overview, Enhancements over 1G Systems, Integration with Existing 1G Systems, GSM, IS-136 System Description, IS-95 System Description, iDEN (Integrated Dispatch Enhanced Network), CDPD

Third Generation (3G)- Overview, Introduction, Universal Mobile Telecommunications Service (UMTS), UMTS Services, The UMTS Air Interface, Overview of the 3GPP Release 1999 Network Architecture, Overview of the 3GPP Release 4 Network Architecture, Overview of the 3GPP Release 5 All-IP Network Architecture, Overview CDMA2000, Commonality Between, DMA/CDMA2000/CDM

UNIT II:

The Evolution Generation (2.5G)

What Is 2.5G?, Enhancements over 2G, Technology Platforms, General Packet Radio Service, (GPRS), Enhanced Data Rates for Global Evolution (EDGE), High-Speed Circuit Switched Data (HSCSD), CDMA2000 (1XRTT), WAP, Migration Path from 2G to 2.5G to 3G,

UNIT III:

OFDM

Introduction to OFDM, Multicarrier Modulation and Cyclic Prefix, Channel model and SNR performance, OFDM Issues – PAPR, Frequency and Timing Offset Issues.

UNIT IV:

UWB:

UWB Definition and Features, UWB Wireless Channels, UWB Data Modulation, Uniform Pulse Train.

UNIT V:

4G Cellular technology

4G evolution, objectives of the projected 4G, advantages of 4G network technology over 3G, applications of 4G, 4G technologies, Smart antenna techniques, 4G software, New technologies in cellular data networks.

TEXT BOOKS:

1. 3G Wireless Networks, 2nd ed., Clint Smith, P.E , Daniel Collins
2. Mobile Cellular Communication, Gottapu Sasibhuhsana Rao, PEARSON, 2013

REFERENCE BOOKS:

1. 3G Networks Architecture, Protocols and Procedures, Sumith Kaseara, Nishit Narang
2. Wireless Networking complete by Zheng ISBN 9789351071563, First Indian reprint 2014., Elsevier publication
3. Towards 4G Technologies: Services with Initiative by Hendrik Berndt [John Wiley & Sons](#)
4. Advanced Wireless Networks: 4G Technologies By Savo G. Glisic [John Wiley & Sons](#)



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

EMBEDDED SYSTEM DESIGN
(Open Elective -II)

UNIT -I:

Introduction to Embedded Systems

Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT -II:

Typical Embedded System:

Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT -III:

Embedded Firmware:

Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT -IV:

RTOS Based Embedded System Design:

Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT -V:

Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOK:

1. Introduction to Embedded Systems - Shibu K.V, Mc Graw Hill.

REFERENCE BOOKS:

1. Embedded Systems - Raj Kamal, TMH.
2. Embedded System Design - Frank Vahid, Tony Givargis, John Wiley.
3. Embedded Systems – Lyla, Pearson, 2013
4. An Embedded Software Primer - David E. Simon, Pearson Education.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

MOBILE COMPUTING TECHNOLOGIES
(Open Elective –II)

UNIT –I:

Introduction to Mobile Computing Architecture: Mobile Computing – Dialog Control – Networks – Middleware and Gateways – Application and Services – Developing Mobile Computing Applications – Security in Mobile Computing – Architecture for Mobile Computing – Three Tier Architecture – Design considerations for Mobile Computing – Mobile Computing through Internet – Making existing Applications Mobile Enabled.

UNIT –II:

Cellular Technologies: GSM, GPS, GPRS, CDMA and 3G: Bluetooth – Radio Frequency Identification – Wireless Broadband – Mobile IP – Internet Protocol Version 6 (IPv6) – Java Card – GSM Architecture – GSM Entities – Call Routing in GSM – PLMN Interfaces – GSM addresses and Identifiers – Network aspects in GSM – Authentication and Security – Mobile computing over SMS – GPRS and Packet Data Network – GPRS Network Architecture – GPRS Network Operations – Data Services in GPRS – Applications for GPRS – Limitations of GPRS – Spread Spectrum technology – Is-95 – CDMA Versus GSM – Wireless Data – Third Generation Networks – Applications on 3G

UNIT –III:

Wireless Application Protocol (WAP) and Wireless LAN: WAP – MMS – Wireless LAN Advantages – IEEE 802.11 Standards – Wireless LAN Architecture – Mobility in wireless LAN

Intelligent Networks and Interworking : Introduction – Fundamentals of Call processing – Intelligence in the Networks – SS#7 Signaling – IN Conceptual Model (INCM) – soft switch – Programmable Networks – Technologies and Interfaces for IN

UNIT –IV:

Client Programming, Palm OS, Symbian OS, Win CE Architecture: Introduction – Moving beyond the Desktop – A Peek under the Hood: Hardware Overview – Mobile phones – PDA – Design Constraints in Applications for Handheld Devices – Palm OS architecture – Application Development – Multimedia – Symbian OS Architecture – Applications for Symbian, Different flavors of Windows CE -Windows CE Architecture

J2ME: JAVA in the Handset – The Three-prong approach to JAVA Everywhere – JAVA 2 Micro Edition (J2ME) technology – Programming for CLDC – GUI in MIDP – UI Design Issues – Multimedia – Record Management System – Communication in MIDP – Security considerations in MIDP – Optional Packages

UNIT –V:

Voice Over Internet Protocol and Convergence: Voice over IP- H.323 Framework for Voice over IP – Session Initiation Protocol – Comparison between H.323 and SIP – Real Time protocols – Convergence Technologies – Call Routing – Voice over IP Applications – IP multimedia subsystem (IMS) – Mobile VoIP

Security Issues in Mobile Computing: Introduction – Information Security – Security Techniques and Algorithms – Security Protocols – Public Key Infrastructure – Trust – Security Models – Security frameworks for Mobile Environment

TEXT BOOKS:

1. Mobile Computing – Technology, Applications and Service Creation – Asoke K Talukder, Roopa R Yavagal, 2009, TATA McGraw Hill
2. Mobile Communications – Jochen Schiller – 2nd Edition – Pearson Education

REFERENCE BOOKS:

1. The CDMA 2000 System for Mobile Communications – Vieri Vaughni, Alexander Damn Jaonvic – Pearson
2. Adalestein : Fundamentals of Mobile & Parvasive Computing, 2008, TMH



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

SCRIPTING LANGUAGES
(Open Elective II)

UNIT -I:

Introduction to Scripts and Scripting:

Characteristics and uses of scripting languages, Introduction to PERL, Names and values, Variables and assignment, Scalar expressions, Control structures, Built-in functions, Collections of Data, Working with arrays, Lists and hashes, Simple input and output, Strings, Patterns and regular expressions, Subroutines, Scripts with arguments.

UNIT -II:

Advanced PERL:

Finer points of Looping, Subroutines, Using Pack and Unpack, Working with files, Navigating the file system, Type globs, Eval, References, Data structures, Packages, Libraries and modules, Objects, Objects and modules in action, Tied variables, Interfacing to the operating systems, Security issues.

UNIT -III:

TCL:

The TCL phenomena, Philosophy, Structure, Syntax, Parser, Variables and data in TCL, Control flow, Data structures, Simple input/output, Procedures, Working with Strings, Patterns, Files and Pipes, Example code.

UNIT -IV:

Advanced TCL:

The eval, source, exec and up-level commands, Libraries and packages, Namespaces, Trapping errors, Event-driven programs, Making applications 'Internet-aware', 'Nuts-and-bolts' internet programming, Security issues, running untrusted code, The C interface.

UNIT -V:

TK and JavaScript:

Visual tool kits, Fundamental concepts of TK, TK by example, Events and bindings, Geometry managers, PERL-TK.

JavaScript – Object models, Design Philosophy, Versions of JavaScript, The Java Script core language, Basic concepts of Pythan.

Object Oriented Programming Concepts (Qualitative Concepts Only): Objects, Classes, Encapsulation, Data Hierarchy.

TEXT BOOKS:

1. The World of Scripting Languages- David Barron, Wiley Student Edition, 2010.
2. Practical Programming in Tcl and Tk - Brent Welch, Ken Jones and Jeff Hobbs., Fourth edition.
3. Java the Complete Reference - Herbert Schildt, 7th Edition, TMH.

REFERENCE BOOKS:

1. Tcl/Tk: A Developer's Guide- Clif Flynt, 2003, Morgan Kaufmann Series.
2. Tcl and the Tk Toolkit- John Ousterhout, 2nd Edition, 2009, Kindel Edition.
3. Tcl 8.5 Network Programming book- Wojciech Kocjan and Piotr Beltowski, Packt Publishing.
4. Tcl/Tk 8.5 Programming Cookbook- Bert Wheeler



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (WMC)

WIRELESS COMMUNICATIONS AND NETWORKS LAB

Note:

- A. Minimum of 10 Experiments have to be conducted
- B. All the Experiments may be Conducted using Network Simulation software like NS-2/ NSG-2.1/ WireSHARK/ SDR etc..

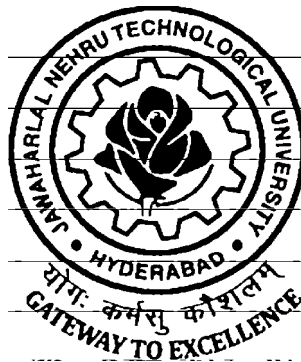
Note: For Experiments 1 to 7 Performance may be evaluated through simulation by using the parameters Throughput, Packet Delivery Ratio, Delay etc.

- 1. Evaluate the performance of various LAN Topologies
- 2. Evaluate the performance of Drop Tail and RED queue management schemes
- 3. Evaluate the performance of CBQ and FQ Scheduling Mechanisms
- 4. Evaluate the performance of TCP and UDP Protocols
- 5. Evaluate the performance of TCP, New Reno and Vegas
- 6. Evaluate the performance of AODV, DSR and DSDV routing protocols
- 7. Evaluate the performance of IEEE 802.11 and IEEE 802.15.4
- 8. Capturing and Analysis of TCP and IP Packets
- 9. Simulation and Analysis of ICMP and IGMP Packets
- 10. Analyze the Protocols SCTP, ARP, NetBIOS, IPX VINES
- 11. Analysis of HTTP, DNS and DHCP Protocols
- 12. Analysis of OFDM Spectrum
- 13. Analysis CDMA Downlink

ACADEMIC REGULATIONS COURSE STRUCTURE AND DETAILED SYLLABUS

**M.TECH
CAD / CAM**

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/ year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - CAD / CAM

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		Advanced CAD	3	-	3
		Advanced Finite Element Analysis	3	-	3
		Precision Engineering	3	-	3
		Design for Manufacturing and Assembly	3	-	3
	Elective-I	Numerical Methods for Partial Differential Equations Advanced Mechanics of Solids Advanced Mechatronics	3	-	3
	Elective-II	Advanced Mechanics of Composite Materials Total Quality Management Stress Analysis and Vibrations	3	-	3
		Computer Aided Design Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Design of Hydraulics and Pneumatics Systems	3	-	3
		Flexible Manufacturing Systems	3	-	3
		Computer Aided Manufacturing	3	-	3
		Industrial Robotics	3	-	3
	Elective -III	Production and Operations Management Computational Fluid Dynamics Automation in Manufacturing	3	-	3
	Elective -IV	Design Optimization Intelligent Manufacturing Systems Computer Aided Process Planning	3	-	3
	Lab	Computer Aided Machining and Robotics Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	-	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED CAD**UNIT I:**

Principles of Computer Graphics : Introduction, graphic primitives, point plotting, lines, Bresenham's circle algorithm, ellipse, transformation in graphics, coordinate systems, view port, 2D and 3D transformation, hidden surface removal, reflection, shading and generation of characters.

UNIT II:

CAD Tools: Definition of CAD Tools, Types of system, CAD/CAM system evaluation criteria, brief treatment of input and output devices. Graphics standard, functional areas of CAD, Modeling and viewing, software documentation, efficient use of CAD software. **Geometricmodelling:** Types of mathematical representation of curves, wire frame models wire frame entities parametric representation of synthetic curves her mite cubic splines Bezier curves B-splines rational curves.

UNIT III:

Surface Modeling :Mathematical representation surfaces, Surface model, Surface entities surface representation, Parametric representation of surfaces, plane surface, rule surface, surface of revolution, Tabulated Cylinder.

UNIT IV:

Parametric Representation of Synthetic Surfaces: Hermite Bicubic surface, **Bezier** surface, **B-** Spline surface, COONs surface, Blending surface Sculptured surface, Surface manipulation — Displaying, Segmentation, Trimming, Intersection, Transformations (both 2D and 3D).

UNIT V:

Geometricmodelling-3D : Solid modeling, Solid Representation, Boundary Representation (13-rep), Constructive So! id Geometry (CSG). CAD/CAM Exchange : Evaluation of data — exchange format, IGES data representations and structure, STEP Architecture, implementation, ACIS & DXF. Design Applications: Mechanical tolerances, Mass property calculations, Finite Element Modeling and Analysis and Mechanical Assembly.

Collaborative Engineering: Collaborative Design, Principles, Approaches, Tools, Design Systems.

REFERENCES :

1. Mastering CAD/CAM / Ibrhim Zeid / Mc Graw Hill International.
2. CAD/CAM Principles and Applications/ P.N.Rao/TMH/3rd Edition.
3. CAD/CAM /Groover M.P./ Pearson education.
4. CAD/CAM Concepts and Applications/ Alavala/ PHI.
5. CAD / CAM / CIM, Radhakrishnan and Subramanian/ New Age.
6. Principles of Computer Aided Design and Manufacturing/ Farid Amirouche/ Pearson.
7. Computer Numerical Control Concepts and programming/ Warren S Seames/ Thomson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED FINITE ELEMENT ANALYSIS

UNIT-I:

Introduction to FEM, basic concepts, historical back ground, applications of FEM, general description, comparison of FEM with other methods, variational approach, Glerkin's Methods. Co-ordinates, basic element shapes, interpolation function, Virtual energy principle, Rayleigh – Ritz method, properties of stiffness matrix, treatment of boundary conditions, solution of system of equations, shape functions and characteristics, Basic equations of elasticity, strain- displacement relations.

UNIT-II:

1-D Structural Problems: Axial bar element – stiffness matrix, load vector, temperature effects, Quadratic shape functions and problems.

Analysis of Trusses : Plane Trusses and Space Truss elements and problems

Analysis of Beams : Hermite shape functions – stiffness matrix – Load vector – Problems.

UNIT-III:

2-D Problems: CST, LST, force terms, Stiffness matrix and load vectors, boundary conditions, Isoparametric elements – quadrilateral element, shape functions – Numerical Integration.

Finite element modeling of Axi-symmetric solids subjected to Axi-symmetric loading with triangular elements.

3-D Problems: Tetrahedran element – Jacobian matrix – Stiffness matrix.

UNIT-VI:

Scalar Field Problems: 1-D Heat conduction-Slabs – fins - 2-D heat conduction problems – Introduction to Torsional problems.

UNIT-V:

Dynamic considerations, Dynamic equations – consistent mass matrix – Eigen Values, Eigen vector, natural frequencies – mode shapes – modal analysis.

REFERENCES:

1. The Finite Element Methods in Engineering / SS Rao / Pergamon.
2. Finite Element Methods: Basic Concepts and applications, Alavala, PHI.
3. Introduction to Finite Elements in Engineering, Chandrupatla, Ashok and Belegundu, Prentice – Hall
4. Finite Element Method – Zincowitz / Mc Graw Hill.
5. Introduction to Finite element analysis- S.Md.Jalaludeen, Anuradha Publications, print-2012.
6. A First Course in the Finite Element Method/Daryl L Logan/Cengage Learning/5th Edition.
7. Finite Element Method – Krishna Murthy / TMH.
8. Finite Element Analysis – Bathe / PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

PRECISION ENGINEERING

UNIT I:

Concepts of Accuracy: Introduction – Concept of Accuracy of Machine Tools – Spindle and Displacement Accuracies – Accuracy of numerical Control Systems – Errors due to Numerical Interpolation Displacement Measurement System and Velocity lags.

Geometric Dimensioning and Tolerancing: Tolerance Zone Conversions – Surfaces, Features, Features of Size, Datum Features – Datum Oddly Configured and Curved Surfaces as Datum Features, Equalizing Datums – Datum Feature of Representation – Form controls, Orientation Controls – Logical Approach to Tolerancing.

UNIT II:

Datum Systems: Design of freedom, Grouped Datum Systems – different types, two and three mutually perpendicular grouped datum planes; Grouped datum system with spigot and recess, pin and hole; Grouped Datum system with spigot and recess pair and tongue – slot pair – Computation of Transnational and rotational accuracy, Geometric analysis and application.

UNIT III:

Tolerance Analysis: Process Capability, Mean, Variance, Skewness, Kurtosis, Process Capability Metrics, Cp, Cpk, Cost aspects, Feature Tolerances, Geometric Tolerances. Surface finish, Review of relationship between attainable tolerance grades and different machining process, Cumulative effect of tolerances sure fit law, normal law and truncated normal law.

UNIT IV:

Tolerance Charting Techniques: Operation Sequence for typical shaft type of components, Preparation of Process drawings for different operations, Tolerance worksheets and centrally analysis, Examples, Design features to facilitate machining; Datum Features – functional and manufacturing Components design – Machining Considerations, Redesign for manufactured, Examples.

UNIT V:

Foundamentals of Nanotechnology: Systems of nanometer accuracies – Mechanism of metal Processing – Nano physical processing of atomic bit units. Nanotechnology and Electrochemical atomic bit processing.

Measuring Systems Processing: In processing or in-situ measurement of position of processing point- Post process and on-machine measurement of dimensional features and surface-mechanical and optical measuring systems.

REFERENCES:

1. Precision Engineering in Manufacturing/Murthy R.L./New Age International (P) limited, 1996.
2. Geometric Dimensioning and Tolerancing / James D. Meadows / Marcel Dekker inc. 1995.
3. Nano Technology / Norio Taniguchi / Oxford University Press, 1996.
4. Engineering Design – A systematic Approach / Matousek / Blackie & Son Ltd., London.
5. Precision Engineering/VC Venkatesh & S Izman/TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

DESIGN FOR MANUFACTURING AND ASSEMBLY

UNIT I:

Introduction: Design philosophy steps in Design process - General Design rules for manufacturability - basic principles of design Ling for economical production - creativity in design. Materials: Selection of Materials for design Developments in Material technology - criteria for material selection - Material selection interrelationship with process selection process selection charts.

UNIT II:

Machining Process: Overview of various machining processes - general design rules for machining - Dimensional tolerance and surface roughness - Design for machining - Ease - Redesigning of components for machining ease with suitable examples. General design recommendations for machined parts. **METAL**

Casting: Appraisal of various casting processes, selection of casting process, - general design considerations for casting - casting tolerances - use of solidification simulation in casting design - product design rules for sand casting.

UNIT III:

Metal Joining: Appraisal of various welding processes, Factors in design of weldments - general design guidelines - pre and post treatment of welds - effects of thermal stresses in weld joints - design of brazed joints. Forging - Design factors for Forging - Closed dies forging design - parting lines of die5 drop forging die design - general design recommendations. Extrusion & Sheet Metal Work: Design guidelines for extruded sections - design principles for Punching, Blanking, Bending, Deep Drawing - Keeler Goodman Forming Line Diagram - Component Design for Blanking.

UNIT-IV

Assemble Advantages: Development of the assemble process, choice of assemble method assemble advantages social effects of automation.

Automatic Assembly Transfer Systems : Continuous transfer, intermittent transfer, indexing mechanisms, and operator - paced free – transfer machine.

UNIT-V:

Design of Manual Assembly: Design for assembly fits in the design process, general design guidelines for manual assembly, development of the systematic DFA methodology, assembly efficiency, classification system for manual handling, classification system for manual insertion and fastening, effect of part symmetry on handling time, effect of part thickness and size on handling time, effect of weight on handling time, parts requiring two hands for manipulation, effects of combinations of factors, effect of symmetry effect of chamfer design on insertion operations, estimation of insertion time.

REFERENCES:

1. Assembly Automation and Product Design/ Geoffrey Boothroyd/ Marcel Dekker Inc., NY, 1992.
2. Engineering Design - Material & Processing Approach/ George E. Deiter/McGraw Hill Intl. 2nd Ed. 2000.
3. Hand Book of Product Design/ Geoffrey Boothroyd/ Marcel and Dekken, N.Y. 1990.
4. Computer Aided Assembly London/ A Delbainbre/.
5. Product Design for Manufacturing and Assembly/ Geoffrey Boothroyd, Peter Dewhurst & Winston Anstony Knight/CRC Press/2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

NUMERICAL METHODS FOR PARTIAL DIFFERENTIAL EQUATIONS

ELECTIVE – I

UNIT-I:

Introduction to finite difference formula- Parabolic Equation: Introduction – Explicit finite difference approximation to one dimensional equation Crank – Nicholson implicit method – derivation boundary conditions.

UNIT-II:

Alternate direction implicit (ADI) method finite difference in cylindrical and spherical polar co-ordinates.

Convergence Stability and Consistency: Definitions of local truncation error and consistency convergence analysis – stability analysis by matrix method eigen value von Neumann stability methods, global rounding error-local truncation error-lax's equation theorem.

UNIT-III:

Hyperbolic Equations: Analytical solution of 1st order quasi linear equation – numerical integration along a characteristic lax wenderoff explicit method.

CFI condition wenderoff implicit approximation – propagation of discontinues – Numerical solution by the method of characteristics.

UNIT-IV:

Elliptic Equations: Introduction – Finite differences in polar co-ordinates – formulas for derivative near a curved boundary analysis of the discretization error of the five point approximation to polman's equation over a rectangle.

UNIT-V:

Systematic iterative methods for large linear systems – necessary and sufficient condition for convergence of iterative methods – steepest implicit methods.

Finite Element Method: weighted residual method – variations methods – division of the region into elements linear element – Galerkin formulation.

REFERENCES:

1. Numerical Solution of partial differential equations, Finite Differences methods/ G.D. Smith/ Brunel University, Clarendon Press Oxford.
2. The Finite Differences Methods in Partial Differential equation/ A.R. Mitchell and D.F. Griffiths/ John Wiley.
3. Numerical Methods for Engineers and scientists/ Joe D. Hoffman/ Mc Graw Hill.
4. Applied Finite Element Analysis/ Larry J. Segerlind/ John Wiley.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED MECHANICS OF SOLIDS

ELECTIVE - I

UNIT - I

Shear Centre: Bending axis and shear center-shear center for axi-symmetric and unsymmetrical sections.

Unsymmetrical Bending: Bending stresses in Beams subjected to Nonsymmetrical bending; Deflection of straight beams due to nonsymmetrical bending.

UNIT - II

Curved Beam Theory: Winkler Bach formula for circumferential stress – Limitations – Correction factors – Radial stress in curved beams – closed ring subjected to concentrated and uniform loads- stresses in chain links.

UNIT - III

Torsion: Torsion of a cylindrical bar of Circular cross Section; Saint-Venant's semi-inverse methods; Linear elastic solution; Prandtl elastic membrane (Soap-Film) Analogy; Narrow rectangular cross Section; Hollow thin wall torsion members, Multiply connected Cross section, Thin wall torsion members with restrained ends

Axi-Symmetric Problems: Rotating Discs – Flat discs, Discs of uniform thickness, Discs of Uniform Strength, Rotating Cylinders.

UNIT - IV

Theory of Plates: Introduction; Stress resultants in a flat plate; Kinematics: Strain- Displacement relations for plates; Equilibrium equations for small displacement theory of flat plates; Stress – Strain – Temperature relation for Isotropic plates; Strain energy of a plate; Boundary conditions for plate; Solution of rectangular plate problem; Solution of circular plate problem.

Beams on Elastic Foundation: General theory; Infinite Beam subjected to Concentrated load; boundary conditions; Infinite beam subjected to a distributed load segment; Semi-infinite beam with concentrated load near its end; Short Beams.

UNIT - V

Contact Stresses: Introduction, problem of determining contact stresses; Assumptions on which a solution for contact stresses is based; Expressions for principal stresses; Methods of computing contact stresses; Deflection of bodies in point contact; Stresses for two bodies in contact over narrow rectangular area (Line contact), Loads normal to area; Stresses for two bodies in line contact. Normal and Tangent to contact area.

REFERENCES:

1. Advanced Mechanics of materials/Seely and Smith/ John Willey.
2. Advanced Mechanics of materials / Boresi & Sidebottom/Wiley international.
3. Advanced strength of materials / Den Hartog J.P./Torrent.
4. Theory of Plates /Timoshenko/
5. Strength of materials / Sadhu singh/ Khanna Publishers.
6. Mechanics of Materials / Beer & Johnson / McGraw Hill.
7. Theory of Plates & Shells / Timoshenko/ McGraw Hill/ 2nd Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED MECHATRONICS**ELECTIVE – I****UNIT-I**

Mechatronics systems, elements, levels of mechatronics system, Mechatronics design process, system, measurement systems, control systems, microprocessor-based controllers, advantages and disadvantages of mechatronics systems. Sensors and transducers, types, displacement, position, proximity, velocity, motion, force, acceleration, torque, fluid pressure, liquid flow, liquid level, temperature and light sensors.

UNIT-II

Solid state electronic devices, PN junction diode, BJT, FET, DIA and TRIAC. Analog signal conditioning, amplifiers, filtering. Introduction to MEMS & typical applications.

UNIT-III

Hydraulic and pneumatic actuating systems, Fluid systems, Hydraulic and pneumatic systems, components, control valves, electro-pneumatic, hydro-pneumatic, electro-hydraulic servo systems: Mechanical actuating systems and electrical actuating systems.

UNIT-IV

Digital electronics and systems, digital logic control, micro processors and micro controllers, programming, process controllers, programmable logic controllers, PLCs versus computers, application of PLCs for control.

UNIT-V

System and interfacing and data acquisition, DAQS, SCADA, A to D and D to A conversions; Dynamic models and analogies, System response. Design of mechatronics systems & future trends.

REFERENCES:

1. MECHATRONICS Integrated Mechanical Electronics Systems/KP Ramachandran & GK Vijaya Raghavan/WILEY India Edition/2008.
2. Mechatronics Electronics Control Systems in Mechanical and Electrical Engineering by W Bolton, Pearson Education Press, 3rd edition, 2005.
3. Mechatronics Source Book by Newton C Braga, Thomson Publications, Chennai.
4. Mechatronics – N. Shanmugam / Anuradha Agencies Publishers.
5. Mechatronics System Design / Devdas shetty/Richard/Thomson.
6. Mechatronics/M.D.Singh/J.G.Joshi/PHI.
7. Mechatronics – Electronic Control Systems in Mechanical and Electrical Engg. 4th Edition, Pearson, 2012 W. Bolton.
8. Mechatronics – Principles and Application Godfrey C. Onwubolu, Wlsevier, 2006 Indian print.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

ADVANCED MECHANICS OF COMPOSITE MATERIALS

ELECTIVE - II

UNIT – I

Basic Concepts and Characteristics: Geometric and Physical definitions, natural and man-made composites, Aerospace and structural applications, types and classification of composites.

Reinforcements: Fibres – Glass, Silica, Kevlar, carbon, boron, silicon carbide, and boron carbide fibres. Particulate composites, Polymer composites, Thermoplastics, Thermosets, Metal matrix and ceramic composites.

UNIT – II

Micromechanics: Unidirectional composites, constituent materials and properties, elastic properties of a lamina, properties of typical composite materials, laminate characteristics and configurations. Characterization of composite properties.

Manufacturing Methods: Autoclave, tape production, moulding methods, filament winding, man layup, pultrusion, RTM.

Unit – III

Coordinate Transformation: Hooke's law for different types of materials, Hooke's law for two dimensional unidirectional lamina, Transformation of stress and strain, Numerical examples of stress strain transformation, Graphic interpretation of stress – strain relations. Off – axis, stiffness modulus, off – axis compliance.

Elastic Behavior of Unidirectional Composites: Elastic constants of lamina, relationship between engineering constants and reduced stiffness and compliances, analysis of laminated composites, constitutive relations.

Unit – IV

Strength of Unidirectional Lamina: Micro mechanics of failure, Failure mechanisms, strength of an orthotropic lamina, strength of a lamina under tension and shear maximum stress and strain criteria, application to design. The failure envelope, first ply failure, free-edge effects. Micro mechanical predictions of elastic constants.

Unit – V

Analysis of Laminated Composite Plates: Introduction thin plate theory, specially orthotropic plate, cross and angle ply laminated plates, problems using thin plate theory.

REFERENCES:

1. Mechanics of Composite Materials/ R. M. Jones/ Mc Graw Hill Company, New York, 1975.
2. Engineering Mechanics of Composite Materials by Isaac and M Daniel, Oxford University Press, 1994.
3. Analysis and performance of fibre Composites/ B. D. Agarwal and L. J. Broutman/ Wiley- Interscience, New York, 1980.
4. Mechanics of Composite Materials/ Second Edition (Mechanical Engineering)/ Autar K. Kaw ,Publisher: CRC.
5. Analysis of Laminated Composite Structures/ L. R. Calcote/ Van Nostrand Reinhold, New York, 1969.
6. Advanced Mechanics of Composite Materials/ Vasiliev & Morozov/Elsevier/Second Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

TOTAL QUALITY MANAGEMENT

ELECTIVE - II

UNIT – I:

Introduction: The concept of TQM, Quality and Business performance, attitude and involvement of top management, communication, culture and management systems. Management of Process Quality: Definition of quality, Quality Control, a brief history, Product Inspection vs, Process Control, Statistical Quality Control, Control Charts and Acceptance Sampling.

UNIT – II:

Customer Focus and Satisfaction: The importance of customer satisfaction and loyalty- Crating satisfied customers, Understanding the customer needs, Process Vs. Customer, internal customer conflict, quality focus, Customer Satisfaction, role of Marketing and Sales, Buyer – Supplier relationships. Bench Marketing: Evolution of Bench Marketing, meaning of Bench marketing, benefits of bench marketing, the bench marketing process, pitfalls of bench marketing.

UNIT – III:

Organizing for TQM: The systems approach, Organizing for quality implementation, making the transition from a traditional to a TQM organizing, Quality Circles. Productivity, Quality and Reengineering: The leverage of Productivity and Quality, Management systems Vs. Technology, Measuring Productivity, Improving Productivity Re-engineering.

UNIT – IV:

The cost of Quality: Definition of the Cost of Quality, Quality Costs, Measuring Quality Costs, use of Quality Cost Information, Accounting Systems and Quality Management.

UNIT – V:

ISO9000: Universal Standards of Quality: ISO around the world, The ISO9000 ANSI/ASQCQ-90. Series Standards, benefits of ISO9000 certification, the third party audit, Documentation ISO9000 and services, the cost of certification implementing the system.

REFERENCES:

1. Total Quality Management / Joel E.Ross/Taylor and Franscis Limited.
2. Total Quality Management/P.N.Mukherjee/PHI.
3. Beyond TQM / Robert L.Flood.
4. Statistical Quality Control / E.L. Grant.
5. Total Quality Management- A Practical Approach/H. Lal.
6. Quality Management/Kanishka Bedi/Oxford University Press/2011.
7. Total Engineering Quality Management/Sunil Sharma/Macmillan.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

STRESS ANALYSIS AND VIBRATION**ELECTIVE - II****UNIT-I:**

Two dimensional elasticity theory in Cartesian coordinates, plane stress problem in polar coordinates , Thick cylinders, Rotating discs - stress concentration.

UNIT- II:

Torsion of non circular prismatic sections, rectangular and axisymmetric, Circular plates, introduction to shell theory — contact stresses.

UNIT- III:

Single degree freedom, two degree freedom system without and with damping - Free and forced vibrations. Transient vibrations.

UNIT- IV:

Transient vibrations of single and two degree freedom systems, multi-degree of freedom systems - applications of matrix methods , continuous systems.

UNIT -V:

Free and forced vibrations of strings bars and beams. Principle of orthogonality - classical and energy methods.

REFERENCES:

1. Theory of Elasticity/Timoshenko S.P. and Goodier J.N./Koakusha Publishers.
2. Advanced strength of materials / Den Hortog J.P./Torrent.
3. Mechanical Vibrations/ Den Ilartog J.P./ Dover Publications.
4. Theory of Vibrations with Applications/ Thomson W.T./ CBS Publishing.
5. Mechanical Vibrations/ Rao S.S./ Addison Wesley Longman.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech – I year I Sem. (CAD/CAM)

COMPUTER AIDED DESIGN LAB

Creation of working drawing, creating geometry, constraining the profile, extracting a part using tools, creating pattern of holes, translating rotating, mirroring, managing the specification tree. Creating sheets and views, creating text and dimensions, creating an assembly, moving components, assembling existing components, creating bill of materials, creating wire frame and surface geometry using generative shape design and sweep tools. Generation of Ferguson's cubic surface patches, Bezier surface patches. Coons patches. Import and export of drawing from other software.

Linear static analysis, Automatic calculation of rigid body modes, uses specified eigen value shift, lumped and consistent mass matrices. Buckling analysis, Jacobi inverse iteration techniques. Steady state harmonic response, mode superposition method, overall structural and damping, linear dynamic analysis, non linear static analysis, non- linear dynamic analysis. Steady state heat transfer analysis problems. Transient heat transfer analysis. Familiarity with element library. Defining Boundary conditions, multipoint constraint familiarity with different types of loads. Solution techniques, direct and iterative solver. Results and analysis. Design optimization.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

DESIGN OF HYDRAULICS & PNEUMATIC SYSTEMS

UNIT – I:

Oil hydraulic systems Hydraulic pumps, types and construction details, sizing and selection.

Direction control valves, flow and pressure control valves.

UNIT – II:

Linear actuators types Piston rod design sizing and selection, Rotary actuators, hydraulic reservoir accumulators.

UNIT – III:

Design of hydraulic circuits, seals and packings, hydraulic servo techniques, cylinders and air motors.

UNIT – IV:

Sequencing and synchronizing circuits, accumulator, low cost automation Hydro circuits, accumulators, Hydro pneumatic circuits principles of pneumatic circuit design.

UNIT – V:

Maintenance and trouble shooting of hydraulic and pneumatic circuits, components, PLC Automation and uses of Microprocessors.

REFERENCES:

- 1 Oil Hydraulic Systems/ S.R. Majumdar/ Tata Mc. Graw Hill.
- 2 Pneumatic systems, principles and maintenance/ S.R. Majumdar/Tata Mc.GrawHill.
- 3 Hydraulic and pneumatics/ Andrew Darr/ Jaico Publishing Hoise.
- 4 Fluid power with applications/ Antony Esponssito/ Prentice Hall.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

FLEXIBLE MANUFACTURING SYSTEMS**UNIT- I:**

Introduction to flexible manufacturing systems. Planning and scheduling and control of FMS. Knowledge based scheduling.

UNIT - II:

Hierarchy of computer control. Supervisory computer.

UNIT - III:

Software for simulation and database of FMS. Specification and selection, trends, application of simulation software.

UNIT - IV:

Manufacturing data systems data flow, CAD/CAM considerations. Planning FMS database, just in time characteristics, Pull method, quality small lot sizes, work station loads, close supplier ties, flexible workforce — line flow strategy.

UNIT - V:

Preventive maintenance. Karban system, implementation issues.

REFERENCES:

1. Hand Book of Flexible Manufacturing Systems/ Jha N K/ Academic Press.
2. Production System Beyond Large Scale Production/ Talichi Ohno/ Toyota Productivity Press India Pvt. Ltd.
3. Flexible Manufacturing Systems/ H K Shivanand/New Age International/2006

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

COMPUTER AIDED MANUFACTURING

UNIT - I

Compute-Aided Programming: General information, APT programming, Examples Apt programming problems (2D machining only). NC programming on CAD/CAM systems, the design and implementation of post processors. Introduction to CAD/CAM software, Automatic Tool Path generation.

UNIT - II

Tooling for CNC Machines: Interchangeable tooling system, preset and qualified tools, coolant fed tooling system, modular fixturing, quick change tooling system, automatic head changers. DNC Systems and Adaptive Control: Introduction, type of DNC systems, advantages and disadvantages of DNC, adaptive control with optimization, Adaptive control with constraints, Adaptive control of machining processes like turning, grinding.

UNIT - III

Post Processors for CNC: Introduction to Post Processors: The necessity of a Post Processor, the general structure of a Post Processor, the functions of a Post Processor, DAPP — based- Post Processor: Communication channels and major variables in the DAPP — based Post Processor, the creation of a DAPP — Based Post Processor.

UNIT - IV

Micro Controllers: Introduction, Hardware components, I/O pins, ports, external memory: counters, timers and serial data I/O interrupts. Selection of Micro Controllers Embedded Controllers, Applications and Programming of Micro Controllers. Programming Logic Controllers (PLC' s): Introduction, Hardware components of PLC, System, basic structure, principle of operations, Programming mnemonics timers, Internal relays and counters, Applications of PLC's in CNC Machines.

UNIT - V

Computer Aided Process Planning: Hybrid CAAP System, Computer Aided Inspection and quality control, Coordinate Measuring Machine, Limitations of CMM, Computer Aided Testing, Optical Inspection Methods, Artificial Intelligence and expert system: Artificial Neural Networks, Artificial Intelligence in CAD, Experts systems and its structures.

REFERENCES:

1. Computer Control of Manufacturing Systems / Yoram Koren / Mc Graw Hill. 1983.
2. Computer Aided Design Manufacturing – K. Lalit Narayan, K. Mallikarjuna Rao and M.M.M. Sarcar, PHI, 2008.
3. CAD/CAM Principles and Applications, P.N.Rao, TMH.
4. CAD / CAM Theory and Practice, / Ibrahim Zeid, TMH.
5. CAD / CAM / CIM, Radhakrishnan and Subramanian, New Age.
6. Principles of Computer Aided Design and Manufacturing, Farid Amirouche, Pearson.
7. Computer Numerical Control Concepts and programming, Warren S Seames, Thomson/2008.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

INDUSTRIAL ROBOTICS

UNIT - I

Introduction: Automation and Robotics, Robot anatomy, robot configuration, motions joint notation work volume, robot drive system, control system and dynamic performance, precision of movement.

Control System and Components: basic concept and medias controllers control system analysis, robot activation and feedback components. Positions sensors, velocity sensors, actuators sensors, power transmission system.

UNIT - II

Motion Analysis and Control: Manipulator kinematics, position representation forward transformation, homogeneous transformation, manipulator path control, robot dynamics, configuration of robot controller.

UNIT - III

End Effectors: Grippers-types, operation, mechanism, force analysis, tools as end effectors consideration in gripper selection and design. **SENSORS:** Desirable features, tactile, proximity and range sensors, uses sensors in robotics.

Machine Vision: Functions, Sensing and Digitizing-imaging, Devices, Lighting techniques, Analog to digital single conversion, image storage, Image processing and Analysis-image data reduction, Segmentation feature extraction. Object recognition, training the vision system, Robotics application.

UNIT - IV

Robot Programming: Lead through programming, Robot programming as a path in space, Motion interpolation, WAIT, SINONAL AND DELAY commands, Branching capabilities and Limitations.

Robot Languages: Textual robot Languages, Generation, Robot language structures, Elements in function.

UNIT - V

Robot Cell Desgin and Control: Robot cell layouts-Robot centered cell, In-line robot cell, Considerations in work design, Work and control, Inter locks, Error detect ion, Work wheel controller.

Robot Application: Material transfer, Machine loading/unloading. Processing operation, Assembly and Inspection, Feature Application.

REFERENCES:

1. Industrial Robotics / Groover M P /Pearson Edu.
2. Introduction to Robotic Mechanics and Control by JJ Craig, Pearson, 3rd edition.
3. Robotics / Fu K S/ McGraw Hill.
4. Robotic Engineering / Richard D. Klafter, Prentice Hall.
5. Robot Analysis and Intelligence / Asada and Slotine / Wiley Inter-Science.
6. Robot Dynamics & Control – Mark W. Spong and M. Vidyasagar / John Wiley & Sons (ASIA) Pte Ltd.
7. Robotics and Control / Mittal R K & Nagrath I J / TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

**PRODUCTION AND OPERATIONS MANAGEMENT
ELECTIVE – III****UNIT -I**

Operation Management: Definition – Objectives – Types of production systems – historical development of operations management – Current issues in operation management.

Product Design: Requirements of good product design – product development – approaches – concepts in product development – standardization – simplification – Speed to market – Introduction to concurrent engineering.

UNIT- II

Value Engineering – objective – types of values – function & cost – product life cycle- steps in value engineering – methodology in value engineers – FAST Diagram – Matrix Method.

Location – Facility location and layout – Factors considerations in Plant location- Comparative Study of rural and urban sites – Methods of selection plant layout – objective of good layout – Principles – Types of layout – line balancing.

UNIT III

Aggregate Planning : Definition – Different Strategies – Various models of Aggregate Planning – Transportation and graphical models. Advance inventory control systems push systems – Material Requirement – Terminology – types of demands – inputs to MRP- techniques of MRP – Lot sizing methods – benefits and drawbacks of MRP –Manufacturing Resources Planning (MRP –II), Pull systems – Vs Push system – Just in time (JIT) philosophy Kanban System – Calculation of number of Kanbans Requirements for implementation JIT – JIT Production process – benefits of JIT.

UNIT IV

Scheduling: Policies – Types of scheduling – Forward and Backward Scheduling – Gantt Charts – Flow shop Scheduling – n jobs and 2 machines, n jobs and 3 machines – job shop Scheduling – 2 jobs and n machines – Line of Balance.

UNIT V

Project Management: Programming Evaluation Review Techniques (PERT) – three times estimation – critical path – probability of completion of project – critical path method – crashing of simple nature.

REFERENCES:

1. Operations Management by E.S. Buffs.
2. Operations Management Theory and Problems: by Joseph G. Monks.
3. Production Systems Management by James I. Riggs.
4. Production and Operations Management by Chary.
5. Operations Management “ by Chase.
6. Production and Operation Management by Panner Selvam.
7. Production and Operation Analysis by Nahima.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

COMPUTATIONAL FLUID DYNAMICS

ELECTIVE – III

UNIT - I

Introduction: Finite difference method, finite volume method, finite element method, governing equations and boundary conditions, Derivation of finite difference equations.

Solution Methods: Solution methods of elliptical equations — finite difference formulations, interactive solution methods, direct method with Gaussian elimination. Parabolic equations-explicit schemes and Von Neumann stability analysis, implicit schemes, alternating direction implicit schemes, approximate factorization, fractional step methods, direct method with tridiagonal matrix algorithm.

UNIT – II

Hyperbolic Equations: explicit schemes and Von Neumann stability analysis, implicit schemes, multi step methods, nonlinear problems, second order one-dimensional wave equations. Burgers equations: Explicit and implicit schemes, Runge-Kutta method.

UNIT - III

Formulations of Incompressible Viscous Flows: Formulations of incompressible viscous flows by finite difference methods, pressure correction methods, vortex methods.

Treatment of Compressible Flows: potential equation, Euler equations, Navier-stokes system of equations, flow field-dependent variation methods, boundary conditions, example problems.

UNIT - IV

Finite Volume Method: Finite volume method via finite difference method, formulations for two and three-dimensional problems.

UNIT - V

Standard Variational Methods: Linear fluid flow problems, steady state problems, Transient problems.

REFERENCES:

1. Computational fluid dynamics/ T. J.C'hung/ Cambridge University press,2002.
2. Text book of fluid dynamics/ Frank Choriton/ CBS Publishers & distributors, 1985.
3. Numerical heat transfer and fluid flow / Suhas V. Patankar/ Hema shava Publishers corporation & Mc Graw Hill.
4. Computational Fluid Flow and Heat Transfer/ Muralidaran/ Narosa Publications.
5. Computational Fluid Dynamics: Basics with applications/John D. Anderson/ Mc Graw Hill.
6. Fundamentals of Computational Fluid Dynamics/Tapan K. Sengupta / Universities Press.
7. Introduction to Theoretical and Computational Fluid Dynamics/C. Pozrikidis /Oxford University Press/ 2nd Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

AUTOMATION IN MANUFACTURING

ELECTIVE-III

UNIT – I

Over View of Manufacturing and Automation : Production systems, Automation in production systems, Automation principles and strategies, Manufacturing operations, production facilities. Basic elements of an automated system, levels of automation; Hardware components for automation and process control, programmable logic controllers and personal computers.

UNIT – II:

Material Handling and Identification Technologies: Material handling, equipment, Analysis. Storage systems, performance and location strategies, Automated storage systems, AS/RS, types. Automatic identification methods, Barcode technology, RFID.

UNIT – III:

Manufacturing Systems and Automated Production Lines: Manufacturing systems: components of a manufacturing system, Single station manufacturing cells; Manual Assembly lines, line balancing Algorithms, Mixed model Assembly lines, Alternative Assembly systems. Automated production lines, Applications, Analysis of transfer lines.

UNIT – IV:

Automated Assembly Systems: Fundamentals, Analysis of Assembly systems. Cellular manufacturing, part families, cooling, production flow analysis. Group Technology and flexible Manufacturing systems, Quantitative Analysis.

UNIT – V:

Quality Control and Support Systems: Quality in Design and manufacturing, inspection principles and strategies, Automated inspection, contact Vs non contact, CMM. Manufacturing support systems. Quality function deployment, computer aided process planning, concurrent engineering, shop floor control, just in time and lean production.

REFERENCES:

1. Automation, production systems and computer integrated manufacturing/ Mikell.P Groover/PHI/3rd edition/2012.
2. Automation, Production Systems and CIM/ MikeJ P. Grower/PHI.
3. CAD/CAM/CIM/ P. Radha Krishnan & S. Subrahmanyarn and Raju/New Age International Publishers/ 2003.
4. Svstem Approach to Computer Integrated Design and Manufacturing/ Singh/John Wiley /96.
5. Computer Aided Manufacturing/Tien-Chien Chang, Richard A. Wysk and Hsu-Pin Wang/ Pearson/ 2009.
6. Manufacturing and Automation Technology / R Thomas Wright and Michael Berkeihiser / Good Heart/Willcox Publishers.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

DESIGN OPTIMIZATION

ELECTIVE – IV

UNIT- I:

General Characteristics of mechanical elements, adequate and optimum design, principles of optimization, formulation of objective function, design constraints, classification of optimization problems. Single and multivariable optimization techniques.

UNIT- II:

Technique of unconstrained minimization. Golden section, Random, Pattern and Gradient search methods, interpolation methods, equality and inequality constraints.

UNIT-III:

Direct methods and indirect methods using penalty function, Lagrange multipliers, Geometric programming, stochastic programming, Genetic algorithms.

UNIT-IV:

Engineering applications, structural-design application axial and transverse loaded members for minimum cost, maximum weight. Design of shafts and torsion members, design optimization of springs.

UNIT-V:

Dynamics applications for two degree freedom system. vibration absorbers. Application in mechanisms.

REFERENCES:

1. Engineering Optimization -Theory and Practice/ Singerusu S. Rao/ New Age.
2. Optimum Design of Mechanical elements/ Johnson Ray C/ Wiley, John & Sons.
3. Genetic Algorithms in search, Optimization and Machine/ Goldberg D. E. Addison/Wesley / NewYork..
4. Optimization for Engineering Design Algorithms and Examples/ Kalyanamoy Deb/Prentice Hall of India.
5. Introduction to Optimum Design/Jasbir S. Arora/ Academic Press/ Everest/ 3rd Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

INTELLIGENT MANUFACTURING SYSTEMS

ELECTIVE – IV

UNIT I:

Computer Integrated Manufacturing Systems Structure and functional areas of CIM system - CAD, CAPP, CAM, CAQC, ASRS. Advantages of CIM. Manufacturing Communication Systems - MAP/TOP, OSI Model, Data Redundancy, Top- down and Bottom-up Approach, Volume of Information. Intelligent Manufacturing System Components, System Architecture and Data Flow, System Operation.

UNIT II:

Components of Knowledge Based Systems - Basic Components of Knowledge Based Systems, Knowledge Representation, Comparison of Knowledge Representation Schemes, Interference Engine, Knowledge Acquisition.

UNIT III:

Machine Learning - Concept of Artificial Intelligence, Conceptual Learning, Artificial Neural Networks - Biological Neuron, Artificial Neuron, Types of Neural Networks, Applications in Manufacturing.

UNIT IV:

Automated Process Planning - Variant Approach, Generative Approach, Expert Systems for Process Planning, Feature Recognition, Phases of Process planning. Knowledge Based System for Equipment Selection (KBSES) - Manufacturing system design. Equipment Selection Problem, Modeling the Manufacturing Equipment Selection Problem, Problem Solving approach in KBSES, Structure of the KBSES.

UNIT V:

Group Technology: Models and Algorithms Visual Method, Coding Method, Cluster Analysis Method, Matrix Formation - Similarity Coefficient Method, Sorting-based Algorithms, Bond Energy Algorithm, Cost Based method, Cluster Identification Method, Extended CI Method. Knowledge Based Group Technology - Group Technology in Automated Manufacturing System. Structure of Knowledge based system for group technology (KBSGT) — Data Base, Knowledge Base, Clustering Algorithm.

REFERENCES:

1. Intelligent Manufacturing Systems/ Andrew Kusiak/Prentice Hall.
2. Artificial Neural Networks/ Yagna Narayana/PHI/2006.
3. Automation, Production Systems and CIM / Groover M.P./PHI/2007.
4. Neural networks: A comprehensive foundation/ Simon Hhaykin/ PHI.
5. Artificial neural networks/ B.Vegnanarayana/PHI.
6. Neural networks in Computer intelligence/ Li Min Fu/ TMH/2003.
7. Neural networks/ James A Freeman David M S kapura/ Pearson education/2004.
8. Introduction to Artificial Neural Systems/Jacek M. Zurada/JAICO Publishing House Ed. 2006.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

**COMPUTER AIDED PROCESS PLANNING
ELECTIVE – IV****UNIT-I**

Introduction to CAPP: Information requirement for process planning system, Role of process planning, advantages of conventional process planning over CAPP, Structure of Automated process planning system, feature recognition, methods.

Generative CAPP System: Importance. principle of Generative CAPP system, automation of logical decisions, Knowledge based systems, Inference Engine, implementation, benefits.

UNIT-II

Retrieval CAPP system: Significance, group technology, structure, relative advantages, implementation, and applications.

Selection of Manufacturing Sequence: Significance, alternative-manufacturing processes, reduction of total set-up cost for a particular sequence. quantitative methods for optimal selection, examples.

UNIT-III

Determination of Machining Parameters: reasons for optimal selection of machining parameters, effect of parameters on production rate, cost and surface quality, different approaches, advantages of mathematical approach over conventional approach, solving optimization models of machining processes.

UNIT -IV

Determination of Manufacturing Tolerances: design tolerances, manufacturing tolerances, methods of tolerance allocation, sequential approach, integration of design and manufacturing tolerances, advantages of integrated approach over sequential approach.

UNIT -V

Generation of Tool Path: Simulation of machining processes, NC tool path generation, graphical implementation, determination of optimal index positions for executing fixed sequence, quantitative methods.

Implementation Techniques for CAPP: MIPLAN system, Computer programming languages for CAPP, criteria for selecting a CAPP system and benefits of CAPP. Computer integrated planning systems, and Capacity planning system.

REFERENCES:

1. Automation, Production systems and Computer Integrated Manufacturing System/ Mikell P Groover.
2. Computer Design and Manufacturing/Sadhu Singh.
3. Computer Engineering /David Bedworth.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.Tech I year II Sem. (CAD/CAM)

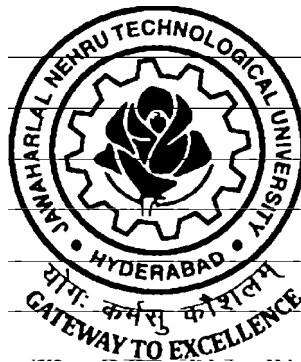
COMPUTER AIDED MACHINING & ROBOTICS LABORATORY

Features and selection of CNC turning and milling centers. Practice in part programming and operation of CNC turning machines, subroutine techniques and use of cycles. Practice in part programming and operating a machining center, tool Joining and selection of sequences of operations, tool setting on machine, practice in APT based NC programming. Practice in Robot programming and its languages. Robotic simulation using software. Robot path control, preparation of various reports and route sheets, Simulation of manufacturing system using CAM software, controller operating system commands.

ACADEMIC REGULATIONS COURSE STRUCTURE AND DETAILED SYLLABUS

M.TECH COMPUTER SCIENCE ENGINEERING

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - COMPUTER SCIENCE ENGINEERING

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		Advanced Data Structures and Algorithms	3	-	3
		Computer System Design	3	-	3
		Advanced Operating Systems	3	-	3
		Distributed Systems	3	-	3
	Elective -I	Software Process and Project Management Natural Language Processing Pattern Recognition	3	-	3
	Elective -II	Machine Learning Parallel and Distributed Algorithms Software Architecture and Design Patterns	3	-	3
	Lab	Advanced Data Structures and Algorithms Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Advanced Network Programming	3	-	3
		Advanced Databases	3	-	3
		Web Services and Service Oriented Architecture	3	-	3
		Grid and Cloud Computing	3	-	3
	Elective -III	Advanced Data Mining Storage Area Networks Database Security	3	-	3
	Elective -IV	Semantic Web and Social Networks Wireless Networks and Mobile Computing Information Retrieval Systems	3	-	3
	Lab	Web Services Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	0	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

ADVANCED DATA STRUCTURES AND ALGORITHMS**Objectives:**

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.
- Significance of algorithms in the computer field.
- Various aspects of algorithm development.
- Qualities of a good solution.

UNIT I

Algorithms, Performance analysis- time complexity and space complexity, Asymptotic Notation-Big Oh, Omega and Theta notations, Complexity Analysis Examples.

Data structures-Linear and non linear data structures, ADT concept, Linear List ADT, Array representation, Linked representation, Vector representation, singly linked lists -insertion, deletion, search operations, doubly linked lists-insertion, deletion operations, circular lists. Representation of single, two dimensional arrays, Sparse matrices and their representation.

UNIT II

Stack and Queue ADTs, array and linked list representations, infix to postfix conversion using stack, implementation of recursion, Circular queue-insertion and deletion, Dequeue ADT, array and linked list representations, Priority queue ADT, implementation using Heaps, Insertion into a Max Heap, Deletion from a Max Heap, java.util package-ArrayList, Linked List, Vector classes, Stacks and Queues in java.util, Iterators in java.util.

UNIT III

Searching–Linear and binary search methods, Hashing-Hash functions, Collision Resolution methods-Open Addressing, Chaining, Hashing in java.util-HashMap, HashSet, Hashtable.

Sorting –Bubble sort, Insertion sort, Quick sort, Merge sort, Heap sort, Radix sort, comparison of sorting methods.

UNIT IV

Trees- Ordinary and Binary trees terminology, Properties of Binary trees, Binary tree ADT, representations, recursive and non recursive traversals, Java code for traversals, Threaded binary trees.

Graphs- Graphs terminology, Graph ADT, representations, graph traversals/search methods-dfs and BFS, Java code for graph traversals, Applications of Graphs-Minimum cost spanning tree using Kruskal's algorithm, Dijkstra's algorithm for Single Source Shortest Path Problem.

UNIT V

Search trees- Binary search tree-Binary search tree ADT, insertion, deletion and searching operations, Balanced search trees, AVL trees-Definition and examples only, Red Black trees –Definition and examples only, B-Trees-definition, insertion and searching operations, Trees in java.util- TreeSet, Tree Map Classes, Tries(examples only),Comparison of Search trees.

Text compression-Huffman coding and decoding, Pattern matching-KMP algorithm.

TEXT BOOKS:

1. Data structures, Algorithms and Applications in Java, S.Sahni, Universities Press.
2. Data structures and Algorithms in Java, Adam Drozdek, 3rd edition, Cengage Learning.
3. Data structures and Algorithm Analysis in Java, M.A.Weiss, 2nd edition, Addison-Wesley (Pearson Education).

REFERENCE BOOKS:

1. Java for Programmers, Deitel and Deitel, Pearson education.
2. Data structures and Algorithms in Java, R.Lafore, Pearson education.
3. Java: The Complete Reference, 8th editon, Herbert Schildt, TMH.
4. Data structures and Algorithms in Java, M.T.Goodrich, R.Tomassia, 3rd edition, Wiley India Edition.
5. Data structures and the Java Collection Frame work,W.J.Collins, Mc Graw Hill.
6. Classic Data structures in Java, T.Budd, Addison-Wesley (Pearson Education).
7. Data structures with Java, Ford and Topp, Pearson Education.
8. Data structures using Java, D.S.Malik and P.S.Nair, Cengage learning.
9. Data structures with Java, J.R.Hubbard and A.Huray, PHI Pvt. Ltd.
10. Data structures and Software Development in an Object-Oriented Domain, J.P.Tremblay and G.A.Cheston, Java edition, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

COMPUTER SYSTEM DESIGN

Objectives:

- To apply the fundamentals of Computer Systems Design and IT in devising IT solutions.
- To design, simulate, and analyze digital hardware.
- To Interface between basic hardware and software computing systems.
- To Simulate and evaluate different computing architectures.

UNIT I

Computer structure – hardware, software, system software, Von-Neumann architecture – case study. IA-32 Pentium: registers and addressing, instructions, assembly language, program flow control, logic and shift/rotate instructions, multiply, divide MMX, SIMD instructions, I/O operations, subroutines.

Input/output organization, interrupts, DMA, Buses, Interface circuits, I/O interfaces, device drivers in windows, interrupt handlers.

UNIT II

Processing Unit: Execution of a complete instruction, multiple bus organization, hardwired control, micro programmed control.

Pipelining: data hazards, instruction hazards, influence on instruction sets, data path & control consideration, and RISC architecture introduction.

UNIT – III

Memory: types and hierarchy, model level organization, cache memory, performance considerations, mapping, virtual memory, swapping, paging, segmentation, replacement policies.

UNIT – IV

Processes and Threads: processes, threads, inter process communication, classical IPC problems, Deadlocks.

UNIT – V

File system: Files, directories, Implementation, Unix file system.

Security: Threats, intruders, accident data loss, basics of cryptography, user authentication.

TEXT BOOKS:

1. Computer Organization – Car Hamacher, Zvonks Vranesic, SafeaZaky, Vth Edition, McGraw Hill.
2. Modern Operating Systems, Andrew S Tanenbaum 2nd edition Pearson/PHI.

REFERENCE BOOKS:

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson /PHI.
2. Morris Mano- Computer System Architecture –3rd Edition-Pearson Education.
3. Operating System Principles- Abraham Silberchatz, Peter B. Galvin, Greg Gagne 7th Edition, John Wiley.
4. Operating Systems – Internals and Design Principles Stallings, Fifth Edition–2005, Pearson Education/ PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

ADVANCED OPERATING SYSTEMS**Objectives:**

- To understand main components of Real time Operating system and their working.
- To study the operations performed by OS as a resource manager.
- To understand the scheduling policies of DOS.
- To implement the working principles of OS.
- To study different OS and compare their features.

UNIT I:**Real-time operating systems:** Design issues, principles and case study.**UNIT II:****Distributed operating system:** Design issues, features and principles of working, case study.**UNIT III:****Network operating system:** Design issues, working principles and characteristic features, case study.**UNIT IV:****Kernel development:** Issues and development principles, case study.**UNIT V:**

Protection, privacy, access control and security issues, solutions.

TEXT BOOKS:

1. A. Silberschatz - Applied Operating System Concepts, Wiley, 2000.
2. Lubemir F Bic and Alan C. Shaw - Operating System Principles, Pearson Education, 2003.

REFERENCE BOOKS:

1. Operating Systems : Internal and Design Principles - Stallings, 6th ed., PE.
2. Modern Operating Systems, Andrew S Tanenbaum 3rd ed., PE.
3. Operating System Principles- Abraham Silberchatz, Peter B. Galvin, Greg Gagne, 7th ed., John Wiley.
4. UNIX User Guide – Ritchie & Yates.
5. UNIX Network Programming - W.Richard Stevens ,1998, PHI.
6. The UNIX Programming Environment – Kernighan & Pike, PE.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – I Sem. (Computer Science & Engg.)****DISTRIBUTED SYSTEMS**

Objectives:

- To explain what a distributed system is, why you would design a system as a distributed system, and what the desired properties of such systems are.
- To list the principles underlying the functioning of distributed systems, describe the problems and challenges associated with these principles and evaluate the effectiveness and shortcomings of their solutions.
- To recognize how the principles are applied in contemporary distributed systems, explain how they affect the software design, and be able to identify features and design decisions that may cause problems.
- To design a distributed system that fulfills requirements with regards to key distributed systems properties (such as scalability, transparency, etc.), be able to recognize when this is not possible, and explain why.
- To build distributed system software using basic OS mechanisms as well as higher-level middleware and languages.

UNIT I

Characterization of Distributed Systems- Introduction, Examples of Distributed systems, Resource sharing and web, challenges, System models- Introduction, Architectural and Fundamental models, Networking and Internetworking, Interprocess Communication.

Distributed objects and Remote Invocation-Introduction, Communication between distributed objects, RPC, Events and notifications, Case study-Java RMI.

UNIT II

Operating System Support- Introduction, OS layer, Protection, Processes and Threads, Communication and Invocation, Operating system architecture, Distributed File Systems-Introduction, File Service architecture, case study- SUN network file systems.

Name Services-Introduction, Name Services and the Domain Name System, Case study of the Global Name Service, Case study of the X.500 Directory Service.

UNIT III

Peer to Peer Systems-Introduction, Napster and its legacy, Peer to Peer middleware, Routing overlays, Overlay case studies-Pastry, Tapestry, Application case studies-Squirrel, OceanStore.

Time and Global States-Introduction, Clocks, events and Process states, Synchronizing physical clocks, logical time and logical clocks, global states, distributed debugging.

Coordination and Agreement - Introduction, Distributed mutual exclusion, Elections, Multicast communication, consensus and related problems.

UNIT IV

Transactions and Concurrency control - Introduction, Transactions, Nested Transactions, Locks, Optimistic concurrency control, Timestamp ordering, Comparison of methods for concurrency controls. Distributed Transactions - Introduction, Flat and Nested Distributed Transactions, Atomic commit protocols, Concurrency control in distributed transactions, Distributed deadlocks, Transaction recovery, Replication-Introduction, System model and group communication, Fault tolerant services, Transactions with replicated data.

UNIT V

Security - Introduction, Overview of Security techniques, Cryptographic algorithms, Digital signatures, Case studies-Kerberos, TLS, 802.11 WiFi.

Distributed shared memory, Design and Implementation issues, Sequential consistency and Ivy case study, Release consistency and Munin case study, other consistency models, CORBA case study-Introduction, CORBA RMI, CORBA Services.

TEXT BOOKS:

1. Distributed Systems Concepts and Design, G Coulouris, J Dollimore and T Kindberg, Fourth Edition, Pearson Education.
2. Distributed Systems, S.Ghosh, Chapman & Hall/CRC, Taylor & Francis Group, 2010.

REFERENCE BOOKS:

1. Distributed Computing, S.Mahajan and S.Shah, Oxford University Press.
2. Distributed Operating Systems Concepts and Design, Pradeep K.Sinha, PHI.
3. Advanced Concepts in Operating Systems, M Singhal, N G Shivarathri, Tata McGraw-Hill Edition.
4. Reliable Distributed Systems, K.P.Birman, Springer.
5. Distributed Systems – Principles and Paradigms, A.S. Tanenbaum and M.V. Steen, Pearson Education.
6. Distributed Operating Systems and Algorithm Analysis, R.Chow, T.Johnson, Pearson.
7. Distributed Operating Systems, A.S.Tanenbaum, Pearson education.
8. Distributed Computing, Principles, Algorithms and Systems, Ajay D. Kshemakalyani & Mukesh Singhal, Cambridge, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

**SOFTWARE PROCESS AND PROJECT MANAGEMENT
(ELECTIVE-I)****Objectives:**

- Describe and determine the purpose and importance of project management from the perspectives of planning, tracking and completion of project.
- Compare and differentiate organization structures and project structures.
- Implement a project to manage project schedule, expenses and resources with the application of suitable project management tools.

UNIT I

Software Process Maturity :Software maturity Framework, Principles of Software Process Change, Software Process Assessment, The Initial Process, The Repeatable Process, The Defined Process, The Managed Process, The Optimizing Process.

Process Reference Models : Capability Maturity Model (CMM), CMMI, PCMM, PSP, TSP.

UNIT II

Software Project Management Renaissance : Conventional Software Management, Evolution of Software Economics, Improving Software Economics, The old way and the new way.

Life-Cycle Phases and Process artifacts : Engineering and Production stages, inception phase, elaboration phase, construction phase, transition phase, artifact sets, management artifacts, engineering artifacts and pragmatic artifacts, model based software architectures.

UNIT III

Workflows and Checkpoints of process : Software process workflows, Iteration workflows, Major milestones, Minor milestones, Periodic status assessments.

Process Planning: Work breakdown structures, Planning guidelines, cost and schedule estimating process, iteration planning process, Pragmatic planning.

UNIT IV

Project Organizations: Line-of- business organizations, project organizations, evolution of organizations, process automation.

Project Control and process instrumentation : The seven core metrics, management indicators, quality indicators, life-cycle expectations, Pragmatic software metrics, and metrics automation.

UNIT V

CCPDS-R Case Study and Future Software Project Management Practices : Modern Project Profiles, Next-Generation software Economics, Modern Process Transitions.

TEXT BOOKS:

1. Managing the Software Process, Watts S. Humphrey, Pearson Education, 1999.
2. Software Project Management, Walker Royce, Pearson Education, 1998.

REFERENCE BOOKS:

1. Effective Project Management: Traditional, Agile, Extreme, Robert Wysocki, Sixth edition, Wiley India, 2011.

2. An Introduction to the Team Software Process, Watts S. Humphrey, Pearson Education, 2000
3. Process Improvement essentials, James R. Persse, O'Reilly, 2006
3. Software Project Management, Bob Hughes & Mike Cotterell, fourth edition, TMH, 2006
4. Applied Software Project Management, Andrew Stellman & Jennifer Greene, O'Reilly, 2006.
5. Head First PMP, Jennifer Greene & Andrew Stellman, O'Reilly, 2007
6. Software Engineering Project Managent, Richard H. Thayer & Edward Yourdon, 2nd edition, Wiley India, 2004.
7. The Art of Project Management, Scott Berkun, SPD, O'Reilly, 2011.
8. Applied Software Project Management, Andrew Stellman & Jennifer Greene, SPD, O'Reilly, 2011.
9. Agile Project Management, Jim Highsmith, Pearson education, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

NATURAL LANGUAGE PROCESSING

(ELECTIVE-I)

Objectives:

- To acquire basic understanding of linguistic concepts and natural language complexity, variability.
- To acquire basic understanding of machine learning techniques as applied to language.
- To implement N-grams Models.

UNIT I

Introduction and Overview What is Natural Language Processing, hands-on demonstrations. Ambiguity and uncertainty in language. The Turing test. **Regular Expressions** Chomsky hierarchy, regular languages, and their limitations. Finite-state automata. Practical regular expressions for finding and counting language phenomena. A little morphology. Exploring a large corpus with regex tools. **Programming in Python** An introduction to programming in Python. Variables, numbers, strings, arrays, dictionaries, conditionals, iteration. The NLTK (Natural Language Toolkit) **String Edit Distance and Alignment** Key algorithmic tool: dynamic programming, a simple example, use in optimal alignment of sequences. String edit operations, edit distance, examples of use in spelling correction and machine translation.

UNIT II

Context Free Grammars Constituency, CFG definition, use and limitations. Chomsky Normal Form. Top-down parsing, bottom-up parsing and the problems with each. The desirability of combining evidence from both directions. **Non-probabilistic Parsing** Efficient CFG parsing with CYK, another dynamic programming algorithms. Early parser, Designing a little grammar, and parsing with it on some test data. **Probability:** Introduction to probability theory, Joint and conditional probability, marginals, independence, Bayes rule, combining evidence. Examples of applications in natural language. **Information Theory:** The “Shannon game”, motivated by language! Entropy, cross-entropy, information gain. Its application to some language phenomena.

UNIT III

Language modeling and Naive Bayes : Probabilistic language modeling and its applications. Markov models. N-grams. Estimating the probability of a word and smoothing. Generative models of language. Part of Speech Tagging and Hidden Markov Models, Viterbi Algorithm for Finding Most Likely HMM Path Dynamic programming with Hidden Markov Models and its use for part-of-speech tagging, Chinese word segmentation, prosody, information extraction, etc.

UNIT IV

Probabilistic Context Free Grammars : Weighted context free grammars. Weighted CYK. Pruning and beam search.

Parsing with PCFGs : A tree bank and what it takes to create one. The probabilistic version of CYK. How do humans parse? Experiments with eye-tracking. Modern parsers.

Maximum Entropy Classifiers : The maximum entropy principle and its relation to maximum likelihood. Maximum entropy classifiers and their application to document classification, sentence segmentation and other language tasks.

UNIT V

Maximum Entropy Markov Models & Conditional Random Fields : Part-of-speech tagging, noun-

phrase segmentation and information extraction models that combine maximum entropy and finite-state machines. State-of-the-art models for NLP.

Lexical Semantics Mathematics of Multinomial and Dirichlet distributions, Dirichlet as a smoothing for multinomial's.

Information Extraction & Reference Resolution: Various methods, including HMMs. Models of anaphora resolution. Machine learning methods for co reference.

TEXT BOOKS:

1. "Speech and Language Processing": Jurafsky and Martin, Prentice Hall.
2. "Statistical Natural Language Processing"- Manning and Schutze, MIT Press.
3. "Natural Language Understanding". James Allen. The Benajmins/Cummings Publishing Company.

REFERENCES BOOKS:

1. Cover, T. M. and J. A. Thomas: Elements of Information Theory. Wiley.
2. Charniak, E: Statistical Language Learning. The MIT Press.
3. Jelinek, F: Statistical Methods for Speech Recognition. The MIT Press.
4. Lutz and Ascher - "Learning Python", O'Reilly.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

PATTERN RECOGNITION

(ELECTIVE – I)

Objectives:

- To implement pattern recognition and machine learning theories.
- To design and implement certain important pattern recognition techniques.
- To apply the pattern recognition theories to applications of interest.
- To implement the entropy minimization, clustering transformation and feature ordering.

UNIT I

Introduction - Basic concepts, Applications, Fundamental problems in pattern Recognition system design, Design concepts and methodologies, Examples of Automatic Pattern recognition systems, Simple pattern recognition model.

Decision and Distance Functions - Linear and generalized decision functions, Pattern space and weight space, Geometrical properties, implementations of decision functions, Minimum-distance pattern classifications.

UNIT II

Probability - Probability of events: Random variables, Joint distributions and densities, Movements of random variables, Estimation of parameter from samples. **Statistical Decision Making** - Introduction, Baye's theorem, Multiple features, Conditionally independent features, Decision boundaries, Unequal cost of error, estimation of error rates, the leaving-one-out-techniques, characteristic curves, estimating the composition of populations. Baye's classifier for normal patterns.

UNIT III

Non Parametric Decision Making - Introduction, histogram, kernel and window estimation, nearest neighbour classification techniques. Adaptive decision boundaries, adaptive discriminate functions, Minimum squared error discriminate functions, choosing a decision making techniques.

Clustering and Partitioning - Hierarchical Clustering: Introduction, agglomerative clustering algorithm, the single-linkage, complete-linkage and average-linkage algorithm. Ward's method Partition clustering-Forg's algorithm, K-means's algorithm, Isodata algorithm.

UNIT IV

Pattern Preprocessing and Feature Selection - Introduction, distance measures, clustering transformation and feature ordering, clustering in feature selection through entropy minimization, features selection through orthogonal expansion, binary feature selection.

UNIT V

Syntactic Pattern Recognition & Application of Pattern Recognition -Introduction, concepts from formal language theory, formulation of syntactic pattern recognition problem, syntactic pattern description, recognition grammars, automata as pattern recognizers, Application of pattern recognition techniques in bio-metric, facial recognition, IRIS SCAN, Finger prints, etc.

TEXT BOOKS:

1. Gose. Johnsonbaugh. Jost. " Pattern recognition and Image Analysis", PHI.
2. Tou. Rafael. Gonzalez. "Pattern Recognition Principle", Pearson Education.

REFERENCE BOOKS:

1. Richard Duda, David Stork, "Pattern Classification", John Wiley.
2. Digital Image Processing, M.Anji Reddy, Y.Hari Shankar, BS Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

MACHINE LEARNING

(ELECTIVE –II)

Objectives:

- To be able to formulate machine learning problems corresponding to different applications.
- To understand a range of machine learning algorithms along with their strengths and weaknesses.
- To understand the basic theory underlying machine learning.
- To be able to apply machine learning algorithms to solve problems of moderate complexity.
- To be able to read current research papers and understands the issues raised by current research.

UNIT I

Introduction - Well-posed learning problems, Designing a learning system, Perspectives and issues in machine learning.

Concept learning and the general to specific ordering – Introduction, A concept learning task, Concept learning as search, Find-S: finding a maximally specific hypothesis, Version spaces and the candidate elimination algorithm, Remarks on version spaces and candidate elimination, Inductive bias.

UNIT II

Decision Tree learning – Introduction, Decision tree representation, Appropriate problems for decision tree learning, The basic decision tree learning algorithm, Hypothesis space search in decision tree learning, Inductive bias in decision tree learning, Issues in decision tree learning.

Artificial Neural Networks – Introduction, Neural network representation, Appropriate problems for neural network learning, Perceptions, Multilayer networks and the back propagation algorithm, Remarks on the back propagation algorithm, An illustrative example face recognition, Advanced topics in artificial neural networks.

Evaluation Hypotheses – Motivation, Estimation hypothesis accuracy, Basics of sampling theory, A general approach for deriving confidence intervals, Difference in error of two hypotheses, Comparing learning algorithms.

UNIT III

Bayesian learning – Introduction, Bayes theorem, Bayes theorem and concept learning, Maximum likelihood and least squared error hypotheses, Maximum likelihood hypotheses for predicting probabilities, Minimum description length principle, Bayes optimal classifier, Gibbs algorithm, Naïve Bayes classifier, An example learning to classify text, Bayesian belief networks The EM algorithm.

Computational learning theory – Introduction, Probability learning an approximately correct hypothesis, Sample complexity for Finite Hypothesis Space, Sample Complexity for infinite Hypothesis Spaces, The mistake bound model of learning - **Instance-Based Learning**- Introduction, k -Nearest Neighbour Learning, Locally Weighted Regression, Radial Basis Functions, Case-Based Reasoning, Remarks on Lazy and Eager Learning.

Genetic Algorithms – Motivation, Genetic Algorithms, An illustrative Example, Hypothesis Space Search, Genetic Programming, Models of Evolution and Learning, Parallelizing Genetic Algorithms.

UNIT IV

Learning Sets of Rules – Introduction, Sequential Covering Algorithms, Learning Rule Sets: Summary, Learning First Order Rules, Learning Sets of First Order Rules: FOIL, Induction as Inverted Deduction,

Inverting Resolution.

Analytical Learning - Introduction, Learning with Perfect Domain Theories: Prolog-EBG Remarks on Explanation-Based Learning, Explanation-Based Learning of Search Control Knowledge.

UNIT V

Combining Inductive and Analytical Learning – Motivation, Inductive-Analytical Approaches to Learning, Using Prior Knowledge to Initialize the Hypothesis, Using Prior Knowledge to Alter the Search Objective, Using Prior Knowledge to Augment Search Operators.

Reinforcement Learning – Introduction, The Learning Task, Q Learning, Non-Deterministic, Rewards and Actions, Temporal Difference Learning, Generalizing from Examples, Relationship to Dynamic Programming.

TEXT BOOKS:

1. Machine Learning – Tom M. Mitchell, - MGH.
2. Machine Learning: An Algorithmic Perspective, Stephen Marsland, Taylor & Francis (CRC).

REFERENCE BOOKS:

1. Machine Learning Methods in the Environmental Sciences, Neural Networks, William W Hsieh, Cambridge Univ Press.
2. Richard o. Duda, Peter E. Hart and David G. Stork, pattern classification, John Wiley & Sons Inc., 2001.
3. Chris Bishop, Neural Networks for Pattern Recognition, Oxford University Press, 1995.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

PARALLEL AND DISTRIBUTED ALGORITHMS

(ELECTIVE –II)

Objectives:

- To learn parallel and distributed algorithms development techniques for shared memory and message passing models.
- To study the main classes of parallel algorithms.
- To study the complexity and correctness models for parallel algorithms.

UNIT-I

Basic Techniques, Parallel Computers for increase Computation speed, Parallel & Cluster Computing.

UNIT-II

Message Passing Technique- Evaluating Parallel programs and debugging, Portioning and Divide and Conquer strategies examples.

UNIT-III

Pipelining- Techniques computing platform, pipeline programs examples.

UNIT-IV

Synchronous Computations, load balancing, distributed termination examples, programming with shared memory, shared memory multiprocessor constructs for specifying parallel sharing data parallel programming languages and constructs, open MP.

UNIT-V

Distributed shared memory systems and programming achieving constant memory distributed shared memory programming primitives, algorithms – sorting and numerical algorithms.

TEXT BOOK:

1. Parallel Programming, Barry Wilkinson, Michael Allen, Pearson Education, 2nd Edition.

REFERENCE BOOK:

1. Introduction to Parallel algorithms by Jaja from Pearson, 1992.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

SOFTWARE ARCHITECTURE AND DESIGN PATTERNS

(ELECTIVE –II)

Objectives:

After completing this course, the student should be able to:

- To understand the concept of patterns and the Catalog.
- To discuss the Presentation tier design patterns and their affect on: sessions, client access, validation and consistency.
- To understand the variety of implemented bad practices related to the Business and Integration tiers.
- To highlight the evolution of patterns.
- To how to add functionality to designs while minimizing complexity.
- To understand what design patterns really are, and are not.
- To learn about specific design patterns.
- To learn how to use design patterns to keep code quality high without overdesign.

UNIT I

Envisioning Architecture : The Architecture Business Cycle, What is Software Architecture, Architectural patterns, reference models, reference architectures, architectural structures and views.

Creating an Architecture : Quality Attributes, Achieving qualities, Architectural styles and patterns, designing the Architecture, Documenting software architectures, Reconstructing Software Architecture.

UNIT II

Analyzing Architectures : Architecture Evaluation, Architecture design decision making, ATAM, CBAM.

Moving from one system to many : Software Product Lines, Building systems from off the shelf components, Software architecture in future.

UNIT III

Patterns : Pattern Description, Organizing catalogs, role in solving design problems, Selection and usage.

Creational and Structural patterns : Abstract factory, builder, factory method, prototype, singleton, adapter, bridge, composite, façade, flyweight.

UNIT IV

Behavioral patterns : Chain of responsibility, command, Interpreter, iterator, mediator, memento, observer, state, strategy, template method, visitor.

UNIT V

Case Studies : A-7E – A case study in utilizing architectural structures, The World Wide Web - a case study in interoperability, Air Traffic Control – a case study in designing for high availability, Celsius Tech – a case study in product line development.

TEXT BOOKS:

1. Software Architecture in Practice, second edition, Len Bass, Paul Clements & Rick Kazman, Pearson Education, 2003.
2. Design Patterns, Erich Gamma, Pearson Education, 1995.

REFERENCE BOOKS:

1. Beyond Software architecture, Luke Hohmann, Addison wesley, 2003.
2. Software architecture, David M. Dikel, David Kane and James R. Wilson, Prentice Hall PTR, 2001.
3. Software Design, David Budgen, second edition, Pearson education, 2003.
4. Head First Design patterns, Eric Freeman & Elisabeth Freeman, O'REILLY, 2007.
5. Design Patterns in Java, Steven John Metsker & William C. Wake, Pearson education, 2006.
6. J2EE Patterns, Deepak Alur, John Crupi & Dan Malks, Pearson education, 2003.
7. Design Patterns in C#, Steven John metsker, Pearson education, 2004.
8. Pattern Oriented Software Architecture, F.Buschmann & others, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Computer Science & Engg.)

ADVANCED DATA STRUCTURES AND ALGORITHMS LAB

Objectives:

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.

Sample Problems on Data structures:

1. Write Java programs that use both recursive and non-recursive functions for implementing the following searching methods:
a) Linear search b) Binary search
2. Write Java programs to implement the following using arrays and linked lists.
a) List ADT
3. Write Java programs to implement the following using an array.
a) Stack ADT b) Queue ADT
4. Write a Java program that reads an infix expression and converts the expression to postfix form. (Use stack ADT).
5. Write a Java program to implement circular queue ADT using an array.
6. Write a Java program that uses both a stack and a queue to test whether the given string is a palindrome or not.
7. Write Java programs to implement the following using a singly linked list.
a) Stack ADT b) Queue ADT
8. Write Java programs to implement the deque (double ended queue) ADT using
a) Array b) Singly linked list c) Doubly linked list.
9. Write a Java program to implement priority queue ADT.
10. Write a Java program to perform the following operations:
a) Construct a binary search tree of elements.
b) Search for a key element in the above binary search tree.
c) Delete an element from the above binary search tree.
11. Write a Java program to implement all the functions of a dictionary (ADT) using Hashing.
12. Write a Java program to implement Dijkstra's algorithm for Single source shortest path problem.
13. Write Java programs that use recursive and non-recursive functions to traverse the given binary tree in
a) Preorder b) Inorder c) Postorder.
14. Write Java programs for the implementation of bfs and dfs for a given graph.
15. Write Java programs for implementing the following sorting methods:
a) Bubble sort d) Merge sort g) Binary tree sort
b) Insertion sort e) Heap sort
c) Quick sort f) Radix sort

16. Write a Java program to perform the following operations:
a) Insertion into a B-tree b) Searching in a B-tree
17. Write a Java program that implements Kruskal's algorithm to generate minimum cost spanning tree.
18. Write a Java program that implements KMP algorithm for pattern matching.

REFERENCE BOOKS:

1. Data Structures and Algorithms in java, 3rd edition, A.Drozdek, Cengage Learning.
2. Data Structures with Java, J.R.Hubbard, 2nd edition, Schaum's Outlines, TMH.
3. Data Structures and algorithms in Java, 2nd Edition, R.Lafore, Pearson Education.
4. Data Structures using Java, D.S.Malik and P.S. Nair, Cengage Learning.
5. Data structures, Algorithms and Applications in java, 2nd Edition, S.Sahani, Universities Press.
6. Design and Analysis of Algorithms, P.H.Dave and H.B.Dave, Pearson education.
7. Data Structures and java collections frame work, W.J.Collins, Mc Graw Hill.
8. Java: the complete reference, 7th editon, Herbert Schildt, TMH.
9. Java for Programmers, P.J.Deitel and H.M.Deitel, Pearson education / Java: How to Program P.J.Deitel and H.M.Deitel , 8th edition, PHI.
10. Java Programming, D.S.Malik,Cengage Learning.
11. A Practical Guide to Data Structures and Algorithms using Java, S.Goldman & K.Goldman, Chapman & Hall/CRC, Taylor & Francis Group.

(Note: Use packages like java.io, java.util, etc)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

ADVANCED NETWORK PROGRAMMING**Objectives:**

Computer network programming involves writing computer programs that enable processes to communicate with each other across a computer network.

Network programming is client-server programming

Interprocess communication, even if it is bidirectional, cannot be implemented in a perfectly symmetric way: to establish a communication channel between two processes, one process must take the initiative, while the other is waiting for it. Therefore, network programming unavoidably assumes a client-server model: The process initiating the communication is a client, and the process waiting for the communication to be initiated is a server. The client and server processes together form a distributed system. In a peer-to-peer communication, the program can act both as a client and a server.

UNIT – I

Linux Utilities- File handling utilities, Security by file permissions, Process utilities, Disk utilities, Networking utilities, Filters, Text processing utilities and Backup utilities.

Bourne again shell(bash) - Introduction, pipes and redirection, here documents, running a shell script, the shell as a programming language, shell meta characters, file name substitution, shell variables, command substitution, shell commands, the environment, quoting, test command, control structures, arithmetic in shell, shell script examples.

Review of C programming concepts-arrays, strings (library functions), pointers, function pointers, structures, unions, libraries in C.

UNIT - II

Files- File Concept, File types File System Structure, Inodes, File Attributes, file I/O in C using system calls, kernel support for files, file status information-stat family, file and record locking-lockf and fcntl functions, file permissions- chmod, fchmod, file ownership-chown, lchown , fchown, links-soft links and hard links – symlink, link, unlink.

File and Directory management – Directory contents, Scanning Directories- Directory file APIs.

Process- Process concept, Kernel support for process, process attributes, process control – process creation, replacing a process image, waiting for a process, process termination, zombie process, orphan process.

UNIT - III

Signals- Introduction to signals, Signal generation and handling, Kernel support for signals, Signal function, unreliable signals, reliable signals, kill, raise , alarm, pause, abort, sleep functions.

Interprocess Communication - Introduction to IPC mechanisms, Pipes- creation, IPC between related processes using unnamed pipes, FIFOs-creation, IPC between unrelated processes using FIFOs(Named pipes), differences between unnamed and named pipes, popen and pclose library functions, Introduction to message queues, semaphores and shared memory.

Message Queues- Kernel support for messages, UNIX system V APIs for messages, client/server example.

Semaphores-Kernel support for semaphores, UNIX system V APIs for semaphores.

UNIT – IV

Shared Memory- Kernel support for shared memory, UNIX system V APIs for shared memory, client/server example.

Network IPC - Introduction to Unix Sockets, IPC over a network, Client-Server model ,Address formats(Unix domain and Internet domain), Socket system calls for Connection Oriented - Communication, Socket system calls for Connectionless-Communication, Example-Client/Server Programs- Single Server-Client connection, Multiple simultaneous clients, Socket options – setsockopt, getsockopt, fcntl.

UNIT-V

Network Programming in Java-Network basics, TCP sockets, UDP sockets (datagram sockets), Server programs that can handle one connection at a time and multiple connections (using multithreaded server), Remote Method Invocation (Java RMI)-Basic RMI Process, Implementation details-Client-Server Application.

TEXT BOOKS:

1. Unix System Programming using C++, T.Chan, PHI.(Units II,III,IV).
2. Unix Concepts and Applications, 4th Edition, Sumitabha Das, TMH.(Unit I).
3. An Introduction to Network Programming with Java, Jan Graba, Springer, rp 2010.(Unit V).
4. Unix Network Programming ,W.R. Stevens, PHI.(Units II,III,IV).
5. Java Network Programming,3rd edition, E.R. Harold, SPD, O'Reilly.(Unit V).

REFERENCE BOOKS:

1. Linux System Programming, Robert Love, O'Reilly, SPD.
2. Advanced Programming in the UNIX environment, 2nd Edition, W.R.Stevens, Pearson Education.
3. UNIX for programmers and users, 3rd Edition, Graham Glass, King Ables, Pearson Education.
4. Beginning Linux Programming, 4th Edition, N.Matthew, R.Stones, Wrox, Wiley India Edition.
5. Unix Network Programming The Sockets Networking API, Vol.-I,W.R.Stevens, Bill Fenner, A.M.Rudoff, Pearson Education.
6. Unix Internals, U.Vahalia, Pearson Education.
7. Unix shell Programming, S.G.Kochan and P.Wood, 3rd edition, Pearson Education.
8. C Programming Language, Kernighan and Ritchie, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

ADVANCED DATABASES**Objectives:**

By the end of the course, you will know:

- History and Structure of databases.
- How to design a database.
- How to convert the design into the appropriate tables.
- Handling Keys appropriately.
- Enforcing Integrity Constraints to keep the database consistent.
- Normalizing the tables to eliminate redundancies.
- Querying relational data.
- Optimizing and processing the queries.
- Storage Strategies for easy retrieval of data through index.
- Triggers, Procedures and Cursors ,Transaction Management.
- Distributed databases management system concepts and Implementation.

UNIT I

Database System Applications, Purpose of Database Systems, View of Data – Data Abstraction, Instances and Schemas, Data Models – the ER Model, Relational Model, Other Models – Database Languages – DDL,DML, Database Access from Applications Programs, Transaction Management, Data Storage and Querying, Database Architecture, Database Users and Administrators, ER diagrams,. Relational Model: Introduction to the Relational Model – Integrity Constraints Over Relations, Enforcing Integrity constraints, Querying relational data, Logical data base Design, Introduction to Views –Altering Tables and Views, Relational Algebra, Basic SQL Queries, Nested Queries, Complex Integrity Constraints in SQL, Triggers.

UNIT II

Introduction to Schema Refinement – Problems Caused by redundancy, Decompositions – Problem related to decomposition, Functional Dependencies - Reasoning about FDS, Normal Forms – FIRST, SECOND, THIRD Normal forms – BCNF –Properties of Decompositions- Loss less- join Decomposition, Dependency preserving Decomposition, Schema Refinement in Data base Design – Multi valued Dependencies – FOURTH Normal Form, Join Dependencies, FIFTH Normal form.

UNIT III

Transaction Management: The ACID Properties, Transactions and Schedules, Concurrent Execution of Transactions – Lock Based Concurrency Control, Deadlocks – Performance of Locking – Transaction Support in SQL.

Concurrency Control: Serializability, and recoverability – Introduction to Lock Management – Lock Conversions, Dealing with Deadlocks, Specialized Locking Techniques – Concurrency Control without Locking.

Crash recovery: Introduction to Crash recovery, Introduction to ARIES, the Log, and Other Recovery related Structures, the Write-Ahead Log Protocol, Check pointing, recovering from a System Crash, Media recovery.

UNIT IV

Overview of Storage and Indexing: Data on External Storage, File Organization and Indexing – Clustered Indexes, Primary and Secondary Indexes, Index data Structures – Hash Based Indexing, Tree based Indexing.

Storing data: Disks and Files: -The Memory Hierarchy – Redundant Arrays of Independent Disks.

Tree Structured Indexing: Intuitions for tree Indexes, Indexed Sequential Access Methods (ISAM)

B+ Trees: A Dynamic Index Structure, Search, Insert, Delete.

Hash Based Indexing: Static Hashing, Extendable hashing, Linear Hashing, Extendable vs. Linear Hashing.

UNIT V

Distributed databases: Introduction to distributed databases, Distributed DBMS architectures, Storing data in a distributed DBMS, Distributed catalog management, Distributed query processing Updating distributed data, Distributed transactions, Distributed concurrency control, Distributed recovery.

TEXT BOOKS:

1. Data base Management Systems, Raghu Ramakrishnan, Johannes Gehrke, TMH, 3rd Edition, 2003.
2. Data base System Concepts, A.Silberschatz, H.F. Korth, S.Sudarshan, McGraw hill, VI edition, 2006.
3. Fundamentals of Database Systems 5th edition. Ramez Elmasri, Shamkant B.Navathe, Pearson Education, 2008.

REFERENCE BOOKS:

1. Introduction to Database Systems, C.J.Date,Pearson Education.
2. Database Management System Oracle SQL and PL/SQL, P.K.Das Gupta, PHI.
3. Database System Concepts, Peter Rob & Carlos Coronel, Cengage Learning, 2008.
4. Database Systems, A Practical approach to Design Implementation and Management Fourth edition, Thomas Connolly, Carolyn Begg, Pearson education.
5. Database-Principles, Programming, and Performance, P.O'Neil & E.O'Neil, 2nd ed., ELSEVIER.
6. Fundamentals of Relational Database Management Systems, S.Sumathi, S.Esakkirajan, Springer.
7. Introduction to Database Management, M.L.Gillenson and others, Wiley Student Edition.
8. Database Development and Management, Lee Chao, Auerbach publications, Taylor & Francis Group.
9. Distributed Databases Principles & Systems, Stefano Ceri, Giuseppe Pelagatti, TMH.
10. Principles of Distributed Database Systems, M. Tamer Ozsu, Patrick Valduriez, Pearson Education, 2nd Edition.
11. Distributed Database Systems, Chhanda Ray, Pearson.
12. Distributed Database Management Systems, S.K.Rahimi and F.S.Haug, Wiley.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – II Sem. (Computer Science & Engg.)****WEB SERVICES AND SERVICE ORIENTED ARCHITECTURE****Objectives:**

- To Understand Web Services and implementation model for SOA.
- To Understand the SOA, its Principles and Benefits.
- To Understand XML concepts.
- To Understand paradigms needed for testing Web Services.
- To explore different Test Strategies for SOA-based applications.
- To implement functional testing, compliance testing and load testing of Web Services.
- To Identify bug-finding ideas in testing Web Services.

UNIT-I

Evolution and Emergence of Web Services – Evolution of distributed computing. Core distributed computing technologies – client/server, CORBA, JAVA RMI, Micro Soft DCOM, MOM, Challenges in Distributed Computing, role of J2EE and XML in distributed computing, emergence of Web Services and Service Oriented Architecture (SOA). Introduction to Web Services – The definition of web services, basic operational model of web services, tools and technologies enabling web services, benefits and challenges of using web services.

UNIT -II

Web Service Architecture – Web services Architecture and its characteristics, core building blocks of web services, standards and technologies available for implementing web services, web services communication, basic steps of implementing web services. Describing Web Services – WSDL introduction, non functional service description, WSDL1.1 Vs WSDL 2.0, WSDL document, WSDL elements, WSDL binding, WSDL tools, WSDL port type, limitations of WSDL.

UNIT III

Brief Over View of XML – XML Document structure, XML namespaces, Defining structure in XML documents, Reuse of XML schemes, Document navigation and transformation. SOAP : Simple Object Access Protocol, Inter-application communication and wire protocols, SOAP as a messaging protocol, Structure of a SOAP message, SOAP envelope, Encoding, Service Oriented Architectures, SOA revisited, Service roles in a SOA, Reliable messaging, The enterprise Service Bus, SOA Development Lifecycle, SOAP HTTP binding, SOAP communication model, Error handling in SOAP.

UNIT – IV

Registering and Discovering Services : The role of service registries, Service discovery, Universal Description, Discovery, and Integration, UDDI Architecture, UDDI Data Model, Interfaces, UDDI Implementation, UDDI with WSDL, UDDI specification, Service Addressing and Notification, Referencing and addressing Web Services, Web Services Notification.

UNIT - V

SOA and web services security considerations, Network-level security mechanisms, Application-level security topologies, XML security standards, Semantics and Web Services, The semantic interoperability problem, The role of metadata, Service metadata, Overview of .NET and J2EE, SOA and Web Service Management, Managing Distributed System, Enterprise management Framework, Standard distributed management frameworks, Web service management, Richer schema languages, WS-Metadata Exchange.

TEXT BOOKS:

1. Web Services & SOA Principles and Technology, Second Edition, Michael P. Papazoglou.
2. Developing Java Web Services, R. Nagappan, R. Skoczylas, R.P. Sriganesh, Wiley India.
3. Developing Enterprise Web Services, S. Chatterjee, J. Webber, Pearson Education.

REFERENCE BOOKS:

1. XML, Web Services, and the Data Revolution, F.P.Coyle, Pearson Education.
2. Building web Services with Java, 2nd Edition, S. Graham and others, Pearson Education.
3. Java Web Services, D.A. Chappell & T. Jewell, O'Reilly, SPD.
4. McGovern, et al., "Java web Services Architecture", Morgan Kaufmann Publishers, 2005.
5. J2EE Wer Services, Richard Monson-Haefel, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

GRID AND CLOUD COMPUTING**Objectives:**

- To implement Basics, techniques and tools for Grid & Cloud Computing.
- To understand any kind of heterogeneous resources over a network using open standards.
- To implement the Service models.

UNIT-I

System models for advanced computing –clusters of cooperative computing, grid computing and cloud computing; software systems for advanced computing-service oriented software and parallel and distributed programming models with introductory details, Features of grid and cloud platform.

UNIT-II

Cloud Computing services models and features in Saas, Paas and Iaas.

Service oriented architecture and web services; Features of cloud computing architectures and simple case studies.

UNIT-III

Virtualization- Characteristic features, Taxonomy Hypervisor, Virtualization and Cloud Computing, Pros and Cons of Cloud Computing, Technology Examples/Case Studies.

UNIT-IV

Cloud programming Environmental- Map Reduce Hadoop Library from Apache, Open Source Cloud Software Systems –Eucalyptus.

UNIT-V

Grid Architecture and Service modeling, Grid resource management, Grid Application trends.

TEXT BOOKS:

1. Distributed and Cloud Computing, Kaittwang Geoffrey C.Fox and Jack J Dongrra, Elsevier India 2012.
2. Mastering Cloud Computing- Raj Kumar Buyya, Christian Vecchiola and S.Tanurai Selvi, TMH, 2012.

REFERENCE BOOKS:

1. Cloud Computing, John W. Ritting House and James F Ramsome, CRC Press, 2012.
2. Enterprise Cloud Computing, Gautam Shroff, Cambridge University Press, 2012.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

ADVANCED DATA MINING

(ELECTIVE –III)

Objectives:

- To develop the abilities of critical analysis to data mining systems and applications.
- To implement practical and theoretical understanding of the technologies for data mining.
- To understand the strengths and limitations of various data mining models.

UNIT-I

Data mining Overview and Advanced Pattern Mining: Data mining tasks – mining frequent patterns, associations and correlations, classification and regression for predictive analysis, cluster analysis, outlier analysis; advanced pattern mining in multilevel, multidimensional space – mining multilevel associations, mining multidimensional associations, mining quantitative association rules, mining rare patterns and negative patterns.

UNIT-II

Advance Classification: Classification by back propagation, support vector machines, classification using frequent patterns, other classification methods – genetic algorithms, rough set approach, fuzzy set approach.

UNIT-III

Advance Clustering : Density - based methods – DBSCAN, OPTICS, DENCLUE; Grid-Based methods – STING, CLIQUE; Exception – maximization algorithm; clustering High- Dimensional Data; Clustering Graph and Network Data.

UNIT-IV

Web and Text Mining: Introduction, web mining, web content mining, web structure mining, web usage mining, Text mining – unstructured text, episode rule discovery for texts, hierarchy of categories, text clustering.

UNIT-V

Temporal and Spatial Data Mining : Introduction; Temporal Data Mining – Temporal Association Rules, Sequence Mining, GSP algorithm, SPADE, SPIRIT Episode Discovery, Time Series Analysis, Spatial Mining – Spatial Mining Tasks, Spatial Clustering. Data Mining Applications.

TEXT BOOKS:

1. Data Mining Concepts and Techniques, Jiawei Han, Micheline Kamber, Jian pei, Morgan Kaufmann.
2. Data Mining Techniques – Arun K. Pujari, Universities Press.

REFERENCE BOOKS:

1. Introduction to Data Mining – Pang-Ning Tan, Vipin kumar, Michael Steinbach, Pearson.
2. Data Mining Principles & Applications – T.V Suresh Kumar, B.Esware Reddy, Jagadish S Kalimani, Elsevier.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

STORAGE AREA NETWORKS

(ELECTIVE-III)

Objectives:

- To understand Storage Area Networks characteristics and components.
- To become familiar with the SAN vendors and their products.
- To learn Fibre Channel protocols and how SAN components use them to communicate with each other.
- To become familiar with Cisco MDS 9000 Multilayer Directors and Fabric Switches Thoroughly learn Cisco SAN-OS features.
- To understand the use of all SAN-OS commands. Practice variations of SANOS features.

UNIT I:

Introduction to Storage Technology: Review data creation and the amount of data being created and understand the value of data to a business, challenges in data storage and data management, Solutions available for data storage, Core elements of a data center infrastructure, role of each element in supporting business activities.

UNIT II:

Storage Systems Architecture: Hardware and software components of the host environment, Key protocols and concepts used by each component ,Physical and logical components of a connectivity environment ,Major physical components of a disk drive and their function, logical constructs of a physical disk, access characteristics, and performance Implications, Concept of RAID and its components , Different RAID levels and their suitability for different application environments: RAID 0, RAID 1, RAID 3, RAID 4, RAID 5, RAID 0+1, RAID 1+0, RAID 6, Compare and contrast integrated and modular storage systems ,High-level architecture and working of an intelligent storage system.

UNIT III:

Introduction to Networked Storage: Evolution of networked storage, Architecture, components, and topologies of FC-SAN, NAS, and IP-SAN, Benefits of the different networked storage options, understand the need for long-term archiving solutions and describe how CAS fulfills the need, understand the appropriateness of the different networked storage options for different application environments.

UNIT IV:

Information Availability & Monitoring & Managing Datacenter: List reasons for planned/unplanned outages and the impact of downtime, Impact of downtime, Differentiate between business continuity (BC) and disaster recovery (DR) ,RTO and RPO, Identify single points of failure in a storage infrastructure and list solutions to mitigate these failures , Architecture of backup/recovery and the different backup/recovery topologies , replication technologies and their role in ensuring information availability and business continuity, Remote replication technologies and their role in providing disaster recovery and business continuity capabilities.

Identify key areas to monitor in a data center, Industry standards for data center monitoring and management, Key metrics to monitor for different components in a storage infrastructure, Key management tasks in a data center.

UNIT V:

Securing Storage and Storage Virtualization: Information security, Critical security attributes for information systems, Storage security domains, List and analyzes the common threats in each domain, Virtualization technologies, block-level and file-level virtualization technologies and processes.

Case Studies: The technologies described in the course are reinforced with EMC examples of actual solutions. Realistic case studies enable the participant to design the most appropriate solution for given sets of criteria.

TEXT BOOK:

1. EMC Corporation, Information Storage and Management, Wiley.

REFERENCE BOOKS:

1. Robert Spalding, "Storage Networks: The Complete Reference", Tata McGraw Hill, Osborne, 2003.
2. Marc Farley, "Building Storage Networks", Tata McGraw Hill, Osborne, 2001.
3. Meeta Gupta, Storage Area Network Fundamentals, Pearson Education Limited, 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

DATABASE SECURITY

(ELECTIVE-III)

Objectives:

- To learn the security of databases.
- To learn the design techniques of database security.
- To learn the secure software design.

UNIT I**Introduction:** Introduction to Databases Security, Problems in Databases Security, Controls, Conclusions.**Security Models -1:** Introduction Access Matrix Model, Take-Grant Model, Acten Model, PN Model, Hartson and Hsiao's Model, Fernandez's Model, Bussolati and Martella's Model for Distributed databases.**UNIT II****Security Models -2:** Bell and LaPadula's Model, Biba's Model, Dion's Model, Sea View Model, Jajodia and Sandhu's Model. The Lattice Model for the Flow Control conclusion.**Security Mechanisms :** Introduction, User Identification/Authentication Memory Protection, Resource Protection, Control Flow Mechanisms, Isolation Security, Functionalities in Some Operating Systems, Trusted Computer System Evaluation Criteria.**UNIT III****Security Software Design:** Introduction, A Methodological Approach to Security Software Design, Secure Operating System Design, Secure DBMS Design, Security Packages, Database Security Design.**UNIT IV****Statistical Database Protection & Intrusion Detection Systems:** Introduction Statistics Concepts and Definitions, Types of Attacks, Inference Controls evaluation Criteria for Control Comparison, .Introduction, IDES System, RETISS System, ASES System Discovery.**UNIT V****Models For The Protection Of New Generation Database Systems -1:** Introduction, A Model for the Protection of Frame Based Systems, A Model for the Protection of Object-Oriented Systems, SORION Model for the Protection of Object-Oriented Databases.**Models For The Protection Of New Generation Database Systems -2:** A Model for the Protection of New Generation Database Systems: the Orion Model, Jajodia and Kogan's Model, A Model for the Protection of Active Databases Conclusions.**TEXT BOOKS:**

1. Database Security and Auditing, Hassan A. Afyouni, India Edition, CENGAGE Learning, 2009.
2. Database Security, Castano, Second edition, Pearson Education.

REFERENCE BOOK:

1. Database security by alfred basta, melissa zgola, CENGAGE learning.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

SEMANTIC WEB AND SOCIAL NETWORKS

(ELECTIVE –IV)

Objectives:

- To learn Web Intelligence.
- To learn Knowledge Representation for the Semantic Web.
- To learn Ontology Engineering.
- To learn Semantic Web Applications, Services and Technology.
- To learn Social Network Analysis and semantic web.

UNIT –I: Web Intelligence

Thinking and Intelligent Web Applications, The Information Age, The World Wide Web, Limitations of Today's Web, The Next Generation Web, Machine Intelligence, Artificial Intelligence, Ontology, Inference engines, Software Agents, Berners-Lee www, Semantic Road Map, Logic on the semantic Web.

UNIT -II: Knowledge Representation for the Semantic Web

Ontologies and their role in the semantic web, Ontologies Languages for the Semantic Web –Resource Description Framework(RDF) / RDF Schema, Ontology Web Language(OWL), UML, XML/XML Schema.

UNIT-III: Ontology Engineering

Ontology Engineering, Constructing Ontology, Ontology Development Tools, Ontology Methods, Ontology Sharing and Merging, Ontology Libraries and Ontology Mapping, Logic, Rule and Inference Engines.

UNIT-IV: Semantic Web Applications, Services and Technology

Semantic Web applications and services, Semantic Search, e-learning, Semantic Bioinformatics, Knowledge Base, XML Based Web Services, Creating an OWL-S Ontology for Web Services, Semantic Search Technology, Web Search Agents and Semantic Methods.

UNIT-V: Social Network Analysis and semantic web

What is social Networks analysis, development of the social networks analysis, Electronic Sources for Network Analysis – Electronic Discussion networks, Blogs and Online Communities, Web Based Networks. Building Semantic Web Applications with social network features.

TEXT BOOKS:

1. Thinking on the Web - Berners Lee, Godel and Turing, Wiley inter science, 2008.
2. Social Networks and the Semantic Web, Peter Mika, Springer, 2007.

REFERENCE BOOKS:

1. Semantic Web Technologies, Trends and Research in Ontology Based Systems, J.Davies, R.Studer, P.Warren, John Wiley & Sons.
2. Semantic Web and Semantic Web Services -Liyang Lu Chapman and Hall/CRC Publishers,(Taylor & Francis Group).
3. Information Sharing on the semantic Web - Heiner Stuckenschmidt; Frank Van Harmelen, Springer Publications.
4. Programming the Semantic Web, T.Segaran, C.Evans, J.Taylor, O'Reilly, SPD.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

**WIRELESS NETWORKS AND MOBILE COMPUTING
(ELECTIVE - IV)****Objectives:**

The main objective of this course is to provide the students with the competences required for understanding and using the communications component of an universal communications environment. Students will be provided, in particular, with the knowledge required to understand.

- emerging communications networks,
- their computational demands,
- the classes of distributed services and applications enabled by these networks, and
- the computational means required to create the new networks and the new applications.

UNIT I

Wireless Networks: Wireless Network, Wireless Network Architecture, Wireless Switching Technology, Wireless Communication problem, Wireless Network Reference Model, Wireless Networking Issues & Standards. **Mobile Computing:** Mobile communication, Mobile computing, Mobile Computing Architecture, Mobile Devices, Mobile System Networks, Mobility Management

UNIT II

Wireless LAN: Infra red Vs radio transmission, Infrastructure and Ad-hoc Network, IEEE 802.11: System Architecture, Protocol Architecture, 802.11b, 802.11a, Newer Developments, HIPERLAN 1, HIPERLAN 2, Bluetooth : User Scenarios, Architecture.

UNIT III

Global System for Mobile Communications (GSM): Mobile Services, System Architecture, Protocols, Localization & Calling, Handover, Security. **GPRS:** GPRS System Architecture, **UMTS:** UMTS System Architecture. **LTE:** Long Term Evolution.

UNIT IV

Mobile Network Layer: Mobile IP: Goals, Assumptions, Entities and Terminology, IP Packet Delivery, Agent Discovery, Registration, Tunneling and Encapsulation, Optimizations, Dynamic Host Configuration Protocol (DHCP).

UNIT V

Mobile Transport Layer: Traditional TCP, Indirect TCP, Snooping TCP, Mobile TCP, Fast retransmit/fast recovery, Transmission /time-out freezing, Selective retransmission, Transaction oriented TCP, TCP over 2.5G/3G Wireless Networks.

TEXT BOOKS:

1. Jochen Schiller, "Mobile Communications", Pearson Education, Second Edition, 2008.
2. Dr. Sunilkumar, et al "Wireless and Mobile Networks: Concepts and Protocols", Wiley India.
3. Raj Kamal, "Mobile Computing", OXFORD UNIVERSITY PRESS.

REFERENCE BOOKS:

1. Asoke K Talukder, et al, "Mobile Computing", Tata McGraw Hill, 2008.
2. Matthew S.Gast, "802.11 Wireless Networks", SPD O'REILLY.
3. Ivan Stojmenovic, "Handbook of Wireless Networks and Mobile Computing", Wiley, 2007.
4. Kumkum Garg, "Mobile Computing", Pearson.
5. Handbook of Security of Networks, Yang Xiao, Frank H Li, Hui Chen, World Scientific, 2011.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – II Sem. (Computer Science & Engg.)****INFORMATION RETRIEVAL SYSTEMS****(ELECTIVE –IV)**

Objectives:

On completion of this course you should have gained a good understanding of the foundation concepts of information retrieval techniques and be able to apply these concepts into practice. Specifically, you should be able to:

- To use different information retrieval techniques in various application areas.
- To apply IR principles to locate relevant information large collections of data.
- To analyze performance of retrieval systems when dealing with unmanaged data sources.
- To implement retrieval systems for web search tasks.

UNIT I

Boolean retrieval. The term vocabulary and postings lists. Dictionaries and tolerant retrieval. Index construction. Index compression.

UNIT II

Scoring, term weighting and the vector space model. Computing scores in a complete search system. Evaluation in information retrieval. Relevance feedback and query expansion.

UNIT III

XML retrieval. Probabilistic information retrieval. Language models for information retrieval. Text classification. Vector space classification.

UNIT IV

Support vector machines and machine learning on documents, Flat clustering, Hierarchical clustering, Matrix decompositions and latent semantic indexing.

UNIT V

Web search basics. Web crawling and indexes, Link analysis.

TEXT BOOK:

1. Introduction to Information Retrieval , Christopher D. Manning and Prabhakar Raghavan and Hinrich Schütze, Cambridge University Press, 2008.

REFERENCE BOOKS:

1. Information Storage and Retrieval Systems: Theory and Implementation, Kowalski, Gerald, Mark T Maybury, Springer.
2. Modern Information Retrieval, Ricardo Baeza-Yates, Pearson Education, 2007.
3. Information Retrieval: Algorithms and Heuristics, David A Grossman and Ophir Frieder, 2nd Edition, Springer, 2004.
4. Information Retrieval Data Structures and Algorithms, William B Frakes, Ricardo Baeza-Yates, Pearson Education, 1992.
5. Information Storage & Retrieval, Robert Korfhage, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Computer Science & Engg.)

WEB SERVICES LAB

Objectives:

- To implement the technologies like WSDL, UDDI.
- To learn how to implement and deploy web service client and server.

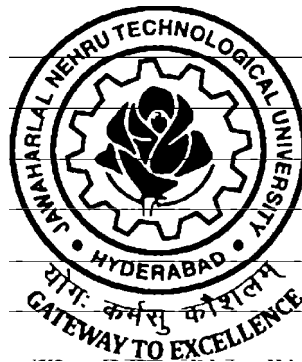
List of Programs:

1. Write a program to implement WSDL Service (Hello Service . WSDL File).
2. Write a program the service provider can be implement a single get price(), static bind() and get product operation.
3. Write a program to implement the operation can receive request and will return a response in two ways.
 - a) One-Way operation
 - b) Request - Response
4. Write a program to implement to create a simple web service that converts the temperature from Fahrenheit to Celsius (using HTTP Post Protocol).
5. Write a program to implement business UDDI Registry entry.
6. Write a program to implement.
 - a) Web based service consumer.
 - b) Windows application based web service consumer.

ACADEMIC REGULATIONS COURSE STRUCTURE AND DETAILED SYLLABUS

M.TECH EMBEDDED SYSTEMS

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - EMBEDDED SYSTEMS

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		Embedded System Design	3	-	3
		Microcontrollers for Embedded System Design	3	-	3
		Embedded Real Time Operating Systems	3	-	3
		Embedded C	3	-	3
	Elective -I	Advanced Computer Architecture VLSI Technology and Design Embedded Computing	3	-	3
	Elective -II	Digital System Design Soft Computing Techniques Advanced Operating Systems	3	-	3
	Lab	Embedded C Laboratory	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Hardware Software Co-Design	3	-	3
		Digital Signal Processors and Architectures	3	-	3
		Embedded Networking	3	-	3
		CPLD and FPGA Architectures and Applications	3	-	3
	Elective - III	Sensors and Actuators Wireless Communications and Networks Network Security and Cryptography	3	-	3
	Elective – IV	Multimedia and Signal Coding System On Chip Architecture Wireless LANs and PANs	3	-	3
	Lab	Embedded Systems Laboratory	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	0	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

EMBEDDED SYSTEMS DESIGN**UNIT -I:****Introduction to Embedded Systems**

Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT -II:**Typical Embedded System:**

Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT -III:**Embedded Firmware:**

Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT -IV:**RTOS Based Embedded System Design:**

Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT -V:

Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOK:

1. Introduction to Embedded Systems - Shibu K.V, Mc Graw Hill.

REFERENCE BOOKS:

1. Embedded Systems - Raj Kamal, TMH.
2. Embedded System Design - Frank Vahid, Tony Givargis, John Wiley.
3. Embedded Systems – Lyla, Pearson, 2013
4. An Embedded Software Primer - David E. Simon, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN**UNIT –I:****ARM Architecture:**

ARM Design Philosophy, Registers, Program Status Register, Instruction Pipeline, Interrupts and Vector Table, Architecture Revision, ARM Processor Families.

UNIT –II:**ARM Programming Model – I:**

Instruction Set: Data Processing Instructions, Addressing Modes, Branch, Load, Store Instructions, PSR Instructions, Conditional Instructions.

UNIT –III:**ARM Programming Model – II:**

Thumb Instruction Set: Register Usage, Other Branch Instructions, Data Processing Instructions, Single-Register and Multi Register Load-Store Instructions, Stack, Software Interrupt Instructions

UNIT –IV:**ARM Programming:**

Simple C Programs using Function Calls, Pointers, Structures, Integer and Floating Point Arithmetic, Assembly Code using Instruction Scheduling, Register Allocation, Conditional Execution and Loops.

UNIT –V:**Memory Management:**

Cache Architecture, Policies, Flushing and Caches, MMU, Page Tables, Translation, Access Permissions, Context Switch.

TEXT BOOKS:

1. ARM Systems Developer's Guides- Designing & Optimizing System Software – Andrew N. Sloss, Dominic Symes, Chris Wright, 2008, Elsevier.

REFERENCE BOOKS:

1. Embedded Microcomputer Systems, Real Time Interfacing – Jonathan W. Valvano – Brookes / Cole, 1999, Thomas Learning.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

EMBEDDED REAL TIME OPERATING SYSTEMS**UNIT – I:****Introduction**

Introduction to UNIX/LINUX, Overview of Commands, File I/O (open, create, close, lseek, read, write), Process Control (fork, vfork, exit, wait, waitpid, exec).

UNIT - II:**Real Time Operating Systems**

Brief History of OS, Defining RTOS, The Scheduler, Objects, Services, Characteristics of RTOS, Defining a Task, tasks States and Scheduling, Task Operations, Structure, Synchronization, Communication and Concurrency.

Defining Semaphores, Operations and Use, Defining Message Queue, States, Content, Storage, Operations and Use.

UNIT - III:**Objects, Services and I/O**

Pipes, Event Registers, Signals, Other Building Blocks, Component Configuration, Basic I/O Concepts, I/O Subsystem.

UNIT - IV:**Exceptions, Interrupts and Timers**

Exceptions, Interrupts, Applications, Processing of Exceptions and Spurious Interrupts, Real Time Clocks, Programmable Timers, Timer Interrupt Service Routines (ISR), Soft Timers, Operations.

UNIT - V:**Case Studies of RTOS**

RT Linux, MicroC/OS-II, Vx Works, Embedded Linux, Tiny OS, and Basic Concepts of Android OS.

TEXT BOOK:

1. Real Time Concepts for Embedded Systems – Qing Li, Elsevier, 2011.

REFERENCE BOOKS:

1. Embedded Systems- Architecture, Programming and Design by Rajkamal, 2007, TMH.
2. Advanced UNIX Programming, Richard Stevens.
3. Embedded Linux: Hardware, Software and Interfacing – Dr. Craig Hollabaugh.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems**EMBEDDED C****UNIT – I:****Programming Embedded Systems in C**

Introduction, What is an embedded system, Which processor should you use, Which programming language should you use, Which operating system should you use, How do you develop embedded software, Conclusions.

Introducing the 8051 Microcontroller Family

Introduction, What's in a name, The external interface of the Standard 8051, Reset requirements, Clock frequency and performance, Memory issues, I/O pins, Timers, Interrupts, Serial interface, Power consumption, Conclusions.

UNIT – II:**Reading Switches**

Introduction, Basic techniques for reading from port pins, Example: Reading and writing bytes, Example: Reading and writing bits (simple version), Example: Reading and writing bits (generic version), The need for pull-up resistors, Dealing with switch bounce, Example: Reading switch inputs (basic code), Example: Counting goats, Conclusions.

UNIT – III:**Adding Structure to the Code**

Introduction, Object-oriented programming with C, The Project Header (MAIN.H), The Port Header (PORT.H), Example: Restructuring the 'Hello Embedded World' example, Example: Restructuring the goat-counting example, Further examples, Conclusions.

UNIT – IV:**Meeting Real-Time Constraints**

Introduction, Creating 'hardware delays' using Timer 0 and Timer 1, Example: Generating a precise 50 ms delay, Example: Creating a portable hardware delay, Why not use Timer 2?, The need for 'timeout' mechanisms, Creating loop timeouts, Example: Testing loop timeouts, Example: A more reliable switch interface, Creating hardware timeouts, Example: Testing a hardware timeout, Conclusions.

UNIT – V:**Case Study: Intruder Alarm System**

Introduction, The software architecture, Key software components used in this example, running the program, the software, Conclusions.

TEXT BOOK:

1. Embedded C - Michael J. Pont, 2nd Ed., Pearson Education, 2008.

REFERENCE BOOK:

1. PICmicro MCU C-An introduction to programming, The Microchip PIC in CCS C - Nigel Gardner.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

**ADVANCED COMPUTER ARCHITECTURE
(ELECTIVE -I)****UNIT -I:****Fundamentals of Computer Design:**

Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, measuring and reporting performance, Quantitative principles of computer design, Amdahl's law.

Instruction set principles and examples- Introduction, classifying instruction set- memory addressing- type and size of operands, Operations in the instruction set.

UNIT –II:**Pipelines:**

Introduction, basic RISC instruction set, Simple implementation of RISC instruction set, Classic five stage pipe lined RISC processor, Basic performance issues in pipelining, Pipeline hazards, Reducing pipeline branch penalties.

Memory Hierarchy Design:

Introduction, review of ABC of cache, Cache performance, Reducing cache miss penalty, Virtual memory.

UNIT -III:**Instruction Level Parallelism (ILP) - The Hardware Approach:**

Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, High performance instruction delivery- Hardware based speculation.

ILP Software Approach:

Basic compiler level techniques, Static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues - Hardware verses Software.

UNIT –IV:**Multi Processors and Thread Level Parallelism:**

Multi Processors and Thread level Parallelism- Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – Memory architecture, Synchronization.

UNIT –V:**Inter Connection and Networks:**

Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.

Intel Architecture: Intel IA-64 ILP in embedded and mobile markets Fallacies and pit falls.

TEXT BOOK:

1. John L. Hennessy, David A. Patterson - Computer Architecture: A Quantitative Approach, 3rd Edition, an Imprint of Elsevier.

REFERENCE BOOKS:

1. John P. Shen and Miikko H. Lipasti -, Modern Processor Design : Fundamentals of Super Scalar Processors.
2. Computer Architecture and Parallel Processing - Kai Hwang, Faye A.Brigs., MC Graw Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

VLSI TECHNOLOGY AND DESIGN

(ELECTIVE -I)

UNIT –I:**Review of Microelectronics and Introduction to MOS Technologies:**

MOS, CMOS, BiCMOS Technology.

Basic Electrical Properties of MOS, CMOS & BiCMOS Circuits: $I_{ds} - V_{ds}$ relationships, Threshold Voltage V_T , G_m , G_{ds} and ω_0 , Pass Transistor, MOS, CMOS & Bi CMOS Inverters, Z_{pu}/Z_{pd} , MOS Transistor circuit model, Latch-up in CMOS circuits.

UNIT –II:**Layout Design and Tools:**

Transistor structures, Wires and Vias, Scalable Design rules, Layout Design tools.

Logic Gates & Layouts:

Static Complementary Gates, Switch Logic, Alternative Gate circuits, Low power gates, Resistive and Inductive interconnect delays.

UNIT –III:**Combinational Logic Networks:**

Layouts, Simulation, Network delay, Interconnect design, Power optimization, Switch logic networks, Gate and Network testing.

UNIT –IV:**Sequential Systems:**

Memory cells and Arrays, Clocking disciplines, Design, Power optimization, Design validation and testing.

UNIT –V:**Floor Planning:**

Floor planning methods, Global Interconnect, Floor Plan Design, Off-chip connections.

TEXT BOOKS:

1. Essentials of VLSI Circuits and Systems, K. Eshraghian Eshraghian. D, A. Pucknell, 2005, PHI.
2. Modern VLSI Design – Wayne Wolf, 3rd Ed., 1997, Pearson Education.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011.
2. Principles of CMOS VLSI Design – N.H.E Weste, K. Eshraghian, 2nd Ed., Addison Wesley.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

EMBEDDED COMPUTING**(ELECTIVE – I)****UNIT –I:****Programming on Linux Platform:**

System Calls, Scheduling, Memory Allocation, Timers, Embedded Linux, Root File System, Busy Box. Operating System Overview: Processes, Tasks, Threads, Multi-Threading, Semaphore, Message Queue.

UNIT –II:**Introduction to Software Development Tools:**

GNU GCC, make, gdb, static and dynamic linking, C libraries, compiler options, code optimization switches, lint, code profiling tools.

UNIT –III:**Interfacing Modules:**

Sensor and actuator interface, data transfer and control, GPS, GSM module interfacing with data processing and display, OpenCV for machine vision, Audio signal processing.

UNIT –IV:**Networking Basics:**

Sockets, ports, UDP, TCP/IP, client server model, socket programming, 802.11, Bluetooth, ZigBee, SSH, firewalls, network security.

UNIT –V:

IA32 Instruction Set: application binary interface, exception and interrupt handling, interrupt latency, assemblers, assembler directives, macros, simulation and debugging tools.

TEXT BOOKS:

1. Modern Embedded Computing - Peter Barry and Patrick Crowley, 1st Ed., Elsevier/Morgan Kaufmann, 2012.
2. Linux Application Development - Michael K. Johnson, Erik W. Troan, Addison Wesley, 1998.
3. Assembly Language for x86 Processors by Kip R. Irvine.
4. Intel® 64 and IA-32 Architectures Software Developer Manuals.

REFERENCE BOOKS:

1. Operating System Concepts by Abraham Silberschatz, Peter B. Galvin and Greg Gagne.
2. The Design of the UNIX Operating System by Maurice J. Bach Prentice-Hall.
3. UNIX Network Programming by W. Richard Stevens.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

DIGITAL SYSTEM DESIGN

(ELECTIVE -II)

UNIT -I:

Minimization and Transformation of Sequential Machines:

The Finite State Model – Capabilities and limitations of FSM – State equivalence and machine minimization – Simplification of incompletely specified machines.

Fundamental mode model – Flow table – State reduction – Minimal closed covers – Races, Cycles and Hazards.

UNIT -II:

Digital Design:

Digital Design Using ROMs, PALs and PLAs , BCD Adder, 32 – bit adder, State graphs for control circuits, Scoreboard and Controller, A shift and add multiplier, Array multiplier, Keypad Scanner, Binary divider.

UNIT -III:

SM Charts:

State machine charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, dice game controller.

UNIT -IV:

Fault Modeling & Test Pattern Generation:

Logic Fault model – Fault detection & Redundancy- Fault equivalence and fault location –Fault dominance – Single stuck at fault model – Multiple stuck at fault models –Bridging fault model.

Fault diagnosis of combinational circuits by conventional methods – Path sensitization techniques, Boolean Difference method – Kohavi algorithm – Test algorithms – D algorithm, PODEM, Random testing, Transition count testing, Signature analysis and test bridging faults.

UNIT -V:

Fault Diagnosis in Sequential Circuits:

Circuit Test Approach, Transition Check Approach – State identification and fault detection experiment, Machine identification, Design of fault detection experiment.

TEXT BOOKS:

1. Fundamentals of Logic Design – Charles H. Roth, 5th Ed., Cengage Learning.
2. Digital Systems Testing and Testable Design – Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman- John Wiley & Sons Inc.
3. Logic Design Theory – N. N. Biswas, PHI.

REFERENCE BOOKS:

1. Switching and Finite Automata Theory – Z. Kohavi , 2nd Ed., 2001, TMH.
2. Digital Design – Morris Mano, M.D.Ciletti, 4th Edition, PHI.
3. Digital Circuits and Logic Design – Samuel C. Lee, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

SOFT COMPUTING TECHNIQUES

(ELECTIVE -II)

UNIT –I:**Introduction:**

Approaches to intelligent control, Architecture for intelligent control, Symbolic reasoning system, Rule-based systems, the AI approach, Knowledge representation - Expert systems.

UNIT –II:**Artificial Neural Networks:**

Concept of Artificial Neural Networks and its basic mathematical model, McCulloch-Pitts neuron model, simple perceptron, Adaline and Madaline, Feed-forward Multilayer Perceptron, Learning and Training the neural network, Data Processing: Scaling, Fourier transformation, principal-component analysis and wavelet transformations, Hopfield network, Self-organizing network and Recurrent network, Neural Network based controller.

UNIT –III:**Fuzzy Logic System:**

Introduction to crisp sets and fuzzy sets, basic fuzzy set operation and approximate reasoning, Introduction to fuzzy logic modeling and control, Fuzzification, inferencing and defuzzification, Fuzzy knowledge and rule bases, Fuzzy modeling and control schemes for nonlinear systems, Self-organizing fuzzy logic control, Fuzzy logic control for nonlinear time delay system.

UNIT –IV:**Genetic Algorithm:**

Basic concept of Genetic algorithm and detail algorithmic steps, Adjustment of free parameters, Solution of typical control problems using genetic algorithm, Concept on some other search techniques like Tabu search and Ant-colony search techniques for solving optimization problems.

UNIT –V:**Applications:**

GA application to power system optimisation problem, Case studies: Identification and control of linear and nonlinear dynamic systems using MATLAB-Neural Network toolbox, Stability analysis of Neural-Network interconnection systems, Implementation of fuzzy logic controller using MATLAB fuzzy-logic toolbox, Stability analysis of fuzzy control systems.

TEXT BOOKS:

1. Introduction to Artificial Neural Systems - Jacek.M.Zurada, Jaico Publishing House, 1999.
2. Neural Networks and Fuzzy Systems - Kosko, B., Prentice-Hall of India Pvt. Ltd., 1994.

REFERENCE BOOKS:

1. Fuzzy Sets, Uncertainty and Information - Klir G.J. & Folger T.A., Prentice-Hall of India Pvt. Ltd., 1993.
2. Fuzzy Set Theory and Its Applications - Zimmerman H.J. Kluwer Academic Publishers, 1994.
3. Introduction to Fuzzy Control - Driankov, Hellendroon, Narosa Publishers.
4. Artificial Neural Networks - Dr. B. Yagananarayana, 1999, PHI, New Delhi.
5. Elements of Artificial Neural Networks - Kishan Mehrotra, Chelkuri K. Mohan, Sanjay Ranka, Penram International.
6. Artificial Neural Network –Simon Haykin, 2nd Ed., Pearson Education.
7. Introduction Neural Networks Using MATLAB 6.0 - S.N. Shivanandam, S. Sumati, S. N. Deepa, 1/e, TMH, New Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

ADVANCED OPERATING SYSTEMS**(ELECTIVE -II)****UNIT –I:****Introduction to Operating Systems:**

Overview of computer system hardware, Instruction execution, I/O function, Interrupts, Memory hierarchy, I/O Communication techniques, Operating system objectives and functions, Evaluation of operating System

UNIT –II:**Introduction to UNIX and LINUX:**

Basic commands & command arguments, Standard input, output, Input / output redirection, filters and editors, Shells and operations.

UNIT –III:**System Calls:**

System calls and related file structures, Input / Output, Process creation & termination.

Inter Process Communication

Introduction, file and record locking, Client – Server example, pipes, FIFOs, Streams & Messages, Name Spaces, Systems V IPC, Message queues, Semaphores, Shared Memory, Sockets & TLI.

UNIT –IV:**Introduction to Distributed Systems:**

Goals of distributed system, Hardware and software concepts, Design issues.

Communication in Distributed Systems:

Layered protocols, ATM networks, Client - Server model, Remote procedure call and Group communication.

UNIT –V:**Synchronization in Distributed Systems:**

Clock synchronization, Mutual exclusion, E-tech algorithms, Bully algorithm, Ring algorithm, Atomic transactions

Deadlocks:

Dead lock in distributed systems, Distributed dead lock prevention and distributed dead lock detection.

TEXT BOOKS:

1. The design of the UNIX Operating Systems – Maurice J. Bach, 1986, PHI.
2. Distributed Operating System - Andrew. S. Tanenbaum, 1994, PHI.
3. The Complete reference LINUX – Richard Peterson, 4th Ed., McGraw – Hill.

REFERENCE BOOKS:

1. Operating Systems: Internal and Design Principles - Stallings, 6th Ed., PE.
2. Modern Operating Systems, Andrew S Tanenbaum, 3rd Ed., PE.
3. Operating System Principles- Abraham Silberchatz, Peter B. Galvin, Greg Gagne, 7th Ed., John Wiley.
4. UNIX User Guide – Ritchie & Yates.

5. UNIX Network Programming - W.Richard Stevens, 1998, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. Embedded Systems

EMBEDDED C LABORATORY

Note:

- Minimum of 10 experiments have to be conducted.
 - The following programs have to be tested on 89C51 Development board/equivalent using Embedded C Language on Keil IDE or Equivalent.
1. Program to toggle all the bits of Port P1 continuously with 250 mS delay.
 2. Program to toggle only the bit P1.5 continuously with some delay. Use Timer 0, mode 1 to create delay.
 3. Program to interface a switch and a buzzer to two different pins of a Port such that the buzzer should sound as long as the switch is pressed.
 4. Program to interface LCD data pins to port P1 and display a message on it.
 5. Program to interface keypad. Whenever a key is pressed, it should be displayed on LCD.
 6. Program to interface seven segment display unit.
 7. Program to transmit a message from Microcontroller to PC serially using RS232.
 8. Program to receive a message from PC serially using RS232.
 9. Program to get analog input from Temperature sensor and display the temperature value on PC Monitor.
 10. Program to interface Stepper Motor to rotate the motor in clockwise and anticlockwise directions
 11. Program to Sort RTOS on to 89C51 development board.
 12. Program to interface Elevator.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – II Sem. Embedded Systems****HARDWARE - SOFTWARE CO-DESIGN****UNIT –I:****Co- Design Issues:**

Co- Design Models, Architectures, Languages, A Generic Co-design Methodology.

Co- Synthesis Algorithms:

Hardware software synthesis algorithms: hardware – software partitioning distributed system co-synthesis.

UNIT –II:**Prototyping and Emulation:**

Prototyping and emulation techniques, prototyping and emulation environments, future developments in emulation and prototyping architecture specialization techniques, system communication infrastructure.

Target Architectures:

Architecture Specialization techniques, System Communication infrastructure, Target Architecture and Application System classes, Architecture for control dominated systems (8051-Architectures for High performance control), Architecture for Data dominated systems (ADSP21060, TMS320C60), Mixed Systems.

UNIT –III:**Compilation Techniques and Tools for Embedded Processor Architectures:**

Modern embedded architectures, embedded software development needs, compilation technologies, practical consideration in a compiler development environment.

UNIT –IV:**Design Specification and Verification:**

Design, co-design, the co-design computational model, concurrency coordinating concurrent computations, interfacing components, design verification, implementation verification, verification tools, interface verification.

UNIT –V:**Languages for System – Level Specification and Design-I:**

System – level specification, design representation for system level synthesis, system level specification languages.

Languages for System – Level Specification and Design-II:

Heterogeneous specifications and multi language co-simulation, the cosyma system and lycos system.

TEXT BOOKS:

1. Hardware / Software Co- Design Principles and Practice – Jorgen Staunstrup, Wayne Wolf – 2009, Springer.
2. Hardware / Software Co- Design - Giovanni De Micheli, Mariagiovanna Sami, 2002, Kluwer Academic Publishers.

REFERENCE BOOK:

1. A Practical Introduction to Hardware/Software Co-design -Patrick R. Schaumont - 2010 – Springer.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES**UNIT –I:****Introduction to Digital Signal Processing:**

Introduction, a Digital signal-processing system, the sampling process, discrete time sequences. Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear time-invariant systems, Digital filters, Decimation and interpolation.

Computational Accuracy in DSP Implementations:

Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter.

UNIT –II:**Architectures for Programmable DSP Devices:**

Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation UNIT, Programmability and Program Execution, Speed Issues, Features for External interfacing.

UNIT -III:**Programmable Digital Signal Processors:**

Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX Instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX Processors, Pipeline Operation of TMS320C54XX Processors.

UNIT –IV:**Analog Devices Family of DSP Devices:**

Analog Devices Family of DSP Devices – ALU and MAC block diagram, Shifter Instruction, Base Architecture of ADSP 2100, ADSP-2181 high performance Processor.

Introduction to Black fin Processor - The Black fin Processor, Introduction to Micro Signal Architecture, Overview of Hardware Processing Units and Register files, Address Arithmetic Unit, Control Unit, Bus Architecture and Memory, Basic Peripherals.

UNIT –V:**Interfacing Memory and I/O Peripherals to Programmable DSP Devices:**

Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA).

TEXT BOOKS:

1. Digital Signal Processing – Avtar Singh and S. Srinivasan, Thomson Publications, 2004.
2. A Practical Approach To Digital Signal Processing - K Padmanabhan, R. Vijayarajeswaran, Ananthi. S, New Age International, 2006/2009.
3. Embedded Signal Processing with the Micro Signal Architecture: Woon-Seng Gan, Sen M. Kuo, Wiley-IEEE Press, 2007.

REFERENCE BOOKS:

1. Digital Signal Processors, Architecture, Programming and Applications – B. Venkataramani and M. Bhaskar, 2002, TMH.
2. Digital Signal Processing – Jonatham Stein, 2005, John Wiley.
3. DSP Processor Fundamentals, Architectures & Features – Lapsley et al. 2000, S. Chand & Co.
4. Digital Signal Processing Applications Using the ADSP-2100 Family by The Applications Engineering Staff of Analog Devices, DSP Division, Edited by Amy Mar, PHI.
5. The Scientist and Engineer's Guide to Digital Signal Processing by Steven W. Smith, Ph.D., California Technical Publishing, ISBN 0-9660176-3-3, 1997.
6. Embedded Media Processing by David J. Katz and Rick Gentile of Analog Devices, Newnes , ISBN 0750679123, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

EMBEDDED NETWORKING**UNIT –I:****Embedded Communication Protocols:**

Embedded Networking: Introduction – Serial/Parallel Communication – Serial communication protocols - RS232 standard – RS485 – Synchronous Serial Protocols -Serial Peripheral Interface (SPI) – Inter Integrated Circuits (I2C) – PC Parallel port programming - ISA/PCI Bus protocols – Firewire.

UNIT –II:**USB and CAN Bus:**

USB bus – Introduction – Speed Identification on the bus – USB States – USB bus communication: Packets –Data flow types –Enumeration –Descriptors –PIC 18 Microcontroller USB Interface – C Programs –CAN Bus – Introduction - Frames –Bit stuffing –Types of errors –Nominal Bit Timing – PIC microcontroller CAN Interface –A simple application with CAN.

UNIT –III:**Ethernet Basics:**

Elements of a network – Inside Ethernet – Building a Network: Hardware options – Cables, Connections and network speed – Design choices: Selecting components –Ethernet Controllers – Using the internet in local and internet communications – Inside the Internet protocol.

UNIT –IV:**Embedded Ethernet:**

Exchanging messages using UDP and TCP – Serving web pages with Dynamic Data – Serving web pages that respond to user Input – Email for Embedded Systems – Using FTP – Keeping Devices and Network secure.

UNIT –V:**Wireless Embedded Networking:**

Wireless sensor networks – Introduction – Applications – Network Topology – Localization –Time Synchronization - Energy efficient MAC protocols –SMAC – Energy efficient and robust routing – Data Centric routing.

TEXT BOOKS:

1. Embedded Systems Design: A Unified Hardware/Software Introduction - Frank Vahid, Tony Givargis, John & Wiley Publications, 2002.
2. Parallel Port Complete: Programming, interfacing and using the PCs parallel printer port - Jan Axelson, Penram Publications, 1996.

REFERENCE BOOKS:

1. Advanced PIC microcontroller projects in C: from USB to RTOS with the PIC18F series - Dogan Ibrahim, Elsevier 2008.
2. Embedded Ethernet and Internet Complete - Jan Axelson, Penram publications, 2003.
3. Networking Wireless Sensors - Bhaskar Krishnamachari?, Cambridge press 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

CPLD AND FPGA ARCHITECTURES AND APPLICATIONS**UNIT-I:****Introduction to Programmable Logic Devices:**

Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation.

UNIT-II:**Field Programmable Gate Arrays:**

Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, Applications of FPGAs.

UNIT -III:**SRAM Programmable FPGAs:**

Introduction, Programming Technology, Device Architecture, The Xilinx XC2000, XC3000 and XC4000 Architectures.

UNIT -IV:**Anti-Fuse Programmed FPGAs:**

Introduction, Programming Technology, Device Architecture, The Actel ACT1, ACT2 and ACT3 Architectures.

UNIT -V:**Design Applications:**

General Design Issues, Counter Examples, A Fast Video Controller, A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.

TEXT BOOKS:

1. Field Programmable Gate Array Technology - Stephen M. Trimberger, Springer International Edition.
2. Digital Systems Design - Charles H. Roth Jr, Lizy Kurian John, Cengage Learning.

REFERENCE BOOKS:

1. Field Programmable Gate Arrays - John V. Oldfield, Richard C. Dorf, Wiley India.
2. Digital Design Using Field Programmable Gate Arrays - Pak K. Chan/Samiha Mourad, Pearson Low Price Edition.
3. Digital Systems Design with FPGAs and CPLDs - Ian Grout, Elsevier, Newnes.
4. FPGA based System Design - Wayne Wolf, Prentice Hall Modern Semiconductor Design Series.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

SENSORS AND ACTUATORS

(ELECTIVE –III)

UNIT -I:

Sensors / Transducers: Principles – Classification – Parameters – Characteristics - Environmental Parameters (EP) – Characterization.

Mechanical and Electromechanical Sensors: Introduction – Resistive Potentiometer – Strain Gauge – Resistance Strain Gauge – Semiconductor Strain Gauges -Inductive Sensors: Sensitivity and Linearity of the Sensor –Types-Capacitive Sensors:– Electrostatic Transducer– Force/Stress Sensors Using Quartz Resonators – Ultrasonic Sensors.

UNIT –II:

Thermal Sensors: Introduction – Gas thermometric Sensors – Thermal Expansion Type Thermometric Sensors –Acoustic Temperature Sensor – Dielectric Constant and Refractive Index thermosensors – Helium Low Temperature Thermometer – Nuclear Thermometer – Magnetic Thermometer – Resistance Change Type Thermometric Sensors –Thermoemf Sensors– Junction Semiconductor Types– Thermal Radiation Sensors –Quartz Crystal Thermoelectric Sensors – NQR Thermometry – Spectroscopic Thermometry – Noise Thermometry – Heat Flux Sensors.

Magnetic sensors: Introduction – Sensors and the Principles Behind – Magneto-resistive Sensors – Anisotropic Magnetoresistive Sensing – Semiconductor Magnetoresistors– Hall Effect and Sensors – Inductance and Eddy Current Sensors– Angular/Rotary Movement Transducers – Synchros – Synchro-resolvers - Eddy Current Sensors – Electromagnetic Flowmeter – Switching Magnetic Sensors SQUID Sensors.

UNIT -III:

Radiation Sensors: Introduction – Basic Characteristics – Types of Photosensistors/Photo detectors– X-ray and Nuclear Radiation Sensors– Fiber Optic Sensors.

Electro analytical Sensors: Introduction – The Electrochemical Cell – The Cell Potential - Standard Hydrogen Electrode (SHE) – Liquid Junction and Other Potentials – Polarization – Concentration Polarization– Reference Electrodes - Sensor Electrodes – Electro ceramics in Gas Media.

UNIT -IV:

Smart Sensors: Introduction – Primary Sensors – Excitation – Amplification – Filters – Converters – Compensation– Information Coding/Processing - Data Communication – Standards for Smart Sensor Interface – The Automation.

Sensors –Applications: Introduction – On-board Automobile Sensors (Automotive Sensors)– Home Appliance Sensors – Aerospace Sensors — Sensors for Manufacturing –Sensors for environmental Monitoring.

UNIT -V:

Actuators: Pneumatic and Hydraulic Actuation Systems- Actuation systems – Pneumatic and hydraulic systems - Directional Control valves – Pressure control valves – Cylinders - Servo and proportional control valves – Process control valves – Rotary actuators.

Mechanical Actuation Systems- Types of motion – Kinematic chains – Cams – Gears – Ratchet and pawl – Belt and chain drives – Bearings – Mechanical aspects of motor selection.

Electrical Actuation Systems-Electrical systems -Mechanical switches – Solid-state switches Solenoids – D.C. Motors – A.C. motors – Stepper motors.

TEXT BOOKS:

1. D. Patranabis – “Sensors and Transducers” –PHI Learning Private Limited.
2. W. Bolton – “Mechatronics” –Pearson Education Limited.

REFERENCE BOOK:

1. Sensors and Actuators – D. Patranabis – 2nd Ed., PHI, 2013.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

WIRELESS COMMUNICATIONS AND NETWORKS

(ELECTIVE –III)

UNIT -I:

The Cellular Concept-System Design Fundamentals: Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference, Power Control for Reducing interference, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring.

UNIT –II:

Mobile Radio Propagation: Large-Scale Path Loss: Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, The Three Basic Propagation Mechanisms, Reflection-Reflection from Dielectrics, Brewster Angle, Reflection from perfect conductors, Ground Reflection (Two-Ray) Model, Diffraction-Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- Longley-Ryze Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models- Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT –III:

Mobile Radio Propagation: Small –Scale Fading and Multipath: Small Scale Multipath propagation-Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel-Relationship between Bandwidth and Received power, Small-Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small-Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler spread in Clarke's model, Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT -IV:

Equalization and Diversity: Introduction, Fundamentals of Equalization, Training A Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity Techniques-Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT -V:

Wireless Networks: Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11, IEEE 802.11 Medium Access Control,

Comparison of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, Hiper Lan, WLL.

TEXT BOOKS:

1. Wireless Communications, Principles, Practice – Theodore, S. Rappaport, 2nd Ed., 2002, PHI.
2. Wireless Communications-Andrea Goldsmith, 2005 Cambridge University Press.
3. Mobile Cellular Communication – Gottapu Sasibhushana Rao, Pearson Education, 2012.

REFERENCE BOOKS:

1. Principles of Wireless Networks – Kaveh Pah Laven and P. Krishna Murthy, 2002, PE.
2. Wireless Digital Communications – Kamilo Feher, 1999, PHI.
3. Wireless Communication and Networking – William Stallings, 2003, PHI.
4. Wireless Communication – Upen Dalal, Oxford Univ. Press.
5. Wireless Communications and Networking – Vijay K. Gary, Elsevier.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

NETWORK SECURITY AND CRYPTOGRAPHY

(ELECTIVE – III)

UNIT -I:

Introduction: Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security.

Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Differential and Linear Cryptanalysis, Block Cipher Design Principles and Modes of operations.

UNIT -II:

Encryption Algorithms: Triple DES, International Data Encryption algorithm, Blowfish, RC5, CAST-128, RC2, Characteristics of Advanced Symmetric block ciphers.

Conventional Encryption: Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT -III:

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography.

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT -IV:

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD File, Message digest Algorithm, Secure Hash Algorithm, RIPEMD-160, HMAC.

Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, X.509 directory Authentication service.

Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT –V:

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Combining security Associations, Key Management. **Web Security:** Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. Cryptography and Network Security: Principles and Practice - William Stallings, Pearson Education.
2. Network Security Essentials (Applications and Standards) - William Stallings Pearson Education.

REFERENCE BOOKS:

1. Fundamentals of Network Security - Eric Maiwald (Dreamtech press).

2. Network Security - Private Communication in a Public World - Charlie Kaufman, Radia Perlman and Mike Speciner, Pearson/PHI.
3. Principles of Information Security - Whitman, Thomson.
4. Network Security: The complete reference - Robert Bragg, Mark Rhodes, TMH.
5. Introduction to Cryptography - Buchmann, Springer.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

MULTI MEDIA AND SIGNAL CODING

(ELECTIVE -IV)

UNIT -I:

Introduction to Multimedia: Multimedia, World Wide Web, Overview of Multimedia Tools, Multimedia Authoring, Graphics/ Image Data Types, and File Formats.

Color in Image and Video: Color Science – Image Formation, Camera Systems, Gamma Correction, Color Matching Functions, CIE Chromaticity Diagram, Color Monitor Specifications, Out-of-Gamut Colors, White Point Correction, XYZ to RGB Transform, Transform with Gamma Correction, L*A*B* Color Model. Color Models in Images – RGB Color Model for CRT Displays, Subtractive Color: CMY Color Model, Transformation from RGB to CMY, Under Color Removal: CMYK System, Printer Gamuts, Color Models in Video – Video Color Transforms, YUV Color Model, YIQ Color Model, Ycbcr Color Model.

UNIT -II:

Video Concepts: Types of Video Signals, Analog Video, Digital Video.

Audio Concepts: Digitization of Sound, Quantization and Transmission of Audio.

UNIT -III:

Compression Algorithms:

Lossless Compression Algorithms: Run Length Coding, Variable Length Coding, Arithmetic Coding, Lossless JPEG, Image Compression.

Lossy Image Compression Algorithms: Transform Coding: KLT And DCT Coding, Wavelet Based Coding. Image Compression Standards: JPEG and JPEG2000.

UNIT -IV:

Video Compression Techniques: Introduction to Video Compression, Video Compression Based on Motion Compensation, Search for Motion Vectors, H.261- Intra-Frame and Inter-Frame Coding, Quantization, Encoder and Decoder, Overview of MPEG1 and MPEG2.

UNIT -V:

Audio Compression Techniques: ADPCM in Speech Coding, G.726 ADPCM, Vocoders – Phase Insensitivity, Channel Vocoder, Formant Vocoder, Linear Predictive Coding, CELP, Hybrid Excitation Vocoders, MPEG Audio – MPEG Layers, MPEG Audio Strategy, MPEG Audio Compression Algorithms, MPEG-2 AAC, MPEG-4 Audio.

TEXT BOOKS:

1. Fundamentals of Multimedia – Ze- Nian Li, Mark S. Drew, PHI, 2010.
2. Multimedia Signals & Systems – Mrinal Kr. Mandal Springer International Edition 1st Edition, 2009

REFERENCE BOOKS:

1. Multimedia Communication Systems – Techniques, Stds& Netwroks K.R. Rao, Zorans. Bojkorc, Dragorad A.Milovanovic, 1st Edition, 2002.
2. Fundamentals of Multimedia Ze- Nian Li, Mark S.Drew, Pearson Education (LPE), 1st Edition, 2009.
3. Multimedia Systems John F. Koegel Bufond Pearson Education (LPE), 1st Edition, 2003.
4. Digital Video Processing – A. Murat Tekalp, PHI, 1996.
5. Video Processing and Communications – Yaowang, Jorn Ostermann, Ya-QinZhang, Pearson, 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

**SYSTEM ON CHIP ARCHITECTURE
(ELECTIVE -IV)****UNIT –I:****Introduction to the System Approach:**

System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT –II:**Processors:**

Introduction, Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT –III:**Memory Design for SOC:**

Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation, SOC Memory System, Models of Simple Processor – memory interaction.

UNIT -IV:**Interconnect Customization and Configuration:**

Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses, Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance- Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT –V:**Application Studies / Case Studies:**

SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

TEXT BOOKS:

1. Computer System Design System-on-Chip - Michael J. Flynn and Wayne Luk, Wiley India Pvt. Ltd.
2. ARM System on Chip Architecture – Steve Furber –2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer.
2. Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology) – Jason Andrews – Newnes, BK and CDROM.
3. System on Chip Verification – Methodologies and Techniques –Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.

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M. Tech – I Year – II Sem. Embedded Systems

WIRELESS LANS AND PANS

(ELECTIVE-IV)

UNIT –I:

Wireless System & Random Access Protocols:

Introduction, First and Second Generation Cellular Systems, Cellular Communications from 1G to 3G, Wireless 4G systems, The Wireless Spectrum; Random Access Methods: Pure ALOHA, Slotted ALOHA, Carrier Sense Multiple Access (CSMA), Carrier Sense Multiple Access with Collision Detection (CSMA/CD), Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA).

UNIT –II:

Wireless LANs:

Introduction, importance of Wireless LANs, WLAN Topologies, Transmission Techniques: Wired Networks, Wireless Networks, comparison of wired and Wireless LANs; WLAN Technologies: Infrared technology, UHF narrowband technology, Spread Spectrum technology.

UNIT –III:

The IEEE 802.11 Standard for Wireless LANs:

Network Architecture, Physical layer, The Medium Access Control Layer; MAC Layer issues: Hidden Terminal Problem, Reliability, Collision avoidance, Congestion avoidance, Congestion control, Security, The IEEE 802.11e MAC protocol.

UNIT –IV:

Wireless PANS:

Introduction, importance of Wireless PANS, The Bluetooth technology: history and applications, technical overview, the Bluetooth specifications, piconet synchronization and Bluetooth clocks, Master-Slave Switch; Bluetooth security; Enhancements to Bluetooth: Bluetooth interference issues, Intra and Inter Piconet scheduling, Bridge selection, Traffic Engineering, QoS and Dynamics Slot Assignment, Scatternet formation.

UNIT –V:

The IEEE 802.15 working Group for WPANs:

The IEEE 802.15.3, The IEEE 802.15.4, ZigBee Technology, ZigBee components and network topologies, The IEEE 802.15.4 LR-WPAN Device architecture: Physical Layer, Data Link Layer, The Network Layer, Applications; IEEE 802.15.3a Ultra wideband.

TEXT BOOKS:

1. Ad Hoc and Sensor Networks - Carlos de Moraes Cordeiro and Dharma Prakash Agrawal, World Scientific, 2011.
2. Wireless Communications and Networking - Vijay K.Garg, Morgan Kaufmann Publishers, 2009.

REFERENCE BOOKS:

1. Wireless Networks - Kaveh Pahlaram, Prashant Krishnamurthy, PHI, 2002.
2. Wireless Communication- Marks Ciampor, George Olenewa, Cengage Learning, 2007.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. Embedded Systems

EMBEDDED SYSTEMS LABORATORY

Note:

- A. The following programs are to be implemented on ARM based Processors/Equivalent.
- B. Minimum of 10 programs from Part –I and 6 programs from Part -II are to be conducted.

PART- I:

The following Programs are to be implemented on ARM Processor

1. Simple Assembly Program for
 - a. Addition | Subtraction | Multiplication | Division
 - b. Operating Modes, System Calls and Interrupts
 - c. Loops, Branches
2. Write an Assembly programs to configure and control General Purpose Input/Output (GPIO) port pins.
3. Write an Assembly programs to read digital values from external peripherals and execute them with the Target board.
4. Program for reading and writing of a file.
5. Program to demonstrate Time delay program using built in Timer / Counter feature on IDE environment.
6. Program to demonstrates a simple interrupt handler and setting up a timer.
7. Program demonstrates setting up interrupt handlers. Press button to generate an interrupt and trace the program flow with debug terminal.
8. Program to Interface 8 Bit LED and Switch Interface.
9. Program to implement Buzzer Interface on IDE environment.
10. Program to Displaying a message in a 2 line x 16 Characters LCD display and verify the result in debug terminal.
11. Program to demonstrate I2C Interface on IDE environment.
12. Program to demonstrate I2C Interface – Serial EEPROM.
13. Demonstration of Serial communication. Transmission from Kit and reception from PC using Serial Port on IDE environment use debug terminal to trace the program.
14. Generation of PWM Signal.
15. Program to demonstrate SD-MMC Card Interface.

PART- II:

Write the following programs to understand the use of RTOS with ARM Processor on IDE Environment using ARM Tool chain and Library:

1. Create an application that creates two tasks that wait on a timer whilst the main task loops.
2. Write an application that creates a task which is scheduled when a button is pressed, which illustrates the use of an event set between an ISR and a task.
3. Write an application that Demonstrates the interruptible ISRs(Requires timer to have higher priority than external interrupt button).
4. a).Write an application to Test message queues and memory blocks.

- b). Write an application to Test byte queues.
- 5. Write an application that creates two tasks of the same priority and sets the time slice period to illustrate time slicing.

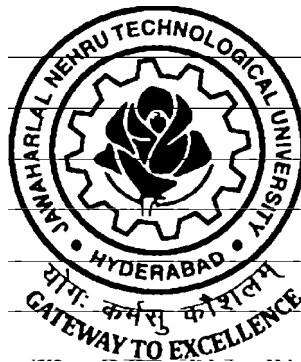
Interfacing Programs:

- 6. Write an application that creates a two task to Blinking two different LEDs at different timings.
- 7. Write an application that creates a two task displaying two different messages in LCD display in two lines.
- 8. Sending messages to mailbox by one task and reading the message from mailbox by another task.
- 9. Sending message to PC through serial port by three different tasks on priority Basis.
- 10. Basic Audio Processing on IDE environment.

**ACADEMIC REGULATIONS
COURSE STRUCTURE
AND
DETAILED SYLLABUS**

**M.TECH
ELECTRICAL POWER SYSTEMS/POWER
ENGINEERING AND ENERGY SYSTEMS/
POWER SYSTEM CONTROL AND
AUTOMATION/ ELECTRICAL POWER
ENGINEERING**

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/ year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - ELECTRICAL POWER SYSTEMS/POWER ENGINEERING AND ENERGY SYSTEMS/ POWER SYSTEM
CONTROL AND AUTOMATION/ ELECTRICAL POWER ENGINEERING

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		Advanced Power System Analysis	3	-	3
		Advanced Power System Protection	3	-	3
		Renewable Energy Systems	3	-	3
		Modern Control Theory	3	-	3
	Elective -I	High Voltage Engineering EHV AC Transmission Microcontrollers and Applications	3	-	3
	Elective -II	Power Quality HVDC Transmission Distribution Automation	3	-	3
	Lab	Power Systems Lab-I	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Power System Dynamics	3	-	3
		Flexible AC Transmission Systems (FACTS)	3	-	3
		Power System Operation and Deregulation	3	-	3
		AI Techniques in Electrical Power Engineering	3	-	3
	Elective -III	Gas Insulated Systems(GIS) Electric Smart Grid Energy Auditing, Conservation and Management	3	-	3
	Elective -IV	Reactive Power Compensation and Management Power System Reliability Voltage Stability	3	-	3
	Lab	Power Systems Lab-II	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva-Voce	-	-	2
		Project Seminar	-	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work Part-II and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

ADVANCED POWER SYSTEM ANALYSIS

UNIT-I:

Admittance Model and Network Calculations, Branch and Node Admittances, Mutually Coupled Branches in Y_{BUS} , An Equivalent Admittance Network, Modification of Y_{BUS} , Network Incidence Matrix and Y_{BUS} , Method of Successive Elimination, Node Elimination, Triangular Factorization, Sparsity and Near Optimal Ordering.

UNIT-II:

Impedance Model and Network Calculations, the BUS Admittance and Impedance Matrices, Thevenin's Theorem and Z_{BUS} , Algorithms for building Z_{BUS} , Modification of existing Z_{BUS} , Calculation of Z_{BUS} elements from Y_{BUS} , Power Invariant Transformations, Mutually Coupled Branches in Z_{BUS} .

UNIT-III:

Gauss Seidel method, N-R Method, Decoupled method, fast decoupled method, comparison between power flow solutions. DC load flow.

UNIT-IV:

Z_{BUS} Method in Contingency Analysis, Adding and Removing Multiple Lines, Piecewise Solution of Interconnected Systems, Analysis of Single Contingencies, Analysis of Multiple Contingencies, Contingency Analysis of DC Model, System Reduction for Contingency and Fault Studies.

UNIT-V:

Fault Analysis: Symmetrical faults-Fault calculations using Z_{BUS} - Fault calculations using Z_{BUS} equivalent circuits-Selection of circuit breakers- Unsymmetrical faults-Problems on various types of faults.

TEXT BOOK:

1. John J.Grainger and W.D. Stevenson," Power System Analysis "- T.M.H.Edition.

REFERENCE:

1. Olle. L.Elgard, "Electrical Energy Systems Theory"-T.M.H.Edition.
2. Power systems stability and control, Prabha Kundur, The Mc Graw – Hill companies.
3. Power System Operation and Control, Dr. K. Uma Rao, Wiley India Pvt. Ltd.
4. Operation and Control in Power Systems, PSR Murthy, Bs Publications.
5. Power System Operation, Robert H. Miller, James H. Malinowski, The Mc Graw – Hill companies.
6. Power Systems Analysis, operation and control by Abhijit Chakrabarti, Sunitha Halder, PHI 3/e , 2010.
7. Modern Power System Analysis – by I.J.Nagrath & D.P.Kothari Tata M Graw – Hill Publishing Company Ltd, 2nd edition.

REFERENCE BOOKS:

1. Power System Analysis and Design by J.Duncan Glover and M.S.Sarma., cengage 3rd Edition.
2. Electric Energy systems Theory – by O.I.Elgerd, Tata Mc Graw-hill Publishing Company Ltd., Second edition.
3. Power System Analysis by Grainger and Stevenson, Tata McGraw Hill.
4. Power System Analysis by C.L.Wadhwa, Newage International-3rd Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

ADVANCED POWER SYSTEM PROTECTION

UNIT-I:

Static Relays : Advantages of static relays-Basic construction of static relays-Level detectors-Replica impedance –Mixing circuits-General equation for two input phase and amplitude comparators-Duality between amplitude and phase comparators.

Amplitude Comparators: Circulating current type and opposed voltage type- rectifier bridge comparators, Direct and Instantaneous comparators.

UNIT-II:

Phase Comparators : Coincidence circuit type- block spike phase comparator, techniques to measure the period of coincidence-Integrating type-Rectifier and Vector product type- Phase comparators.

Static Over Current Relays: Instantaneous over-current relay-Time over-current relays-basic principles – definite time and Inverse definite time over-current relays.

UNIT-III:

Static Differential Relays : Analysis of Static Differential Relays –Static Relay schemes –Duo bias transformer differential protection –Harmonic restraint relay.

Static Distance Relays: Static impedance-reactance–MHO and angle impedance relay-sampling comparator –realization of reactance and MHO relay using sampling comparator.

UNIT-IV:

Multi-input Comparators : Conic section characteristics-Three input amplitude comparator –Hybrid comparator-switched distance schemes –Poly phase distance schemes- phase fault scheme –three phase scheme – combined and ground fault scheme.

Power Swings: Effect of power swings on the performance of distance relays –Power swing analysis- Principle of out of step tripping and blocking relays-effect of line and length and source impedance on distance relays.

UNIT-V:

Microprocessor Based Protective Relays : (Block diagram and flowchart approach only)-Over current relays–impedance relays-directional relay-reactance relay .Generalized mathematical expressions for distance relays-measurement of resistance and reactance –MHO and offset MHO relays-Realization of MHO characteristics- Realization of offset MHO characteristics -Basic principle of Digital computer relaying.

TEXT BOOK:

1. Badri Ram and D.N.Vishwakarma, "Power system protection and Switch gear ", TMH publication New Delhi 1995.

REFERENCE:

1. T.S.Madhava Rao , "Static relays", TMH publication, second edition 1989.
2. Protection and Switchgear, Bhavesh Bhalja, R. P. Mahesheari, Nilesh G. Chothani, Oxford University Press.
3. Electrical Power System Protection, C. Christopoulos and A. Wright, Springer International.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

RENEWABLE ENERGY SYSTEMS

UNIT-I:

Photo voltaic power generation ,spectral distribution of energy in solar radiation, solar cell configurations, voltage developed by solar cell, photo current and load current, practical solar cell performance, commercial photo voltaic systems, test specifications for PV systems, applications of super conducting materials in electrical equipment systems.

UNIT-II:

Principles of MHD power generation, ideal MHD generator performance, practical MHD generator, MHD technology.

Wind Energy conversion: Power from wind, properties of air and wind, types of wind Turbines, operating characteristics.

UNIT-III:

Tides and tidal power stations, modes of operation, tidal project examples, turbines and generators for tidal power generation.

Wave energy conversion: properties of waves and power content, vertex motion of Waves, device applications. Types of ocean thermal energy conversion systems Application of OTEC systems examples.

UNIT-IV:

Miscellaneous energy conversion systems: coal gasification and liquefaction, biomass conversion, geothermal energy, thermo electric energy conversion, principles of EMF generation, description of fuel cells, Co-generation and energy storage, combined cycle co-generation, energy storage.

Global energy position and environmental effects: energy units, global energy position.

UNIT-V:

Types of fuel cells, H_2 - O_2 Fuel cells, Application of fuel cells – Batteries, Description of batteries, Battery application for large power. Environmental effects of energy conversion systems, pollution from coal and preventive measures steam stations and pollution, pollution free energy systems.

TEXT BOOKS:

1. "Energy conversion systems" by Rakosh das Begamudre, New age International publishers, New Delhi - 2000.
2. "Renewable Energy Resources" by John Twidell and Tony Weir, 2nd Edition, Fspan & Co.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

MODERN CONTROL THEORY

UNIT-I:

Mathematical Preliminaries : Fields, Vectors and Vector Spaces – Linear combinations and Bases – Linear Transformations and Matrices – Scalar Product and Norms – Eigen-values, Eigen Vectors and a Canonical form representation of Linear operators – The concept of state – State Equations for Dynamic systems – Time invariance and Linearity – Non-uniqueness of state model – State diagrams for Continuous-Time State models.

UNIT-II:

State Variable Analysis : Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and its properties. General concept of controllability – General concept of Observability – Controllability tests for Continuous-Time Invariant Systems – Observability tests for Continuous-Time Invariant Systems – Controllability and Observability of State Model in Jordan Canonical form – Controllability and Observability Canonical forms of State model.

UNIT-III:

Non Linear Systems : Introduction – Non Linear Systems - Types of Non-Linearities – Saturation – Dead-Zone - Backlash – Jump Phenomenon etc;– Singular Points – Introduction to Linearization of nonlinear systems, Properties of Non-Linear systems – Describing function–describing function analysis of nonlinear systems – Stability analysis of Non-Linear systems through describing functions. Introduction to phase-plane analysis, Method of Isoclines for Constructing Trajectories, singular points, phase-plane analysis of nonlinear control systems.

UNIT-IV:

Stability Analysis : Stability in the sense of Lyapunov, Lyapunov's stability and Lyapunov's instability theorems - Stability Analysis of the Linear continuous time invariant systems by Lyapunov second method – Generation of Lyapunov functions – Variable gradient method – Krasovskii's method. State feedback controller design through Pole Assignment – State observers: Full order and Reduced order.

UNIT-V:

Optimal Control : Introduction to optimal control - Formulation of optimal control problems – calculus of variations – fundamental concepts, functional, variation of functional – fundamental theorem of theorem of Calculus of variations – boundary conditions – constrained minimization – formulation using Hamiltonian method – Linear Quadratic regulator.

TEXT BOOKS:

1. Modern Control System Theory by M.Gopal – New Age International -1984.
2. Modern Control Engineering by Ogata.K – Prentice Hall - 1997.

REFERENCES:

1. Optimal control by Kirck's.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

HIGH VOLTAGE ENGINEERING

(Elective-I)

UNIT- I:

Introduction to High Voltage Engineering : Electric Field Stresses, Gas / Vacuum as Insulator, Liquid Dielectrics, Solids and Composites, Estimation and Control of Electric Stress, Numerical methods for electric field computation, Surge voltages, their distribution and control, Applications of insulating materials in transformers, rotating machines, circuit breakers, cable power capacitors and bushings.

UNIT- II:

Break Down in Dielectric Materials : Gases as insulating media, collision process, Ionization process, Townsend's criteria of breakdown in gases, Paschen's law. Liquid as Insulator, pure and commercial liquids, breakdown in pure and commercial liquids. Intrinsic breakdown, electromechanical breakdown, thermal breakdown, breakdown of solid dielectrics in practice, Breakdown in composite dielectrics, solid dielectrics used in practice.

UNIT-III:

Generation & Measurement of high Voltages & Currents : Generation of High Direct Current Voltages, Generation of High alternating voltages, Generation of Impulse Voltages, Generation of Impulse currents, Tripping and control of impulse generators. Measurement of High Direct Current voltages, Measurement of High Voltages alternating and impulse, Measurement of High Currents-direct, alternating and Impulse, Oscilloscope for impulse voltage and current measurements.

UNIT-IV:

Over Voltages & Insulation Co-ordination : Natural causes for over voltages – Lightning phenomenon, Overvoltage due to switching surges, system faults and other abnormal conditions, Principles of Insulation Coordination on High voltage and Extra High Voltage power systems.

UNIT- V:

Testing Of Materials & Electrical Apparatus : Measurement of D.C Resistivity, Measurement of Dielectric Constant and loss factor, Partial discharge measurements. Testing of Insulators and bushings, Testing of Isolators and circuit breakers, testing of cables, Testing of Transformers, Testing of Surge Arresters, and Radio Interference measurements.

TEXT BOOKS:

1. High Voltage Engineering by M.S.Naidu and V. Kamaraju – TMH Publications, 3rd Edition.
2. High Voltage Engineering: Fundamentals by E.Kuffel, W.S.Zaengl, J.Kuffel by Elsevier, 2nd Edition.

REFERENCE BOOKS:

1. High Voltage Engineering by C.L.Wadhwa, New Age International (P) Limited, 1997.
2. High Voltage Insulation Engineering by Ravindra Arora, Wolfgang Mosch, New Age International (P) Limited, 1995.
3. High Voltage Engineering, Theory and Practice by Mazen Abdel Salam, Hussein Anis, Ahdan El-Morshedy, Roshdy Radwan, Marcel Dekker.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

EHV AC TRANSMISSION

(Elective-I)

UNIT- I:

E.H.V.A.C. Transmission line trends and preliminary aspect standard transmission voltages – Estimation at line and ground parameters-Bundle conductor systems-Inductance and Capacitance of E.H.V. lines – positive, negative and zero sequence impedance – Line Parameters for Modes of Propagation.

UNIT- II:

Electrostatic field and voltage gradients – calculations of electrostatic field of AC lines – effect of high electrostatic field on biological organisms and human beings - surface voltage gradients and maximum gradients of actual transmission lines – voltage gradients on sub conductor.

UNIT- III:

Electrostatic induction in unenergized lines – measurement of field and voltage gradients for three phase single and double circuit lines – un energized lines. Power Frequency Voltage control and over-voltages in EHV lines: No load voltage – charging currents at power frequency-voltage control – shunt and series compensation – static VAR compensation.

UNIT - IV:

Corona in E.H.V. lines – Corona loss formulae- attention of traveling waves due to Corona – Audio noise due to Corona, its generation, characteristic and limits. Measurements of audio noise radio interference due to Corona - properties of radio noise – frequency spectrum of RI fields – Measurements of RI and RIV.

UNIT- V:

Design of EHV lines based on steady state and transient limits - EHV cables and their characteristics.

TEXT BOOKS:

1. EHVAC Transmission Engineering by R. D. Begamudre, New Age International (p) Ltd.
2. HVAC and DC Transmission by S. Rao.

REFERENCE BOOKS :

1. Rokosh Das Begamudre, "Extra High Voltage AC Transmission Engineering" – Wiley Eastern LTD., NEW DELHI – 1987.
2. Edison, "EHV Transmission line"- Electric Institution (GEC 1968).

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

MICROCONTROLLERS AND APPLICATIONS

(Elective-I)

UNIT-I:

Overview Of Architecture & Microcontroller Resources : Architecture of a microcontroller – Microcontroller resources – Resources in advanced and next generation microcontrollers – 8051 microcontroller – Internal and External memories – Counters and Timers – Synchronous serial-cum asynchronous serial communication - Interrupts.

UNIT-II:

8051- Microcontrollers Instruction Set : Basic assembly language programming – Data transfer instructions – Data and Bit-manipulation instructions – Arithmetic instructions – Instructions for Logical operations on the test among the Registers, Internal RAM, and SFRs – Program flow control instructions – Interrupt control flow.

UNIT-III:

Real Time Control Interrupts: Interrupt handling structure of an MCU – Interrupt Latency and Interrupt deadline – Multiple sources of the interrupts – Non-maskable interrupt sources – Enabling or disabling of the sources – Polling to determine the interrupt source and assignment of the priorities among them – Interrupt structure in Intel 8051.

Timers: Programmable Timers in the MCU's – Free running counter and real time control – Interrupt interval and density constraints.

UNIT-IV: SYSTEMS DESIGN

Digital and Analog Interfacing Methods: Switch, Keypad and Keyboard interfacing – LED and Array of LEDs – Keyboard-cum-Display controller (8279) – Alphanumeric Devices – Display Systems and its interfaces – Printer interfaces – Programmable instruments interface using IEEE 488 Bus – Interfacing with the Flash Memory – Interfaces – Interfacing to High Power Devices – Analog input interfacing – Analog output interfacing – Optical motor shaft encoders – Industrial control – Industrial process control system – Prototype MCU based Measuring instruments – Robotics and Embedded control – Digital Signal Processing and digital filters.

UNIT-V:

Real Time Operating System for Microcontrollers: Real Time operating system – RTOS of Keil (RTX51) – Use of RTOS in Design – Software development tools for Microcontrollers.

16-Bit Microcontrollers: Hardware – Memory map in Intel 80196 family MCU system – IO ports – Programmable Timers and High-speed outputs and input captures – Interrupts – instructions. ARM 32 Bit MCUs: Introduction to 16/32 Bit processors – ARM architecture and organization – ARM / Thumb programming model – ARM / Thumb instruction set – Development tools.

TEXT BOOKS:

1. Raj Kamal, "Microcontrollers Architecture, Programming, Interfacing and System Design" – Pearson Education.
2. Mazidi and Mazidi, "The 8051 Microcontroller and Embedded Systems" – PHI, 2000.

REFERENCE BOOKS:

1. A.V. Deshmuk, "Microcontrollers (Theory & Applications)" – WTMH, 2005.
2. John B. Peatman, "Design with PIC Microcontrollers" – Pearson Education, 2005.
3. Microcontroller Programming, Julio Sanchez, Maria P. Canton, CRC Press.
4. The 8051 Microcontroller, Ayala, Cengage Learning.
5. Microprocessors and Microcontrollers, Architecture, Programming and System Design, Krishna Kant, PHI Learning PVT. Ltd.
6. Microprocessors, Nilesh B. Bahadure, PHI Learning PVT. Ltd.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

POWER QUALITY

(Elective – II)

UNIT-I:

Introduction : Introduction of the Power Quality (PQ) problem, Terms used in PQ: Voltage, Sag, Swell, Surges, Harmonics, over voltages, spikes, Voltage fluctuations, Transients, Interruption, overview of power quality phenomenon, Remedies to improve power quality, power quality monitoring.

UNIT-II:

Long & Short Interruptions : Interruptions – Definition – Difference between failures, outage, Interruptions – causes of Long Interruptions – Origin of Interruptions – Limits for the Interruption frequency – Limits for the interruption duration – costs of Interruption – Overview of Reliability evaluation to power quality, comparison of observations and reliability evaluation.

Short interruptions: definition, origin of short interruptions, basic principle, fuse saving, voltage magnitude events due to re-closing, voltage during the interruption, monitoring of short interruptions, difference between medium and low voltage systems. Multiple events, single phase tripping – voltage and current during fault period, voltage and current at post fault period, stochastic prediction of short interruptions.

UNIT III:

1 & 3-Phase Voltage Sag Characterization : Voltage sag – definition, causes of voltage sag, voltage sag magnitude, and monitoring, theoretical calculation of voltage sag magnitude, voltage sag calculation in non-radial systems, meshed systems, and voltage sag duration.

Three phase faults, phase angle jumps, magnitude and phase angle jumps for three phase unbalanced sags, load influence on voltage sags.

UNIT-IV:

Power Quality Considerations in Industrial Power Systems : Voltage sag – equipment behavior of Power electronic loads, induction motors, synchronous motors, computers, consumer electronics, adjustable speed AC drives and its operation. Mitigation of AC Drives, adjustable speed DC drives and its operation, mitigation methods of DC drives.

UNIT-V:

Mitigation of Interruptions & Voltage Sags : Overview of mitigation methods – from fault to trip, reducing the number of faults, reducing the fault clearing time changing the power system, installing mitigation equipment, improving equipment immunity, different events and mitigation methods. System equipment interface – voltage source converter, series voltage controller, shunt controller, combined shunt and series controller.

Power Quality and EMC Standards: Introduction to standardization, IEC Electromagnetic compatibility standards, European voltage characteristics standards, PQ surveys.

REFERENCE BOOKS:

1. "Understanding Power Quality Problems" by Math H J Bollen. IEEE Press.
2. Power Quality VAR Compensation in Power Systems, R. SastryVedam Mulukutla S. Sarma, CRC Press.
3. Power Quality, C. Sankaran, CRC Press.
4. Electrical Power Systems Quality, Roger C. Dugan , Mark F. Mc Granaghan, Surya Santoso, H. Wayne Beaty, Tata McGraw Hill Education Private Ltd.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

HVDC TRANSMISSION

(Elective-II)

UNIT-I:

Introduction : General consideration, Power Handling Capabilities of HVDC Lines Basic Conversion principles, static converter configuration.

UNIT-II:

Static Power Converters : 3-pulse, 6-pulse, and 12-pulse converters, converter station and Terminal equipment, commutation process, Rectifier and inverter operation, equivalent circuit for converter – special features of converter transformers. Harmonics in HVDC Systems, Harmonic elimination, AC and DC filters.

UNIT-III:

Control of HVDC Converters and systems : Constant current, constant extinction angle and constant ignition angle control Individual phase control and equidistant firing angle control DC power flow control. Interaction between HV AC and DC systems – Voltage interaction Harmonic instability problems and DC power modulation.

UNIT-IV:

Mtdc Systems & Over Voltages : Series parallel and series parallel systems their operation and control. Over voltages due to disturbances on DC side, over voltages due to DC and AC side line faults.

UNIT-V:

Converter Faults & Protection : Converter faults, over current protection – valve group, and DC line protection over voltage protection of converters, surge arresters.

REFERENCE BOOKS:

1. E.W. Kimbark: Direct current Transmission, Wiley Inter Science – New York.
2. J. Arillaga HVDC Transmission Peter Peregrinus Ltd. London UK 1983.
3. KR Padiyar : High Voltage Direct current Transmission Wiley Eastern Ltd New Delhi – 1992.
4. E. Uhlman : Power Transmission by Direct Current , Springer Verlag, Berlin Helberg. 1985.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

DISTRIBUTION AUTOMATION

(Elective-II)

UNIT-I:

Distribution Automation and the Utility System : Introduction to Distribution Automation (DA), control system interfaces, control and data requirements, centralized (Vs) decentralized control, DA System (DAS), DA Hardware, DAS software.

UNIT-II:

Distribution Automation Functions : DA capabilities, Automation system computer facilities, management processes, Information management, system reliability management, system efficiency management, voltage management, Load management.

UNIT-III:

Communication Systems for DA : DA communication requirements, Communication reliability, Cost effectiveness, Data rate Requirements, Two way capability, Ability to communicate during outages and faults, Ease of operation and maintenance, Conforming to the architecture of data flow

Communication systems used in DA : Distribution line carrier (Power line carrier), Ripple control, Zero crossing technique, telephone, cable TV, Radio, AM broadcast, FM SCA, VHF Radio, UHF Radio, Microwave satellite. Fiber optics, Hybrid Communication systems, Communication systems used in field tests.

UNIT-IV:

Technical Benefits : DA benefit categories, Capital deferred savings, Operation and Maintenance savings, Interruption related savings, Customer related savings, Operational savings, improved operation, Function benefits, Potential benefits for functions, and function shared benefits, Guidelines for formulation of estimating equations Parameters required, economic impact areas, Resources for determining benefits impact on distribution system, integration of benefits into economic evaluation.

UNIT-V:

Economic Evaluation Methods : Development and evaluation of alternate plans, Select study area, Select study period, Project load growth, Develop Alternatives, Calculate operating and maintenance costs, Evaluate alternatives. Economic comparison of alternate plans, Classification of expenses and capital expenditures, Comparison of revenue requirements of alternative plans, Book Life and Continuing plant analysis, Year by year revenue requirement analysis, short term analysis, end of study adjustment, Break even analysis, Sensitivity analysis computational aids.

REFERENCES:

1. IEEE Tutorial Course "Distribution Automation".
2. IEEE Working Group on "Distribution Automation".
3. Control and Automation of Electrical Distribution Systems, James. Northcote – Green Robert Wilson, CRC Press.
4. Electric Power Distribution Automation, Dr. M. K. Khedkar, Dr. G.M.Dhole, University Science press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – I SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM LAB – I

1. Develop MATLAB program for Y_{BUS} formation.
2. Develop MATLAB program for G-S Load Flow Analysis.
3. Develop MATLAB program for N-R Load Flow Analysis.
4. Develop MATLAB program for FDLF Load Flow Analysis.
5. Develop MATLAB program for Short Circuit Analysis.
6. Transient Stability Analysis for Single Machine connected to Infinite Bus by Point by Point Method.
7. Develop PSPICE Program for Generation System Reliability Analysis.
8. Develop PSPICE Program for Distribution System Reliability Analysis.
9. Simulation of RLC Circuit using PSPICE.
10. Simulation of Single Phase Full Converter with RLE Load using PSPICE
11. Develop MATLAB model for Closed Loop Speed Control of Separately Excited D.C Motor.
12. Develop MATLAB model for Sinusoidal Pulse Width Modulation.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM DYNAMICS

UNIT-I:

Basic Concepts : Power system stability states of operation and system security - system dynamics - problems system model analysis of steady State stability and transient stability - simplified representation of Excitation control.

UNIT-II:

Modeling of Synchronous Machine : Synchronous machine - park's Transformation-analysis of steady state performance per - unit quantities-Equivalent circuits of synchronous machine-determination of parameters of equivalent circuits.

UNIT-III:

Excitation System : Excitation system modeling-excitation systems block Diagram - system representation by state equations- Dynamics of a synchronous generator connected to infinite bus - system model Synchronous machine model-stator equations rotor equations - Synchronous machine model with field circuit - one equivalent damper winding on q axis (model 1.1) - calculation of Initial conditions.

UNIT-IV:

Analysis of Single Machine System : Small signal analysis with block diagram - Representation Characteristic equation and application of Routh Hurwitz criterion- synchronizing and damping torque analysis-small signal model - State equations.

UNIT-V:

Application of Power System Stabilizers : Basic concepts in applying PSS - Control signals - Structure and tuning of PSS - Washout circuit - Dynamic compensator analysis of single machine infinite bus system with and without PSS.

TEXT BOOK:

1. K.R. PADIYAR," Power system dynamics "- B.S. Publications.

REFERENCE BOOKS:

1. P.M. Anderson and A.A. Fouad,"Power system control and stability", IEEE Press.
2. R. Ramanujam, "Power Systems Dynamics"- PHI Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

FLEXIBLE AC TRANSMISSION SYSTEMS

(FACTS)

UNIT-I:

Facts Concepts : Transmission interconnections power flow in an AC system, loading capability limits, Dynamic stability considerations, importance of controllable parameters basic types of FACTS controllers, benefits from FACTS controllers.

UNIT-II:

Voltage Source Converters : Single phase three phase full wave bridge converters transformer connections for 12 pulse 24 and 48 pulse operation. Three level voltage source converter, pulse width modulation converter, basic concept of current source Converters, and comparison of current source converters with voltage source converters.

UNIT-III:

Static Shunt Compensation : Objectives of shunt compensation, mid-point voltage regulation voltage instability prevention, improvement of transient stability, Power oscillation damping, Methods of controllable VAR generation, variable impedance type static VAR generators switching converter type VAR generators hybrid VAR generators.

UNIT-IV:

SVC and Statcom : The regulation and slope transfer function and dynamic performance, transient stability enhancement and power oscillation damping operating point control and summary of compensator control.

UNIT-V:

Static Series Compensators : Concept of series capacitive compensation, improvement of transient stability, power oscillation damping, and functional requirements of GTO thyristor controlled series capacitor (GSC), thyristor switched series capacitor (TSSC), and thyristor controlled series capacitor (TCSC)

Control schemes for GSC TSSC and TCSC.

TEXT BOOK:

1. "Understanding FACTS Devices" N.G. Hingorani and L. Guygi. IEEE Press Publications 2000.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM OPERATION AND DEREGULATION

UNIT-I:

Optimal Power Flow : Introduction- Solution to the optimal power flow-gradient method-Newton's method- Linear sensitivity analysis- Linear programming methods- Security constrained OPF-Interior point algorithm- Bus incremental costs.

UNIT-II:

Power System Security : Introduction –Factors affecting power system security-Contingency analysis- Detection of network problems-Linear sensitivity analysis-AC power flow methods-contingency selection- concentric relaxation-Bounding area method.

UNIT-III:

State Estimation in Power Systems : Introduction- Power system state estimation- Maximum likelihood Weighted Least squares estimation-Matrix formulation- State estimation of AC network- State estimation by orthogonal decomposition- detection and identification of Bad measurements- Estimation of quantities not being measured- Network observability and pseudo measurements.

UNIT-IV:

Power System Deregulation : Introduction- motivation for restructuring of power systems- Electricity market entities model-benefits of deregulation- terminology-deregulation in Indian power sector-Operations in power markets-power pools-transmission networks and electricity markets.

UNIT-V:

Available Transfer Capability : Introduction methods: of determination of ATC - ATC calculation considering the effect of contingency analysis- Transmission open access and pricing-cost components of transmission system- transmission pricing methods-Incremental cost based transmission pricing.

TEXT BOOKS:

1. A.J.Wood & B.F.Woollenberg- John Wiley Power Generation, "Operation and Control"- 2nd edition.
2. P.Venkatesh. B.V.Manikandan, S.Charles Raja- A.Srinivasan, "Electrical power systems: Analysis, security, Deregulation"– PHI 2012.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

AI TECHNIQUES IN ELECTRICAL ENGINEERING

UNIT – I:

Artificial Neural Networks : Introduction-Models of Neural Network - Architectures – Knowledge representation – Artificial Intelligence and Neural networks–Learning process – Error correction learning – Hebbian learning –Competitive learning –Boltzman learning –Supervised learning – Unsupervised learning – Reinforcement learning- learning tasks.

UNIT- II:

ANN Paradigms : Multi – layer perceptron using Back propagation Algorithm-Self – organizing Map – Radial Basis Function Network – Functional link, network – Hopfield Network.

UNIT – III:

Fuzzy Logic : Introduction – Fuzzy versus crisp – Fuzzy sets - Membership function – Basic Fuzzy set operations – Properties of Fuzzy sets – Fuzzy cartesian Product –Operations on Fuzzy relations – Fuzzy logic – Fuzzy Quantifiers-Fuzzy Inference-Fuzzy Rule based system-Defuzzification methods.

UNIT – IV:

Genetic Algorithms : Introduction-Encoding –Fitness Function-Reproduction operators-Genetic Modeling –Genetic operators-Crossover-Single – site crossover-Two point crossover –Multi point crossover-Uniform crossover – Matrix crossover-Crossover Rate-Inversion & Deletion –Mutation operator –Mutation –Mutation Rate-Bit-wise operators-Generational cycle-convergence of Genetic Algorithm.

UNIT-V:

Applications of AI Techniques : Load forecasting – Load flow studies – Economic load dispatch – Load frequency control – Single area system and two area system – Small Signal Stability (Dynamic stability) Reactive power control – speed control of DC and AC Motors.

TEXT BOOK:

1. S.Rajasekaran and G.A.V.Pai, "Neural Networks, Fuzzy Logic & Genetic Algorithms"- PHI, New Delhi, 2003.

REFERENCE BOOKS:

1. P.D.Wasserman, Van Nostrand Reinhold, "Neural Computing Theory & Practice"- New York, 1989.
2. Bart Kosko, "Neural Network & Fuzzy System" Prentice Hall, 1992.
3. G.J.Klir and T.A.Folger, "Fuzzy sets, Uncertainty and Information"-PHI, Pvt.Ltd, 1994.
4. D.E.Goldberg, " Genetic Algorithms"- Addison Wesley 1999.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

GAS INSULATED SYSTEMS (GIS)

(Elective–III)

UNIT–I:

Introduction to GIS and Properties of SF₆ : Characteristics of GIS- Introduction to SF₆ - Physical properties- Chemical properties - Electrical properties-Specification of SF₆ gas for GIS application - Handling of SF₆ gas before use - Safe handling of SF₆ gas in electrical equipment - Equipment for handling the SF₆ Gas - SF₆ and environment.

UNIT–II:

Layout of GIS Stations : Advancement of GIS station - Comparison with Air Insulated Substation - Economics of GIS - User Requirements for GIS - Main Features for GIS - Planning and Installation components of a GIS station.

UNIT–III:

Design And Construction of GIS Station : Introduction - Rating of GIS components - Design Features - Estimation of different types of Electrical Stresses -Design Aspects of GIS components - Insulation Design for Components - Insulation Design for GIS - Thermal Considerations in the Design of GIS - Effect of very Fast Transient Over-voltages (VFTO) on the GIS design - Insulation Coordination systems - Gas handling and Monitoring System Design.

UNIT-IV:

Fast Transient Phenomena in GIS : Introduction- Disconnect Switching in Relation to Very fast Transients- Origin of VFTO-Propagation and Mechanism of VFTO-VFTO Characteristics- Effects of VFTO-Testing of GIS for VFTO.

UNIT–V:

Special Problems in GIS and GIS Diagnostics : Introduction - particles their effects and their control- Insulating Spacers and their Reliability - SF₆ Gas Decomposition - Characteristics of imperfections in insulation - Insulation Diagnostic methods - PD Measurement and UHF Method.

TEXT BOOK:

1. M. S. Naidu, "Gas Insulated Substations"- IK International Publishing House.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

ELECTRIC SMART GRID

(Elective – III)

UNIT–I:

Introduction : Introduction to smart grid- Electricity network-Local energy networks- Electric transportation- Low carbon central generation-Attributes of the smart grid- Alternate views of a smart grid.

Smart Grid to Evolve a Perfect Power System: Introduction- Overview of the perfect power system configurations- Device level power system- Building integrated power systems- Distributed power systems- Fully integrated power system-Nodes of innovation.

UNIT–II:

DC Distribution and Smart Grid : AC vs DC sources-Benefits of and drives of DC power delivery systems- Powering equipment and appliances with DC-Data centers and information technology loads-Future neighborhood-Potential future work and research.

Intelligrid Architecture for the Smartgrid: Introduction- Launching intelligrid-Intelligrid today- Smart grid vision based on the intelligrid architecture-Barriers and enabling technologies.

UNIT–III:

Dynamic Energy Systems Concept : Smart energy efficient end use devices-Smart distributed energy resources-Advanced whole building control systems- Integrated communications architecture-Energy management-Role of technology in demand response- Current limitations to dynamic energy management- Distributed energy resources-Overview of a dynamic energy management-Key characteristics of smart devices- Key characteristics of advanced whole building control systems-Key characteristics of dynamic energy management system.

UNIT–IV:

Energy Port as Part of the Smart Grid: Concept of energy -Port, generic features of the energy port.

Policies and Programs to Encourage End – Use Energy Efficiency: Policies and programs in action - multinational - national-state-city and corporate levels.

Market Implementation: Framework-factors influencing customer acceptance and response - program planning-monitoring and evaluation.

UNIT–V:

Efficient Electric End – Use Technology Alternatives : Existing technologies – lighting - Space conditioning - Indoor air quality - Domestic water heating - hyper efficient appliances - Ductless residential heat pumps and air conditioners - Variable refrigerant flow air conditioning-Heat pump water heating - Hyper efficient residential appliances - Data center energy efficiency- LED street and area lighting - Industrial motors and drives - Equipment retrofit and replacement - Process heating - Cogeneration, Thermal energy storage - Industrial energy management programs - Manufacturing process-Electro-technologies, Residential, Commercial and industrial sectors.

TEXT BOOKS:

1. Clark W Gellings, "The Smart Grid, Enabling Energy Efficiency and Demand Side Response"- CRC Press, 2009.
2. Janaka Ekanayake, Kithsiri Liyanage,Jianzhong.Wu, Akihiko Yokoyama, Nick Jenkins, "Smart Grid: Technology and Applications"- Wiley, 2012.
3. James Momoh, "Smart Grid :Fundamentals of Design and Analysis"- Wiley, IEEE Press, 2012.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

ENERGY AUDITING, CONSERVATION AND MANAGEMENT

(Elective-III)

UNIT-I:

Basic Principles of Energy Audit : Energy audit- definitions, concept , types of audit, energy index, cost index ,pie charts, Sankey diagrams, load profiles, Energy conservation schemes- Energy audit of industries- energy saving potential, energy audit of process industry, thermal power station, building energy audit.

UNIT-II:

Energy Management : Principles of energy management, organizing energy management program, initiating, planning, controlling, promoting, monitoring, reporting- Energy manger, Qualities and functions, language, Questionnaire – check list for top management.

UNIT-III:

Energy Efficient Motors : Energy efficient motors , factors affecting efficiency, loss distribution , constructional details , characteristics - variable speed , variable duty cycle systems, RMS hp- voltage variation-voltage unbalance- over motoring- motor energy audit

UNIT-IV:

Power Factor Improvement, Lighting and Energy Instruments : Power factor – methods of improvement, location of capacitors, Pf with non linear loads, effect of harmonics on power factor, power factor motor controllers - Good lighting system design and practice, lighting control ,lighting energy audit - Energy Instruments- wattmeter, data loggers, thermocouples, pyrometers,lux meters, tongue testers, application of PLC's.

UNIT-V:

Economic Aspects and Analysis : Economics Analysis-Depreciation Methods, time value of money, rate of return , present worth method , replacement analysis, life cycle costing analysis- Energy efficient motors- calculation of simple payback method, net present worth method- Power factor correction, lighting - Applications of life cycle costing analysis, return on investment.

REFERENCE BOOKS:

1. Energy management by W.R. Murphy AND G. McKay Butter worth, Heinemann publications.
2. Energy management by Paul o' Callaghan, Mc-graw Hill Book company-1st edition, 1998.
3. Energy efficient electric motors by John .C. Andreas, Marcel Dekker Inc Ltd-2nd edition, 1995-.
4. Energy management hand book by W.C.Turner, John wiley and sons.
5. Energy management and good lighting practice : fuel efficiency- booklet12-EEO.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

REACTIVE POWER COMPENSATION AND MANAGEMENT

(Elective-IV)

UNIT-I:

Load Compensation : Objectives and specifications – reactive power characteristics – inductive and capacitive approximate biasing – Load compensator as a voltage regulator – phase balancing and power factor correction of unsymmetrical loads- examples.

UNIT-II:

Steady – State Reactive Power Compensation in Transmission System : Uncompensated line – types of compensation – Passive shunt and series and dynamic shunt compensation –examples.

Transient state reactive power compensation in transmission systems : Characteristic time periods – passive shunt compensation – static compensations- series capacitor compensation –compensation using synchronous condensers – examples.

UNIT-III:

Reactive Power Coordination : Objective – Mathematical modeling – Operation planning – transmission benefits – Basic concepts of quality of power supply – disturbances- steady –state variations – effects of under voltages – frequency –Harmonics, radio frequency and electromagnetic interferences.

UNIT-IV:

Demand Side Management : Load patterns – basic methods load shaping – power tariffs- KVAR based tariffs penalties for voltage flickers and Harmonic voltage levels.

Distribution Side Reactive Power Management : System losses –loss reduction methods – examples – Reactive power planning – objectives – Economics Planning capacitor placement – retrofitting of capacitor banks.

UNIT-V:

User Side Reactive Power Management : KVAR requirements for domestic appliances – Purpose of using capacitors – selection of capacitors – deciding factors – types of available capacitor, characteristics and Limitations.

Reactive Power Management in Electric Traction Systems and arc Furnaces : Typical layout of traction systems – reactive power control requirements – distribution transformers- Electric arc furnaces – basic operations- furnaces transformer –filter requirements – remedial measures –power factor of an arc furnace.

REFERENCE BOOKS:

1. Reactive power control in Electric power systems by T.J.E.Miller, John Wiley and sons, 1982 (Units I to IV).
2. Reactive power Management by D.M. Tagare, Tata McGraw Hill, 2004. (Units V to VIII).

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

POWER SYSTEM RELIABILITY

(Elective-IV)

UNIT-I:

Generating System Reliability Analysis – I : Generation system model – capacity outage probability tables – Recursive relation for capacitive model building – sequential addition method – unit removal – Evaluation of loss of load and energy indices – Examples.

UNIT-II:

Generating System Reliability Analysis – II : Frequency and Duration methods – Evaluation of equivalent transitional rates of identical and non-identical units – Evaluation of cumulative probability and cumulative frequency of non-identical generating units – 2- level daily load representation - merging generation and load models – Examples.

UNIT-III:

Operating Reserve Evaluation : Basic concepts - risk indices – PJM methods – security function approach – rapid start and hot reserve units – Modelling using STPM approach.

Bulk Power System Reliability Evaluation: Basic configuration – conditional probability approach – system and load point reliability indices – weather effects on transmission lines – Weighted average rate and Markov model – Common mode failures.

UNIT-IV:

Inter Connected System Reliability Analysis : Probability array method – Two inter connected systems with independent loads – effects of limited and unlimited tie capacity - imperfect tie – Two connected Systems with correlated loads – Expression for cumulative probability and cumulative frequency.

Distribution System Reliability Analysis – I (Radial Configuration): Basic Techniques – Radial networks – Evaluation of Basic reliability indices, performance indices – load point and system reliability indices – customer oriented, loss and energy oriented indices – Examples.

UNIT-V:

Distribution System Reliability Analysis - II (Parallel Configuration) : Basic techniques – inclusion of bus bar failures, scheduled maintenance – temporary and transient failures – weather effects – common mode failures – Evaluation of various indices – Examples.

Substations and Switching Stations: Effects of short-circuits - breaker operation – Open and Short-circuit failures – Active and Passive failures – switching after faults – circuit breaker model – preventive maintenance – exponential maintenance times.

REFERENCE BOOKS:

1. Reliability Evaluation of Power Systems by Roy Billinton and Ronald N. Allan, Plenum press, New York and London (Second Edition), 1996.
2. Reliability Modeling in Electric Power Systems by J. Endrenyi, John Wiley and Sons, 1978. (First Edition).

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

VOLTAGE STABILITY

(Elective – IV)

UNIT-I:

Introduction to Voltage Stability : Definitions: Voltage Stability, Voltage Collapse, Voltage Security; Physical relation indicating dependency of voltage on reactive power flow; Factors affecting Voltage collapse and instability; Previous cases of voltage collapse incidences.

UNIT-II:

Graphical Analysis of Voltage Stability : Comparison of Voltage and angular stability of the system; Graphical Methods describing voltage collapse phenomenon: P-V and Q-V curves; detailed description of voltage collapse phenomenon with the help of Q-V curves.

UNIT-III:

Analysis of Voltage Stability : Analysis of voltage stability on SMLB system: Analytical treatment and analysis.

Voltage Stability Indices: Voltage collapse proximity indicator; Determinant of Jacobin as proximity indicators; Voltage stability margin.

UNIT-IV:

Power System Loads : Loads that influences voltage stability: Discharge lights, Induction Motor, Air-conditioning, heat pumps, electronic power supplies, OH lines and cables.

Reactive Power Compensation: Generation and Absorption of reactive power; Series and Shunt compensation; Synchronous condensers, SVC s; OLTC s; Booster Transformers.

UNIT-V:

Voltage Stability Margin : Stability Margin: Compensated and un-compensated systems.

Voltage Security : Definition; Voltage security; Methods to improve voltage stability and its practical aspects.

TEXT BOOKS:

1. "Performance, operation and control of EHV power transmission system"- A.CHAKRABARTHY, D.P.KOTARI and A.K.MUKOPADYAY, A.H.Wheeler Publishing, I Edition, 1995.
2. "Power System Dynamics: Stability and Control" – K.R.PADIYAR, II Edition, B.S.Publications.

REFERENCE:

1. "Power System Voltage Stability"- C.W.TAYLOR, Mc Graw Hill, 1994.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH – I YEAR – II SEM. (EPE/EPS/PEES/PSC&A)

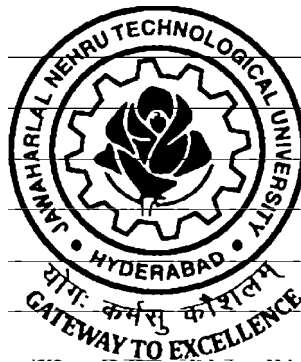
POWER SYSTEM LAB – II

1. Determination of Equivalent circuit of a 3-Winding Transformer.
2. Determination of Sequence Impedances of a Cylindrical Rotor Synchronous Machine.
3. Fault Analysis:
 - i. Single Line to Ground fault (L-G).
 - ii. Line to Line fault (L-L).
 - iii. Double Line to Ground fault (L-L-G).
 - iv. Triple Line to Ground fault (L-L-L-G).
4. Determination of Sub-transient reactance's of a Salient Pole Synchronous Machine.
5. Determination of Sequence Impedances of Three Phase Transformer.
6. Characteristics of Over Current Relays.
 - i. IDMT Electromagnetic Relay (7051 A).
 - ii. Microprocessor based Relay (7051 B)
7. Characteristics of Percentage biased Differential Relay.
 - i. Electromagnetic Relay (7054 A).
 - ii. Static Relay (7054 B).
8. Characteristics of Over Voltage Relay.
 - i. Electromagnetic Relay (7053 A).
 - ii. Microprocessor based Relay (7053 B).
9. Characteristics of Under Voltage (UV) and Negative sequence Relays.
 - i. UV Electromagnetic Relay (7052 A).
 - ii. UV Microprocessor based Relay (7052 B).
 - iii. Static Negative Sequence Relay (7055 B).
10. Performance and Testing of Generator Protection System.
11. Performance and Testing of Transformer Protection System.
12. Performance and Testing of Feeder Protection System.
13. Performance and Testing of Transmission Line Model.
14. Differential protection on Single Phase Transformer.

**ACADEMIC REGULATIONS
COURSE STRUCTURE
AND
DETAILED SYLLABUS**

**M.TECH
POWER ELECTRONICS/POWER AND
INDUSTRIAL DRIVES/POWER
ELECTRONICS AND ELECTRIC DRIVES**

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE**Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards**

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

25. Electronics and Communication Engineering
26. Embedded Systems
27. Embedded Systems and VLSI Design
28. Energy Systems
29. Engineering Design
30. Environmental Engineering
31. Geoinformatics and Surveying Technology
32. Geotechnical Engineering.
33. Heating Ventilation & Air Conditioning.
34. Highway Engineering
35. Image Processing
36. Industrial Engineering and Management
37. Information Technology
38. Infrastructure Engineering
39. Machine Design
40. Mechatronics.
41. Microwave & Radar Engineering
42. Nano Technology
43. Neural Networks
44. Parallel Computing
45. Power and Industrial Drives
46. Power Electronics
47. Power Electronics and Electrical Drives
48. Power Engineering and Energy Systems
49. Power Plant Engineering & Energy Management
50. Power System Control and Automation
51. Power System with Emphasis H.V. Engineering / H.V. Engineering
52. Production Engineering.
53. Real Time Systems
54. Software Engineering
55. Structural Engineering
56. Systems & Signal Processing
57. Thermal Engineering.
58. Transportation Engineering
59. VLSI
60. VLSI and Embedded System/ Electronics Design Technology
61. VLSI Design
62. VLSI System Design
63. Web Technologies
64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - POWER ELECTRONICS/POWER AND INDUSTRIAL DRIVES/
POWER ELECTRONICS AND ELECTRIC DRIVES

COURSE STRUCTURE AND SYLLABUS

I Year I Semester					
Code	Group	Subject	L	P	Credits
		Machine Modeling and Analysis	3	-	3
		Power Electronic Converters-I	3	-	3
		Modern Control Theory	3	-	3
		Power Electronic Control of DC Drives	3	-	3
	Elective -I	HVDC Transmission Operations Research Embedded Systems	3	-	3
	Elective -II	Microcontrollers and Applications Programmable Logic Controllers and their Applications Special Machines	3	-	3
	Lab	Power Converters Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Power Electronic Converters-II	3	-	3
		Power Electronic Control of AC Drives	3	-	3
		Flexible AC Transmission Systems (FACTS)	3	-	3
		Neural Networks and Fuzzy Systems	3	-	3
	Elective -III	Digital Control Systems Power Quality Advanced Digital Signal Processing	3	-	3
	Elective -IV	Dynamics of Electrical Machines High-Frequency Magnetic Components Renewable Energy Systems	3	-	3
	Lab	Electrical Systems Simulation Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	0	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – I Sem. (PE/PEED/PID)****MACHINE MODELLING AND ANALYSIS****UNIT-I:**

Basic Two-pole DC machine - primitive 2-axis machine – Voltage and Current relationship – Torque equation.

UNIT-II:

Mathematical model of separately excited DC motor and DC Series motor in state variable form – Transfer function of the motor - Numerical problems.

Mathematical model of D.C. shunt motor D.C. Compound motor in state variable form – Transfer function of the motor - Numerical Problems

UNIT-III:

Linear transformation – Phase transformation (α, β, γ to α, β, γ) – Active transformation (α, β, γ to d, q).

Circuit model of a 3 phase Induction motor – Linear transformation - Phase Transformation – Transformation to a Reference frame – Two axis models for induction motor.

UNIT-IV:

Voltage and current Equations in stator reference frame – equation in Rotor reference frame – equations in a synchronously rotating frame – Torque equation - Equations in state – space form.

UNIT-V:

Circuit model of a 3ph Synchronous motor – Two axis representation of Syn. Motor.

Voltage and current Equations in state – space variable form – Torque equation.

TEXT BOOKS:

1. Thyristor control of Electric Drives - Vedam Subramanyam.
2. Analysis of electric machinery and Drives systems - Paul C. Krause, Oleg wasynezuk, Scott D. Sudhoff.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

POWER ELECTRONIC CONVERTERS-I

UNIT-I:

Modern Power Semiconductor Devices : Modern power semiconductor devices – MOS turn Off Thyristor (MTO) – Emitter Turn off Thyristor (ETO) – Intergrated Gate-Commutated thyristor (IGCTs) – MOS-controlled thyristors (MCTs) – Static Induction circuit – comparison of their features.

UNIT-II:

1&3-Three-Phase AC Voltage Controllers & Cyclo-converters : Single phase AC voltage controllers: with Resistive, Resistive –inductive and Resistive –inductive-induced EMF loads – AC voltage controllers with PWM Control – Effects of source and load inductances – Synchronous tap changers – Applications – numerical problems.

Three Phase AC Voltage Controllers – Analysis of controllers with star and delta Connected Resistive, Resistive-inductive loads – Effects of source and load Inductances – applications – numerical problems.

Single phase to single phase cyclo-converters – analysis of midpoint and bridge Configurations – Three phase to three phase cyclo-converters – analysis of Midpoint and bridge configurations – Limitations – Advantages – Applications – numerical problems.

UNIT-III:

Single-Phase Converters : Half controlled and fully controlled converters – Evaluation of input power factor and harmonic factor – continuous and Discontinuous load current – single phase dual converters – power factor Improvements – Extinction angle control – symmetrical angle control – PWM – single phase sinusoidal PWM – single phase series converters – Applications – Numerical problems.

UNIT-IV:

Three-Phase Converters : Half controlled and fully controlled converters – Evaluation of input power factor and harmonic factor – continuous and Discontinuous load current – three phase dual converters – power factor Improvements – three-phase PWM – Twelve phase converters – applications – Numerical problems.

UNIT-V:

D.C. to D.C. Converters : Choppers: Analysis of step – down and step-up dc to dc converters with resistive and Resistive –inductive loads – Switched mode regulators – Analysis of Buck Regulators – Boost regulators – buck and boost regulators – Cuk regulators – Condition for Continuous inductor current and capacitor voltage – comparison of regulators – Multi-output boost converters – advantages applications – Numerical problems.

TEXT BOOKS:

1. Power Electronics – Mohammed H. Rashid – Pearson Education Third Edition – First Indian reprint 2004.
2. Power Electronics – Ned Mohan, Tore M. Undeland and William P. Robbins – John Wiley and Sons – Second Edition.
3. Power Electronics Devices, Circuits and Industrial applications, V. R. Moorthi, Oxford University Press

REFERENCE BOOKS:

1. Power Electronics, Dr. P. S. Bimbhra, Khanna Publishers.
2. Elements of Power Electronics, Philip T. Krein, Oxford University Press.
3. Power Electronics, M. S. Jamil Asghar, PHI Private Limited.
4. Principles of Power Electronics John G. Kassakian, Martin F. Schlect, Gerooge C. Verghese, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

MODERN CONTROL THEORY

UNIT-I:

Mathematical Preliminaries : Fields, Vectors and Vector Spaces – Linear combinations and Bases – Linear Transformations and Matrices – Scalar Product and Norms – Eigen-values, Eigen Vectors and a Canonical form representation of Linear operators – The concept of state – State Equations for Dynamic systems – Time invariance and Linearity – Non-uniqueness of state model – State diagrams for Continuous-Time State models.

UNIT-II:

State Variable Analysis : Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and its properties. General concept of controllability – General concept of Observability – Controllability tests for Continuous-Time Invariant Systems – Observability tests for Continuous-Time Invariant Systems – Controllability and Observability of State Model in Jordan Canonical form – Controllability and Observability Canonical forms of State model.

UNIT-III:

Non Linear Systems : Introduction – Non Linear Systems - Types of Non-Linearities – Saturation – Dead-Zone - Backlash – Jump Phenomenon etc;– Singular Points – Introduction to Linearization of nonlinear systems, Properties of Non-Linear systems – Describing function–describing function analysis of nonlinear systems – Stability analysis of Non-Linear systems through describing functions. Introduction to phase-plane analysis, Method of Isoclines for Constructing Trajectories, singular points, phase-plane analysis of nonlinear control systems.

UNIT-IV:

Stability Analysis : Stability in the sense of Lyapunov, Lyapunov's stability and Lyapunov's instability theorems - Stability Analysis of the Linear continuous time invariant systems by Lyapunov second method – Generation of Lyapunov functions – Variable gradient method – Krasovskii's method. State feedback controller design through Pole Assignment – State observers: Full order and Reduced order.

UNIT-V:

Optimal Control : Introduction to optimal control - Formulation of optimal control problems – calculus of variations – fundamental concepts, functional, variation of functional – fundamental theorem of theorem of Calculus of variations – boundary conditions – constrained minimization – formulation using Hamiltonian method – Linear Quadratic regulator.

TEXT BOOKS:

1. Modern Control System Theory by M.Gopal – New Age International -1984.
2. Modern Control Engineering by Ogata.K – Prentice Hall - 1997.

REFERENCE:

1. Optimal control by Kirckcs.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – I Sem. (PE/PEED/PID)****POWER ELECTRONIC CONTROL OF DC DRIVES****UNIT-I:**

Single-Phase Controlled Rectifiers fed DC Motor : Separately excited DC motors with rectified single – phase supply – single-phase semi converter and single phase full converter for continuous and discontinuous modes of operation – power and power factor.

UNIT-II:

Three-Phase Controlled Rectifiers fed DC Motor : Three-phase semi converter and Three phase full converter for continuous and discontinuous modes of operations – power and power factor - Addition of Free wheeling diode – Three phase double converter.

Three phase controlled bridge rectifier with passive load impedance, resistive load and ideal supply – Highly inductive load and ideal supply for load side and supply side quantities, shunt capacitor compensation, three phase controlled bridge rectifier inverter.

UNIT-III:

Phase, Current & Speed Controlled DC Drive : Three-phase controlled converter, control circuit, control modeling of three phase converter – Steady state analysis of three phase converter control DC motor drive – Two quadrant, Three phase converter controlled DC motor drive – DC motor and load, converter.

Current and speed controllers - Current and speed feedback – Design of controllers – Current and speed controllers – Motor equations – filter in the speed feedback loop speed controller – current reference generator – current controller and flow chart for simulation – Harmonics and associated problems – sixth harmonics torque.

UNIT-IV:

Chopper Controlled DC Motor Drives : Principle of operation of the chopper – Four – quadrant chopper circuit – Chopper for inversion – Chopper with other power devices – model of the chopper – input to the chopper – steady state analysis of chopper controlled DC motor drives – rating of the devices – Pulsating torque.

Closed Loop Operation: Speed controlled drive system – current control loop – pulse width modulated current controller – hysteresis current controller – modeling of current controller – design of current controller.

UNIT-V:

Simulation of DC Motor Drives : Dynamic simulations of the speed controlled DC motor drives – Speed feedback speed controller – command current generator – current controller.

REFERENCES:

1. Power Electronics and motor control – Shepherd, Hullely, Liang – II Edition Cambridge University Press.
2. Electronic motor drives modeling Analysis and control – R. Krishnan – I Edition Prentice Hall India.
3. Power Electronics circuits, Devices and Applications – MH Rashid – PHI – 1 Edition 1995.
4. Fundamentals of Electric Drives – GK Dubey Narosa Publishers 1995.
5. Power Semiconductor drives – SB Dewan and A Straughen -1975.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

HVDC TRANSMISSION

(Elective-I)

UNIT-I:

Introduction : General consideration, Power Handling Capabilities of HVDC Lines Basic Conversion principles, static converter configuration.

UNIT-II:

Static Power Converters : 3-pulse, 6-pulse, and 12-pulse converters, converter station and Terminal equipment, commutation process, Rectifier and inverter operation, equivalent circuit for converter – special features of converter transformers. Harmonics in HVDC Systems, Harmonic elimination, AC and DC filters.

UNIT-III:

Control of HVDC Converters and Systems : Constant current, constant extinction angle and constant ignition angle control Individual phase control and equidistant firing angle control DC power flow control. Interaction between HV AC and DC systems – Voltage interaction Harmonic instability problems and DC power modulation.

UNIT-IV:

MTDC Systems & Over Voltages : Series parallel and series parallel systems their operation and control. Over voltages due to disturbances on DC side, over voltages due to DC and AC side line faults.

UNIT-V:

Converter Faults & Protection : Converter faults, over current protection – valve group, and DC line protection over voltage protection of converters, surge arresters.

REFERENCE BOOKS:

1. E.W. Kimbark: Direct current Transmission, Wiley Inter Science – New York.
2. J. Arillaga HVDC Transmission Peter Peregrinus Ltd. London UK 1983.
3. KR Padiyar : High Voltage Direct current Transmission Wiley Eastern Ltd New Delhi – 1992.
4. E. Uhlman : Power Transmission by Direct Current, Springer Verlag, Berlin Helberg. 1985.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

OPERATIONS RESEARCH

(Elective – I)

UNIT-I:

Linear Programming Problem: Formulation – Graphical method - Simplex method – Artificial variable techniques – Big-M tune –phase methods

Duality theorem – Dual simplex method – Sensitivity analysis - effect of changes in cost coefficients, Constraint constants, Addition/Deletion of variables & constraints.

UNIT-II:

Transportation problem – formulation – Initial basic feasible solution methods – Northwest, Least cost & Vogels methods, MODI optimization - Unbalanced & degeneracy treatment. Assignment problem – Formulation – Hungarian method – Variants of assignment problems, Sequencing problems – Flow shop sequencing – $n \text{ jobs} \times 2 \text{ machines}$ sequencing - $n \text{ jobs} \times 3 \text{ machines}$ sequencing – Job-shop sequencing – $2 \text{ jobs} \times m \text{ machines}$ sequencing – Graphical methods.

UNIT-III:

Game Theory - Introduction - Terminology – Saddle point games - with out Saddle point games - 2×2 games, analytical method - $2 \times n$ and $m \times 2$ games – graphical method – dominance principle. Dynamic programming – Bellman's principle of optimality – short route – capital investment – inventory allocation.

UNIT-IV:

Non linear optimization – Single variable optimization problem – Unimodal function - Elimination methods – Fibonacci & Golden reaction methods - Interpolation methods - Quadratic & cubic interpolation method. Multi variable optimization problem – Direct research methods – Univariate method – Pattern search methods – Powell's , Hook-Jeeves & Rosen-brock's search method.

UNIT-V:

Geometric programming – Polynomial – Arithmetic – Seametric inequality – Unconstrained G.P – Constraint G.P with $\leq$ type constraint.

Simulation: Definition – Types- steps- Simulation of simple electrical systems – Advantages and Disadvantages

TEXT BOOKS:

1. Optimization theory & Applications – S.S.Rao, New Age International.
2. Operations Research - S.D.Sharma, Galgotia publishers.
3. Operations Research – Kausur & Kumar, Springer Publishers.

REFERENCES:

1. Optimization techniques: Theory & Practice – M.C.Joshi & K.M. More Ugalya, Narosa Publications.
2. Optimization : Theory & Practice – Beweridze, Mc Graw Hill.
3. Simulation Modelling & Analysis – Law & Kelton –TMH.
4. Optimization Concepts and Applications in Engineering- A.D. Belegundu , J.R. Chandrupata, Pearson Education, Asia.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

EMBEDDED SYSTEMS

(Elective-I)

UNIT- I:

Overview of Embedded System : Embedded System, types of Embedded System, Requirements of Embedded System, and Issues in Embedded software development, Applications.

UNIT-II:**Processor & Memory Organization**

Structural units in a processor, Processor selection, Memory devices, Memory selection, Memory Allocation & Map, Interfacing.

UNIT-III:

Devices, Device Drivers & Buses for Device Networks : I/O devices, Timer & Counter devices, Serial Communication, Communication between devices using different buses. Device drives, Parallel and serial port device drives in a system, Interrupt servicing mechanism, context and periods for context switching, Deadline and Interrupt Latency.

UNIT-IV:

Programming & Modeling Concepts : Program elements, Modeling Processes for Software Analysis, Programming Models, Modeling of Multiprocessor Systems, Software algorithm Concepts, design, implementation, testing, validating, debugging, Management and maintenance, Necessity of RTOS.

UNIT-V:

Hardware and Software Co-Design : Embedded system design and co design issues in software development, design cycle in development phase for Embedded System, Use of ICE & Software tools for development of ES, Issues in embedded system design.

REFERENCE BOOKS:

1. Embedded Systems: Architecture, Programming and Design – Rajkamal, TMH 2003.
2. Programming for Embedded System: DreamTech Software Team-John Wiley -2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

MICROCONTROLLERS AND APPLICATIONS

(Elective-II)

UNIT-I:

Overview of Architecture & Microcontroller Resources : Architecture of a microcontroller – Microcontroller resources – Resources in advanced and next generation microcontrollers – 8051 microcontroller – Internal and External memories – Counters and Timers – Synchronous serial-cum asynchronous serial communication – Interrupts.

UNIT-II:

8051- Microcontrollers Instruction Set : Basic assembly language programming – Data transfer instructions – Data and Bit-manipulation instructions – Arithmetic instructions – Instructions for Logical operations on the test among the Registers, Internal RAM, and SFRs – Program flow control instructions – Interrupt control flow.

UNIT-III:

Real Time Control Interrupts: Interrupt handling structure of an MCU – Interrupt Latency and Interrupt deadline – Multiple sources of the interrupts – Non-maskable interrupt sources – Enabling or disabling of the sources – Polling to determine the interrupt source and assignment of the priorities among them – Interrupt structure in Intel 8051.

Timers : Programmable Timers in the MCU's – Free running counter and real time control – Interrupt interval and density constraints.

UNIT-IV:

Systems Design Digital and Analog Interfacing Methods : Switch, Keypad and Keyboard interfacing – LED and Array of LEDs – Keyboard-cum-Display controller (8279) – Alphanumeric Devices – Display Systems and its interfaces – Printer interfaces – Programmable instruments interface using IEEE 488 Bus – Interfacing with the Flash Memory – Interfaces – Interfacing to High Power Devices – Analog input interfacing – Analog output interfacing – Optical motor shaft encoders – Industrial control – Industrial process control system – Prototype MCU based Measuring instruments – Robotics and Embedded control – Digital Signal Processing and digital filters.

UNIT-V:

Real Time Operating System for Microcontrollers: Real Time operating system – RTOS of Keil (RTX51) – Use of RTOS in Design – Software development tools for Microcontrollers.

16-bit Microcontrollers: Hardware – Memory map in Intel 80196 family MCU system – IO ports – Programmable Timers and High-speed outputs and input captures – Interrupts – instructions. ARM 32 Bit MCUs: Introduction to 16/32 Bit processors – ARM architecture and organization – ARM / Thumb programming model – ARM / Thumb instruction set – Development-tools.

TEXT BOOKS:

1. Raj Kamal, "Microcontrollers Architecture, Programming, Interfacing and System Design" – Pearson Education, 2005.
2. Mazidi and Mazidi, "The 8051 Microcontroller and Embedded Systems" – PHI, 2000.

REFERENCE BOOKS:

1. A.V. Deshmuk, "Microcontrollers (Theory & Applications)" – WTMH, 2005.
2. John B. Peatman, "Design with PIC Microcontrollers" – Pearson Education, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – I Sem. (PE/PEED/PID)****PROGRAMMABLE LOGIC CONTROLLERS AND THEIR APPLICATIONS****(Elective–II)****UNIT-I:**

PLC Basics PLC system, I/O modules and interfacing CPU processor programming equipment programming formats, construction of PLC ladder diagrams, devices connected to I/O modules.

UNIT-II:

PLC Programming input instructions, outputs, operational procedures, programming examples using contacts and coils. Drill-press operation.

Digital logic gates programming in the Boolean algebra system, conversion examples Ladder diagrams for process control Ladder diagrams and sequence listings, ladder diagram construction and flow chart for spray process system.

UNIT-III:

PLC Registers: Characteristics of Registers module addressing holding registers input registers, output registers. PLC Functions Timer functions and industrial applications counters counter function industrial applications, Architecture functions, Number comparison functions, number conversion functions.

UNIT-IV:

Data handling functions: SKIP, Master control Relay Jump Move FIFO, FAL, ONS, CLR and Sweep functions and their applications. Bit Pattern and changing a bit shift register, sequence functions and applications, controlling of two axes and three axis Robots with PLC, Matrix functions.

UNIT-V:

Analog PLC operation: Analog modules and systems Analog signal processing multi bit data processing, analog output application examples, PID principles position indicator with PID control, PID modules, PID tuning, PID functions.

REFERENCE BOOKS:

1. Programmable Logic Controllers – Principle and Applications by John W Webb and Ronald A Reiss Fifth edition, PHI.
2. Programmable Logic Controllers – Programming Method and Applications by JR Hackworth and F.D Hackworth – Jr- Pearson, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

SPECIAL MACHINES

(Elective–II)

UNIT-I:

Special Types of D.C Machines-I : Series booster-Shunt booster-Non-reversible boost-Reversible booster**Special Types of DC Machines –II** : Armature excited machines—Rosenberg generator- The Amplidyne and metadyne— Rototrol and Regulex-third brush generator-three-wire generator-dynamometer.

UNIT–II:

Stepper Motors : Introduction-synchronous inductor (or hybrid stepper motor), Hybrid stepping motor, construction, principles of operation, energization with two phase at a time- essential conditions for the satisfactory operation of a 2-phase hybrid step motor - very slow - speed synchronous motor for servo control-different configurations for switching the phase windings-control circuits for stepping motors-an open-loop controller for a 2-phase stepping motor.

UNIT-III:

Variable Reluctance Stepping Motors : Variable reluctance (VR) Stepping motors, single-stack VR step motors, Multiple stack VR motors-Open-loop control of 3-phase VR step motor-closed-Loop control of step motor, discriminator (or rotor position sensor) transilator, major loop-characteristics of step motor in open-loop drive – comparison between open-loop position control with step motor and a position control servo using a conventional (dc or ac) servo motor- Suitability and areas of application of stepping motors-5- phase hybrid stepping motor - single phase - stepping motor, the construction, operating principle torque developed in the motor.**Switched Reluctance Motor** : Introduction – improvements in the design of conventional reluctance motors-Some distinctive differences between SR and conventional reluctance motors-principle of operation of SRM- Some design aspects of stator and rotor pole arcs, design of stator and rotor and pole arcs in SR motor-determination of $L(\theta)$ - θ profile - power converter for SR motor-A numerical example –Rotor sensing mechanism and logic control, drive and power circuits, position sensing of rotor with Hall problems-derivation of torque expression, general linear case.

UNIT–IV:

Permanent Magnet Materials and Motors : Introduction, Hysteresis loops and recoil line- stator frames (pole and yoke - part) of conventional PM dc Motors, Equivalent circuit of a PM-Development of Electronically commutated dc motor from conventional DC motor.**Brushless DC Motor** : Types of construction – principle of operation of BLDM- sensing and switching logic scheme, sensing logic controller, lockout pulses –drive and power circuits, Base drive circuits, power converter circuit-Theoretical analysis and performance prediction, modeling and magnet circuit d-q analysis of BLDM -transient analysis formulation in terms of flux linkages as state variables-Approximate solution for current and torque under steady state –Theory of BLDM as variable speed synchronous motor (assuming sinusoidal flux distribution)- Methods of reducing Torque Pulsations, 180 degrees pole arc and 120 degree current sheet.

UNIT-V:

Linear Induction Motor : Development of a double sided LIM from rotary type IM- A schematic of LIM drive for electric traction development of one sided LIM with back iron-field analysis of a DSLIM fundamental assumptions.

TEXT BOOKS:

1. K. Venkataratnam, "Special Electrical Machines" - University Press.
2. R.K. Rajput, "Electrical machines"-5th edition.
3. V.V. Athani, "Stepper motor : Fundamentals, Applications and Design"- New Age International Pub.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (PE/PEED/PID)

POWER CONVERTERS LAB

1. Speed Measurement and closed loop control using PMDC motor.
2. Thyristorised drive for PMDC Motor with speed measurement and closed Loop control.
3. IGBT used single 4 quadrant chopper drive for PMDC motor with speed measurement and closed loop control.
4. Thyristorised drive for 1Hp DC motor with closed loop control.
5. 3-Phase input, thyristorised drive, 3 Hp DC motor with closed loop
6. 3-Phase input IGBT, 4 quadrant chopper drive for DC motor with closed Loop control equipment.
7. Cyclo-converter based AC Induction motor control equipment.
8. Speed control of 3 phase wound rotor Induction motor.
9. Single-phase fully controlled converter with inductive load.
10. Single phase half wave controlled converter with inductive load.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER ELECTRONIC CONVERTERS-II**UNIT-I:**

PWM Inverters (Single-Phase & Three-Phase) : Principle of operation – performance parameters – single phase bridge inverter – evaluation of output voltage and current with resistive, inductive and Capacitive loads – Voltage control of single phase inverters – single PWM – Multiple PWM – sinusoidal PWM – modified PWM – phase displacement Control – Advanced modulation techniques for improved performance – Trapezoidal , staircase, stepped, harmonic injection and delta modulations – Advantage – application – numerical problems.

Three phase inverters – analysis of 180 degree condition for output voltage And current with resistive, inductive loads – analysis of 120 degree Conduction – voltage control of three phase inverters – sinusoidal PWM – Third Harmonic PWM – 60 degree PWM – space vector modulation – Comparison of PWM techniques – harmonic reductions – Current Source Inverter – variable DC link inverter – buck and boost inverter – inverter circuit design – advantage applications – numerical problems.

UNIT-II:

Resonant Pulse Inverters : Resonant pulse inverters – series resonant inverters – series resonant inverters with unidirectional switches – series resonant inverters with bidirectional Switches – analysis of half bridge resonant inverter - evaluation of currents and Voltages of a simple resonant inverter – analysis of half bridge and full bridge resonant inverter with bidirectional switches – Frequency response of series resonant inverters – for series loaded inverter – for parallel loaded inverter – For series and parallel loaded inverters – parallel resonant inverters – Voltage control of resonant inverters – class E inverter and Class E rectifier – numerical problems.

Resonant converters:

Resonant converters – Zero current switching resonant converters – L type ZCS resonant converter – M type ZCS resonant converter – zero voltage Switching resonant converters – comparison between ZCS and ZVS resonant Converters – Two quadrant ZVS resonant converters – resonant de-link Inverters – evaluation of L and C for a zero current switching inverter – Numerical problems.

UNIT-III:

Multilevel Inverters : Multilevel concept – Classification of multilevel inverters – Diode clamped multilevel inverter – principle of operation – main features – improved diode Clamped inverter – principle of operation – Flying capacitors multilevel inverter – principle of operation – main features. Cascaded multilevel inverter – principle of operation – main features – Multilevel inverter applications – reactive power compensation – back to back intertie system – adjustable drives – Switching device currents – de link capacitor voltage balancing – features of Multilevel inverters – comparisons of multilevel converters.

UNIT-IV:

DC Power Supplies : DC power supplies – classification – switched mode dc power supplies – fly back Converter – forward converter – push-pull converter – half bridge converter – Full bridge converter – Resonant dc power supplies – bidirectional power supplies – Applications.

UNIT-V:

AC Power Supplies : AC power supplies – classification – switched mode ac power supplies – Resonant AC power supplies – bidirectional ac power supplies – multistage conversions – control circuits – applications. Introduction – power line disturbances – power conditioners – uninterruptible Power supplies – applications.

TEXT BOOKS:

1. Power Electronics – Mohammed H. Rashid – Pearson Education – Third Edition.
2. Power Electronics – Ned Mohan, Tore M. Undeland and William P. Robbins – John Wiley and Sons – Second Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER ELECTRONIC CONTROL OF AC DRIVES**UNIT-I:**

Introduction : Introduction to motor drives – Torque production – Equivalent circuit analysis – Speed – Torque Characteristics with variable voltage operation Variable frequency operation constant v/t operation – Variable stator current operation – Induction motor characteristics in constant torque and field weakening regions.

UNIT-II:

Stator Side Control of Induction Drives : Scalar control – Voltage fed inverter control – Open loop volts/Hz control – speed control slip regulation – speed control with torque and flux control – current controlled voltage fed inverter drive – current – fed inverter control – Independent current and frequency control – Speed and flux control in Current –Fed inverter drive – Volts/Hz control of Current –fed inverter drive – Efficiency optimization control by flux program.

UNIT-III:

Rotor Side Control of Induction Drives : Slip power recovery drives – Static Kramer Drive – Phasor diagram – Torque expression – speed control of Kramer Drive – Static Scheribus Drive – modes of operation.

Vector control of Induction Motor Drives : Principles of Vector control – Vector control methods – Direct methods of vector control – Indirect methods of vector control – Adaptive control principles – Self tuning regulator Model referencing control.

UNIT-IV:

Control of Synchronous Motor Drives : Synchronous motor and its characteristics – Control strategies – Constant torque angle control – Unity power factor control – Constant mutual flux linkage control.

Controllers : Flux weakening operation – Maximum speed – Direct flux weakening algorithm – Constant Torque mode controller – Flux Weakening controller – indirect flux weakening – Maximum permissible torque – speed control scheme – Implementation strategy speed controller design.

UNIT-V:

Variable Reluctance Motor Drive : Variable Reluctance motor drive – Torque production in the variable reluctance motor Drive characteristics and control principles – Current control variable reluctance motor service drive.

Brushless DC Motor Drives : Three phase full wave Brushless dc motor – Sinusoidal type of Brushless dc motor- current controlled Brushless dc motor Servo drive.

REFERENCES:

1. Electric Motor Drives Pearson Modeling, Analysis and control – R. Krishnan – Publications – 1st edition – 2002.
2. Modern Power Electronics and AC Drives B K Bose – Pearson Publications 1st edition.
3. Power Electronics and Control of AC Motors – MD Murthy and FG Turn Bull pergman Press (For Chapters II, III, V) 1st edition.
4. Power Electronics and AC Drives – BK Bose – Prentice Hall Eagle wood diffs New Jersey (for chapters I, II, IV) - 1st edition.
5. Power Electronic circuits Deices and Applications – M H Rashid – PHI – 1995.
6. Fundamentals of Electrical Drives – G. K. Dubey – Narora publications – 1995 (for chapter II).
7. Power Electronics and Variable frequency drives – BK Bose – IEEE Press – Standard publications - 1st edition – 2002.
8. Power Electronics and Motor Drives Advances and Trends, Bimal Bose, Elsevier.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

FLEXIBLE AC TRANSMISSION SYSTEMS

(FACTS)

UNIT-I:

Facts Concepts : Transmission interconnections power flow in an AC system, loading capability limits, Dynamic stability considerations, importance of controllable parameters basic types of FACTS controllers, benefits from FACTS controllers.

UNIT-II:

Voltage Source Converters : Single phase three phase full wave bridge converters transformer connections for 12 pulse 24 and 48 pulse operation. Three level voltage source converter, pulse width modulation converter, basic concept of current source Converters, and comparison of current source converters with voltage source converters.

UNIT-III:

Static Shunt Compensation : Objectives of shunt compensation, mid-point voltage regulation voltage instability prevention, improvement of transient stability, Power oscillation damping, Methods of controllable VAR generation, variable impedance type static VAR generators switching converter type VAR generators hybrid VAR generators.

UNIT-IV:

SVC and STATCOM : The regulation and slope transfer function and dynamic performance, transient stability enhancement and power oscillation damping operating point control and summary of compensator control.

UNIT-V:

Static Series Compensators : Concept of series capacitive compensation, improvement of transient stability, power oscillation damping, and functional requirements of GTO thyristor controlled series capacitor (GSC), thyristor switched series capacitor (TSSC), and thyristor controlled series capacitor (TCSC).

Control schemes for GSC TSSC and TCSC.

TEXT BOOK:

1. "Understanding FACTS Devices" N.G. Hingorani and L. Guygi. IEEE Press Publications 2000.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – II Sem. (PE/PEED/PID)****NEURAL NETWORK AND FUZZY SYSTEMS****UNIT-I:**

Biological neuron Vs artificial neuron, structure and activation functions – Neural network architectures – learning methods, stability and convergence .Single layer networks –Mcculloh–pitts neuron model, Perceptron training and algorithm, delta learning, widrow-Hoff learning rules, limitations, adaline and modification.

UNIT-II:

Multilayer networks, architectures and modeling, BP algorithm, radial basis functions. Unsupervised learning- Winner all learning, out star learning, Counter propagation networks, self organizing networks-Kohonen.

UNIT-III:

Grossberg, Hamming NET, MAXNET, Hopfield networks, recurrent and associative memory, BAM and ART architectures Fuzzy sets and systems – geometry of fuzzy sets – theorems – fuzzy and neural function estimators – FAM system architectures – Uncertainty and estimation – Types of uncertainty.

UNIT-IV:

Measures of Fuzziness – Classical measures of uncertainty – measures of Dissonance – confession specificity – knowledge base defuzzification.

UNIT-V:

Application to load forecasting, load flow, fault detection-unit commitments, LF control – economic dispatch, Neuro-Fuzzy controllers.

TEXTBOOKS:

1. Artificial neural networks – B.Yegna Narayana –phi -1st edition 1999.
2. Neural networks – Simon Haykin – prentice hall international inc.1999.

REFERENCE BOOKS:

1. Neural networks and fuzzy system – Bart Kosko – 2nd edition, 2001.
2. Neural network fundamentals with graphs, algorithms & applications – N.K.Bose and Liang –McGraw Hill, 1996.
3. Fuzzy logic with fuzzy applications – T.J.Rosee-Mcgraw hill 1997.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

DIGITAL CONTROL SYSTEMS

(Elective-III)

UNIT – I:

Introduction : Block Diagram of typical control system- advantages of sampling in control systems – examples of discrete data and digital systems – data conversion and quantization – sample and hold devices – D/A and A/D conversion – sampling theorem – reconstruction of sampled signals – ZOH.

Z-transform: Definition and evaluation of Z-transforms – mapping between s-plane and z-plane – inverse z-plane transform – theorems of the Z-transforms – limitations of z-transforms – pulse transfer function – pulse transfer function of ZOH – relation between $G(s)$ and $G(z)$ – signal flow graph method applied to digital systems.

UNIT- II:

State Space Analysis : State space modeling of digital systems with sample and hold – state transition equation of digital time in variant systems – solution of time in variant discrete state equations by the Z-Transformation – transfer function from the state model – Eigen values – Eigen vector and diagonalisation of the A-matrix – Jordan canonical form. Computation of state transition matrix-Transformation to phase to variable canonical form-The state diagram – decomposition of digital system – Response of sample data system between sampling instants using state approach.

Stability: Definition of stability – stability tests – The second method of Liapunov.

UNIT- III:

Time Domain Analysis : Comparison of time response of continuous data and digital control systems- correlation between time response and root locus in the s-plane and z-plane – effect of pole-zero configuration in the z-plane upon the maximum overshoot and peak time of transient response – Root loci for digital control systems – steady state error analysis of digital control systems – Nyquits plot – Bode plot-G.M and P.M.

UNIT- IV:

Design : The digital control design with digital controller with bilinear transformation – Digital PID controller- Design with deadbeat response-Pole placement through state feedback-Design of full order state observer- Discrete Euler Lagrange Equation – Discrete maximum principle.

UNIT-V:

Digital State Observer : Design of - Full order and reduced order observers. Design by max.principle: Discrete Euler language equation-discrete maximum principle.

TEXT BOOKS:

1. Discrete-Time Control systems - K. Ogata, Pearson Education/PHI, 2nd Edition.
2. Digital Control and State Variable Methods by M.Gopal, TMH.

REFERENCE BOOKS:

1. Digital Control Systems, Kuo, Oxford University Press, 2nd Edition, 2003.
2. Digital Control Engineering, M.Gopal.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

POWER QUALITY

(Elective – III)

UNIT-I:

Introduction : Introduction of the Power Quality (PQ) problem, Terms used in PQ: Voltage, Sag, Swell, Surges, Harmonics, over voltages, spikes, Voltage fluctuations, Transients, Interruption, overview of power quality phenomenon, Remedies to improve power quality, power quality monitoring.

UNIT-II:

Long & Short Interruptions : Interruptions – Definition – Difference between failures, outage, Interruptions – causes of Long Interruptions – Origin of Interruptions – Limits for the Interruption frequency – Limits for the interruption duration – costs of Interruption – Overview of Reliability evaluation to power quality, comparison of observations and reliability evaluation.

Short interruptions: definition, origin of short interruptions, basic principle, fuse saving, voltage magnitude events due to re-closing, voltage during the interruption, monitoring of short interruptions, difference between medium and low voltage systems. Multiple events, single phase tripping – voltage and current during fault period, voltage and current at post fault period, stochastic prediction of short interruptions.

UNIT III:

1 & 3-Phase Voltage SAG Characterization : Voltage sag – definition, causes of voltage sag, voltage sag magnitude, and monitoring, theoretical calculation of voltage sag magnitude, voltage sag calculation in non-radial systems, meshed systems, and voltage sag duration.

Three phase faults, phase angle jumps, magnitude and phase angle jumps for three phase unbalanced sags, load influence on voltage sags.

UNIT-IV:

Power Quality Considerations in Industrial Power Systems : Voltage sag – equipment behavior of Power electronic loads, induction motors, synchronous motors, computers, consumer electronics, adjustable speed AC drives and its operation. Mitigation of AC Drives, adjustable speed DC drives and its operation, mitigation methods of DC drives.

UNIT-V:

Mitigation of Interruptions & Voltage Sags : Overview of mitigation methods – from fault to trip, reducing the number of faults, reducing the fault clearing time changing the power system, installing mitigation equipment, improving equipment immunity, different events and mitigation methods. System equipment interface – voltage source converter, series voltage controller, shunt controller, combined shunt and series controller.

Power Quality and EMC Standards: Introduction to standardization, IEC Electromagnetic compatibility standards, European voltage characteristics standards, PQ surveys.

REFERENCE BOOK:

1. “Understanding Power Quality Problems” by Math H J Bollen. IEEE Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

ADVANCED DIGITAL SIGNAL PROCESSING

(Elective–III)

UNIT–I:

Digital Filter Structures : Block diagram representation – Equivalent Structures – FIR and IIR digital filter Structures All pass Filters-tunable IIR Digital Sine-cosine generator- Computational complexity of digital filter structures.

UNIT–II:

Digital Filter Design : Preliminary considerations- Bilinear transformation method of IIR filter design – design of Low pass high-pass – Band-pass, and Band stop- IIR digital filters – Spectral transformations of IIR filters – FIR filter design –based on Windowed Fourier series – design of FIR digital filters with least – mean square-error – constrained Least –square design of FIR digital filters.

UNIT-III:

DSP Algorithm Implementation : Computation of the discrete Fourier transform- Number representation – Arithmetic operations – handling of overflow – Tunable digital filters – function approximation.

UNIT-IV:

Analysis of Finite Word Length Effects : The Quantization process and errors-Quantization of fixed – point and floating –point Numbers – Analysis of coefficient Quantization effects – Analysis of Arithmetic Round-off errors- Dynamic range scaling – signal –to- noise in Low –order IIR filters- Low –Sensitivity Digital filter – Reduction of Product round-off errors feedback – Limit cycles in IIR digital filter – Round – off errors in FFT Algorithms.

UNIT-V:

Power Spectrum Estimation : Estimation of spectra from Finite Duration Observations signals- Non-parametric methods for power spectrum Estimation- parametric method for power spectrum Estimation- Estimation of spectral form-Finite duration observation of signals- Non-parametric methods for power spectrum estimation – Walsh methods – Blackman and torchy method.

REFERENCE BOOKS:

1. Digital signal processing –sanjit K. Mitra – TMH second edition.
2. Discrete Time Signal Processing – Alan V. Oppenheim, Ronald W, Shafer – PHI 1996 1ST Edition reprint.
3. Digital Signal Processing principles – algorithms and Applications- john G. Proakis – PHI – 3RD edition 2002.
4. Digital Signal Processing – S Salivahanan. A. Vallavaraj C. Gnanapriya – TMH – 2nd reprint 2001.
5. Theory and Applications of Digital Signal Processing –Lourens R Rebinarand Bernold.
6. Digital Filter Analysis and Design Auntoniam – TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

DYNAMICS OF ELECTRICAL MACHINES

(Elective-IV)

UNIT-I:

Basic Machine Theory : Electromechanical Analogy – Magnetic Saturation – Rotating field theory – Operation of Inductor motor – equivalent circuit – Steady state equations of DC machines – operations of synchronous motor – Power angle characteristics

UNIT-II:

Electrodynamical Equation & Their Solutions : Spring and Plunger system - Rotational motion – mutually coupled coils – Lagrange's equation – Application of Lagrange's equation solution of Electro dynamical equations.

UNIT-III:

Dynamics of DC Machines : Separately excited d. c. generators – steady state analysis – transient analysis – Separately excited d. c. motors – steady state analysis – transient analysis – interconnection of machines – Ward Leonard system of speed control.

UNIT-IV:

Induction Machine Dynamics : Induction machine dynamics during starting and braking – accelerating time – induction machine dynamic during normal operation – Equation for dynamical response of the induction motor.

UNIT-V:

Synchronous Machine Dynamics : Electromechanical equation – motor operation – generator operation – small oscillations – general equations for small oscillations – representation of the oscillation equations in state variable form.

REFERENCE BOOKS:

1. Sen Gupta D.P. and J.W “ Electrical Machine Dynamics “Macmillan Press Ltd 1980.
2. Bimbhra P.S. “Generalized Theory of Electrical Machines “ Khanna Publishers 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

HIGH-FREQUENCY MAGNETIC COMPONENTS

(Elective-IV)

UNIT-I:

Fundamentals of Magnetic Devices: Introduction, Magnetic Relationships, Magnetic Circuits, Magnetic Laws, Eddy Currents, Core Saturation, Volt-Second Balance, Inductance, Inductance Factor, Magnetic Energy, Self-Resonant Frequency, Classification of Power Losses in Magnetic Components, Non-inductive Coils.

Magnetic Cores: Introduction, Properties of Core Materials, Magnetic Dipoles, Magnetic Domains, Curie Temperature, Magnetization, Magnetic Materials, Hysteresis, Core Permeability, Core Geometries, Iron Alloy Cores, Amorphous Alloy Cores, Nickel–Iron and Cobalt–Iron Cores, Ferrite Cores, Powder Cores, Nano-crystalline Cores, Superconductors, Hysteresis Core Loss, Eddy-Current Core Loss, Total Core Loss, Complex Permeability.

UNIT-II:

Skin Effect & Proximity Effect: Introduction, Magnet Wire, Wire Insulation, Skin Depth, Ratio of AC-to-DC Winding Resistance, Skin Effect in Long Single Round Conductor, Current Density in Single Round Conductor, Impedance of Round Conductor, Magnetic Field Intensity for Round Wire, Other Methods of Determining the Round Wire Inductance, Power Density in Round Conductor, Skin Effect on Single Rectangular Plate. Proximity and Skin Effects in Two Parallel Plates, Anti-proximity and Skin Effects in Two Parallel Plates, Proximity Effect in Multiple-Layer Inductor, Appendix: Derivation of Proximity Power Loss.

Winding Resistance at High Frequencies: Introduction, Winding Resistance, Square and Round Conductors, Winding Resistance of Rectangular Conductor, Winding Resistance of Square Wire, Winding Resistance of Round Wire, Leakage Inductance, Solution for Round Conductor Winding in Cylindrical Coordinates, Litz Wire, Winding Power Loss for Inductor Current with Harmonics, Effective Winding Resistance for Non-sinusoidal Inductor Current, Thermal Model of Inductors.

UNIT-III:

Transformers: Introduction, Neumann's Formula for Mutual Inductance, Mutual Inductance, Energy Stored in Coupled Inductors, Magnetizing Inductance, Leakage Inductance, Measurement of Transformer Inductances, Stray Capacitance, High-Frequency Transformer Model, Non-interleaved Windings, Interleaved Windings, AC Current Transformers, Winding Power Losses with Harmonics, Thermal Model of Transformers.

Design of Transformers: Introduction, Area Product Method, Optimum Flux Density, Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM, Transformer Design for Fly-back Converter in CCM, Transformer Design for Fly-back Converter in DCM.

UNIT-IV:

Integrated Inductors: Introduction, Resistance of Rectangular Trace, Inductance of Straight Rectangular Trace, Construction of Integrated Inductors, Meander Inductors, Inductance of Straight Round Conductor, Inductance of Circular Round Wire Loop, Inductance of Two-Parallel Wire Loop, Inductance of Rectangle of Round Wire, Inductance of Polygon Round Wire Loop, Bond-wire Inductors, Single-Turn Planar Inductor, Inductance of Planar Square Loop, Planar Spiral Inductors, Multi-metal Spiral Inductors, Planar Transformers, MEMS Inductors, Inductance of Coaxial Cable, Inductance of Two-Wire Transmission Line, Eddy Currents in Integrated Inductors, Model of RF Integrated Inductors, PCB Inductors.

Design of Inductors: Introduction, Restrictions on Inductors, Window Utilization Factor, Temperature Rise of Inductors, Mean Turn Length of Inductors, Area Product Method, AC Inductor Design, Inductor Design for

Buck Converter in CCM, Inductor Design for Buck Converter in DCM method.

UNIT-V:

Self-Capacitance: Introduction, High-Frequency Inductor Model, Self-Capacitance Components, Capacitance of Parallel-Plate Capacitor, Self-Capacitance of Foil Winding Inductors, Capacitance of Two Parallel Round Conductors, Capacitance of Round Conductor and Conducting Plane, Self-Capacitance of Single-Layer Inductors, Self-Capacitance of Multi-layer Inductors, Capacitance of Coaxial Cable.

TEXT BOOK:

1. Design of Magnetic Components for Switched Mode Power Converters, Umanand L., Bhat, S.R., ISBN: 978-81-224-0339-8, Wiley Eastern Publication, 1992.

REFERENCES:

1. High-Frequency Magnetic Components, Marian K. Kazimierczuk, ISBN: 978-0-470-71453-9 John Wiley & Sons, Inc.
2. G.C. Chrysis, High frequency switching power supplies, McGraw Hill, 1989 (2nd Edn.)
3. Eric Lowdon, Practical Transformer Design Handbook, Howard W. Sams & Co., Inc., 1980 .
4. "Thompson — Electrodynamic Magnetic Suspension.pdf"
5. Witulski — "Introduction to modeling of transformers and coupled inductors" Beattie — "Inductance 101.pdf"
6. P. L. Dowell, "Effects of eddy currents in transformer windings.pdf".
7. Dixon — "Eddy current losses in transformer windings.pdf".
8. J J Ding, J S Buckkeridge, "Design Considerations For A Sustainable Hybrid Energy System" IPENZ Transactions, 2000, Vol. 27, No. 1/EMCh.
9. Texas Instruments — "Windings.pdf".
10. Texas Instruments — "Magnetic core characteristics.pdf".
11. Ferroxcube — "3f3 ferrite datasheet.pdf".
12. Ferroxcube — "Ferrite selection guide.pdf".
13. Magnetics, Inc., Ferrite Cores (www.mag-inc.com).

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

RENEWABLE ENERGY SYSTEMS

(Elective – IV)

UNIT-I:

Photo voltaic power generation ,spectral distribution of energy in solar radiation, solar cell configurations, voltage developed by solar cell, photo current and load current, practical solar cell performance, commercial photo voltaic systems, test specifications for PV systems, applications of super conducting materials in electrical equipment systems.

UNIT-II:

Principles of MHD power generation, ideal MHD generator performance, practical MHD generator, MHD technology.

Wind Energy conversion: Power from wind, properties of air and wind, types of wind Turbines, operating characteristics.

UNIT-III:

Tides and tidal power stations, modes of operation, tidal project examples, turbines and generators for tidal power generation.

Wave energy conversion: properties of waves and power content, vertex motion of Waves, device applications. Types of ocean thermal energy conversion systems Application of OTEC systems examples,

UNIT-IV:

Miscellaneous energy conversion systems: coal gasification and liquefaction, biomass conversion, geothermal energy, thermo electric energy conversion, principles of EMF generation, description of fuel cells, Co-generation and energy storage, combined cycle co-generation, energy storage.

Global energy position and environmental effects: energy units, global energy position.

UNIT-V:

Types of fuel cells, H₂-O₂ Fuel cells, Application of fuel cells – Batteries, Description of batteries, Battery application for large power. Environmental effects of energy conversion systems, pollution from coal and preventive measures steam stations and pollution, pollution free energy systems.

TEXT BOOKS:

1. "Energy conversion systems" by Rakosh das Begamudre, New age International publishers, New Delhi - 2000.
2. "Renewable Energy Resources" by John Twidell and Tony Weir, 2nd Edition, Fspan & Co.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (PE/PEED/PID)

ELECTRICAL SYSTEMS SIMULATION LAB

1. Write program and simulate dynamical system of following models:

- a) I/O Model
- b) State variable model

Also identify time domain specifications of each.

2. Obtain frequency response of a given system by using various methods:

- (a) General method of finding the frequency domain specifications.
- (b) Polar plot
- (c) Bode plot

Also obtain the Gain margin and Phase margin.

3. Determine stability of a given dynamical system using following methods.

- a) Root locus
- b) Bode plot
- c) Nyquist plot
- d) Liapunov stability criteria

4. Transform a given dynamical system from I/O model to state variable model and vice versa.

5. Obtain model matrix of a given system, obtain its diagonalize form if exists or obtain Jordan Canonical form of system.

6. Write a program and implement linear quadratic regulator

7. Design a compensator for a given systems for required specifications.

8. Conduct a power flow study on a given power system.

9. Design a PID controller.

10. Conduct a power flow study on a given power system network using Gauss-Seidel iterative method.

11. Develop a program to solve Swing Equation.

12. Develop a Simulink model for a single area load frequency problem and simulate the same.

13. Develop a Simulink model for a two-area load frequency problem and simulate the same.

14. Design a PID controller for two-area power system and simulate the same.

15. PSICE Simulation of Single phase full converter using RL and E loads.

16. PSICE Simulation of Three phase full converter using RL and E loads.

17. PSICE Simulation of Single phase AC Voltage controller using RL load.

18. PSICE Simulation of Three phase inverter with PWM controller.

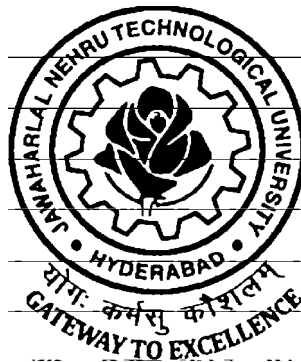
19. PSICE Simulation of resonant pulse commutation circuit.

20. PSICE Simulation of impulse commutation circuit.

ACADEMIC REGULATIONS COURSE STRUCTURE AND DETAILED SYLLABUS

M.TECH SOFTWARE ENGINEERING

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/ year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD.

M.TECH - SOFTWARE ENGINEERING

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		Advanced Data Structures and Algorithms	3	-	3
		Web Technologies and Services	3	-	3
		Software Requirements and Estimation	3	-	3
		Software Development Methodologies	3	-	3
	Elective -I	Secure Software Engineering Cyber Security and Cyber Laws Information Security and Audit	3	-	3
	Elective -II	Parallel Algorithms Advanced Data Mining Object Oriented Modeling	3	-	3
	Lab	Web Technologies and Services Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Software Architecture and Design Patterns	3	-	3
		Software Process and Project Management	3	-	3
		Software Quality Assurance and Testing	3	-	3
		Component Based Software Engineering	3	-	3
	Elective -III	Scripting Languages Information Retrieval Systems Semantic Web and Social Networks	3	-	3
	Elective -IV	Cloud Computing Advanced Databases Business Process Management	3	-	3
	Lab	Software Testing Lab	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	0	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

ADVANCED DATA STRUCTURES AND ALGORITHMS**Objectives:**

- The fundamental design, analysis, and implementation of basic data structures.
- Basic concepts in the specification and analysis of programs.
- Principles for good program design, especially the uses of data abstraction.
- Significance of algorithms in the computer field
- Various aspects of algorithm development
- Qualities of a good solution

UNIT I

Algorithms, Performance analysis- time complexity and space complexity, Asymptotic Notation-Big Oh, Omega and Theta notations, Complexity Analysis Examples.

Data structures-Linear and non linear data structures, ADT concept, Linear List ADT, Array representation, Linked representation, Vector representation, singly linked lists -insertion, deletion, search operations, doubly linked lists-insertion, deletion operations, circular lists.

Representation of single, two dimensional arrays, Sparse matrices and their representation.

UNIT II

Stack and Queue ADTs, array and linked list representations, infix to postfix conversion using stack, implementation of recursion, Circular queue-insertion and deletion, Dequeue ADT, array and linked list representations, Priority queue ADT, implementation using Heaps, Insertion into a Max Heap, Deletion from a Max Heap, java.util package-ArrayList, LinkedList, Vector classes, Stacks and Queues in java.util, Iterators in java.util.

UNIT III

Searching–Linear and binary search methods, Hashing-Hash functions, Collision Resolution methods-Open Addressing, Chaining, Hashing in java.util-HashMap, HashSet, Hashtable.

Sorting –Bubble sort, Insertion sort, Quick sort, Merge sort, Heap sort, Radix sort, comparison of sorting methods.

UNIT IV

Trees- Ordinary and Binary trees terminology, Properties of Binary trees, Binary tree ADT, representations, recursive and non recursive traversals, Java code for traversals, threaded binary trees.

Graphs- Graphs terminology, Graph ADT, representations, graph traversals/search methods-DFS and BFS, Java code for graph traversals, Applications of Graphs-Minimum cost spanning tree using Kruskal's algorithm, Dijkstra's algorithm for Single Source Shortest Path Problem.

UNIT V

Search trees- Binary search tree-Binary search tree ADT ,insertion, deletion and searching operations, Balanced search trees, AVL trees-Definition and examples only, Red Black trees –Definition and examples only, B-Trees-definition, insertion and searching operations, Trees in java.util-TreeSet, TreeMap Classes, Tries(examples only),Comparison of Search trees.

Text compression-Huffman coding and decoding, Pattern matching-KMP algorithm.

TEXT BOOKS:

1. Data structures, Algorithms and Applications in Java, S.Sahni, Universities Press.
2. Data structures and Algorithms in Java, Adam Drozdek, 3rd edition, Cengage learning.
3. Data structures and Algorithm Analysis in Java, M. A. Weiss, 2nd edition, Addison-Wesley (Pearson Education).

REFERENCE BOOKS:

1. Java for Programmers, Deitel and Deitel, Pearson education.
2. Data structures and Algorithms in Java, R.Lafore, Pearson education.
3. Java: The Complete Reference, 8th edition, Herbert Schildt, TMH.
4. Data structures and Algorithms in Java, M. T. Goodrich, R. Tomassia, 3rd edition, Wiley India Edition.
5. Data structures and the Java Collection Frame work, W. J. Collins, Mc Graw Hill.
6. Classic Data structures in Java, T.Budd, Addison-Wesley (Pearson Education).
7. Data structures with Java, Ford and Topp, Pearson Education.
8. Data structures using Java, D.S.Malik and P.S.Nair, Cengage learning.
9. Data structures with Java, J.R.Hubbard and A.Huray, PHI Pvt. Ltd.
10. Data structures and Software Development in an Object-Oriented Domain, J.P.Tremblay and G.A.Cheston, Java edition, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

WEB TECHNOLOGIES AND SERVICES**Objectives:**

The student who has knowledge of programming with java should be able to develop web based solutions using multi-tier architecture. She / He should have good understanding of different technologies on client and server side components as Follows:

Client Side: HTML, CSS, Javascript, Ajax, JQuery and JSON

Server Side: Servlets, JSP

Database: MySQL with Hibernate and Connection Pooling

Framework: Struts with validation framework, Internationalization (I18N)

SOA: Service Oriented Architecture, Web services fundamentals, Axis framework for WS

UNIT I**Client Side Technologies:**

Overview of HTML - Common tags for text formatting, Lists, Tables, Images, Forms, Frames etc., XHTML Cascading Style sheets, linking to HTML Pages, Classes in CSS, General CSS statements for Text, Table, List and Page formatting

Introduction to JavaScripts, variables, arrays, methods and string manipulation, BOM/DOM (Browser/ Document Object Model), accessing elements by ID, Objects in JavaScript

Dynamic HTML with JavaScript and with CSS, form validation with JavaScript, Handling Timer Events, JQuery.

UNIT II:**Introduction to Java Servlets:**

Introduction to Servlets: Lifecycle of a Servlet, Reading request and initialization parameters, Writing output to response, MIME types in response, Session Tracking: Using Cookies and Sessions

Steps involved in Deploying an application.

Database Access with JDBC and Connection Pooling.

Introduction to XML, XML Parsing with DOM and SAX Parsers in Java.

Ajax - Ajax programming with JSP/Servlets, creating XML Http Object for various browsers, Sending request, Processing response data and displaying it.

Introduction to Hibernate.

UNIT III:**Introduction to JSP:**

JSP Application Development: Types of JSP Constructs (Directives, Declarations, Expressions, Code Snippets), Generating Dynamic Content, Exception Handling, Implicit JSP Objects, Conditional Processing, Sharing Data Between JSP pages, Sharing Session and Application Data, Using user defined classes with jsp:useBean tag, Accessing a Database from a JSP.

UNIT IV:**Introduction to Struts Framework:**

Introduction to MVC architecture, Anatomy of a simple struts application, struts configuration file, Presentation

layer with JSP, JSP bean, html and logic tag libraries, Struts Controller class, Using form data in Actions, Page Forwarding, validation frame work, Internationalization

UNIT V:

Service Oriented Architecture and Web Services

Overview of Service Oriented Architecture – SOA concepts, Key Service Characteristics, Technical Benefits of a SOA.

Introduction to Web Services– The definition of web services, basic operational model of web services, basic steps of implementing web services.

Core fundamentals of SOAP – SOAP Message Structure, SOAP encoding, SOAP message exchange models.

Describing Web Services –Web Services life cycle, anatomy of WSDL.

Introduction to Axis– Installing axis web service framework, deploying a java web service on axis.

Web Services Interoperability – Creating java and .Net client applications for an Axis Web Service.

(Note: The Reference Platform for the course will be open source products Apache Tomcat Application Server, MySQL database, Hibernate and Axis).

TEXT BOOKS:

1. Web Programming, building internet applications, Chris Bates 3rd edition, WILEY Dreamtech.
2. The complete Reference Java 7th Edition , Herbert Schildt., TMH.
3. Java Server Pages,Hans Bergsten, SPD, O'Reilly.
4. Professional Jakarta Struts - James Goodwill, Richard Hightower, Wrox Publishers.
5. Developing Java Web Services, R. Nagappan, R. Skoczylas, R.P. Sriganesh, Wiley India, rp – 2008.
6. Understanding SOA with Web Services, Eric Newcomer and Greg Lomow, Pearson Edition – 2009
7. Java Web Service Architecture, James McGovern, Sameer Tyagi et al., Elsevier - 2009

REFERENCE BOOKS:

1. Programming the world wide web,4th edition,R.W.Sebesta,Pearson
2. Core SERVLETS ANDJAVASERVER PAGES VOLUME 1: CORE
3. TECHNOLOGIES , Marty Hall and Larry Brown Pearson
4. Internet and World Wide Web – How to program , Dietel and Nieto PHI/Pearson.
5. Jakarta Struts Cookbook , Bill Siggelkow, S P D O'Reilly.
6. Professional Java Server Programming,S.Allamaraju & othersApress(dreamtech).
7. Java Server Programming ,Ivan Bayross and others,The X Team,SPD
8. Web Warrior Guide to Web Programmimg-Bai/Ekedaw-Cengage Learning.
9. Beginning Web Programming-Jon Duckett ,WROX.
10. Java Server Pages, Pekowsky, Pearson.
11. Java Script,D.Flanagan,O'Reilly,SPD.
12. Building Web Services with Java, 2nd Edition, S. Graham and others, Pearson Edn., 2008.
13. Java Web Services, D.A. Chappell & T. Jewell, O'Reilly,SPD.
14. McGovern, et al., Java Web Services Architecture, Morgan Kaufmann Publishers,2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

SOFTWARE REQUIREMENTS AND ESTIMATION**Objectives:**

- Students will demonstrate knowledge of the distinction between critical and non- critical systems.
- Students will demonstrate the ability to manage a project including planning, scheduling and risk assessment/management.
- Students will author a software requirements document.
- Students will demonstrate an understanding of the proper contents of a software requirements document.
- Students will author a formal specification for a software system.
- Students will demonstrate an understanding of distributed system architectures and application architectures.
- Students will demonstrate an understanding of the differences between real-time and non-real time systems.
- Students will demonstrate proficiency in rapid software development techniques.
- Students will demonstrate proficiency in software development cost estimation
- Students will author a software testing plan.

UNIT I**Software Requirements: What and Why**

Essential Software requirement, Good practices for requirements engineering, Improving requirements processes, Software requirements and risk management.

Software Requirements Engineering

Requirements elicitation, requirements analysis documentation, review, elicitation techniques, analysis models, Software quality attributes, risk reduction through prototyping, setting requirements priorities, verifying requirements quality.

UNIT II**Software Requirements Management**

Requirements management Principles and practices, Requirements attributes, Change Management Process, Requirements Traceability Matrix, Links in requirements chain.

Software Requirements Modeling

Use Case Modeling, Analysis Models, Dataflow diagram, state transition diagram, class diagrams, Object analysis, Problem Frames.

UNIT III**Software Estimation**

Components of Software Estimations, Estimation methods, Problems associated with estimation, Key project factors that influence estimation.

Size Estimation

Two views of sizing, Function Point Analysis, Mark II FPA, Full Function Points, LOC Estimation, Conversion between size measures.

UNIT IV**Effort, Schedule and Cost Estimation**

What is Productivity? Estimation Factors, Approaches to Effort and Schedule Estimation, COCOMO II, Putnam Estimation Model, Algorithmic models, Cost Estimation.

UNIT V**Tools for Requirements Management and Estimation**

Requirements Management Tools: Benefits of using a requirements management tool, commercial requirements management tool, Rational Requisite pro, Caliber – RM, implementing requirements management automation.

Software Estimation Tools:

Desirable features in software estimation tools, IFPUG, USC's COCOMO II, SLIM (Software Life Cycle Management) Tools.

TEXT BOOK:

1. Software Requirements and Estimation by *Rajesh Naik and Swapna Kishore*, Tata Mc Graw Hill.

REFERENCE BOOKS:

1. Software Requirements by Karl E. Weigers, Microsoft Press.
2. Managing Software Requirements, Dean Leffingwell & Don Widrig, Pearson Education, 2003.
3. Mastering the requirements process, second edition, Suzanne Robertson & James Robertson, Pearson Education, 2006.
4. Estimating Software Costs, Second edition, Capers Jones, TMH, 2007.
5. Practical Software Estimation, M.A. Parthasarathy, Pearson Education, 2007.
6. Measuring the software process, William A. Florac & Anita D. Carleton, Pearson Education, 1999.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

SOFTWARE DEVELOPMENT METHODOLOGIES

Objectives:

Your studies will enable you to develop:

- a broad and critical understanding of all the processes for engineering high quality software and the principles, concepts and techniques associated with software development.
- an ability to analyze and evaluate problems and draw on the theoretical and technical knowledge to develop solutions and systems.
- a range of skills focused on the analysis of requirements, design and implementation of reliable and maintainable software, with strong emphasis on engineering principles applied over the whole development lifecycle.
- an awareness of current research in software development, the analytical skills and research techniques for their critical and independent evaluation and their application to new problems.

UNIT I

Introduction to Software Engineering: The evolving role of software, Changing Nature of Software, legacy software, Software myths.

A Generic view of process: Software engineering - A layered technology, a process framework, The Capability Maturity Model Integration (CMMI), Process patterns, process assessment, personal and team process models.

Process models: The waterfall model, Incremental process models, Evolutionary process models, specialized process models, The Unified process.

UNIT II

Software Requirements: Functional and non-functional requirements, User requirements, System requirements, Interface specification, the software requirements document.

Requirements engineering process: Feasibility studies, Requirements elicitation and analysis, Requirements validation, Requirements management.

System models: Context Models, Behavioral models, Data models, Object models, structured methods.

UNIT III

Design Engineering: Design process and Design quality, Design concepts, the design model, pattern based software design.

Creating an architectural design: software architecture, Data design, Architectural styles and patterns, Architectural Design, assessing alternative architectural designs, mapping data flow into a software architecture.

Modeling component-level design: Designing class-based components, conducting component-level design, Object constraint language, designing conventional components.

Performing User interface design: Golden rules, User interface analysis and design, interface analysis, interface design steps, Design evaluation.

UNIT IV

Testing Strategies: A strategic approach to software testing, test strategies for conventional software, Black-Box and White-Box testing, Validation testing, System testing, the art of Debugging.

Product metrics: Software Quality, Frame work for Product metrics, Metrics for Analysis Model, Metrics for Design Model, Metrics for source code, Metrics for testing, Metrics for maintenance.

Metrics for Process and Products: Software Measurement, Metrics for software quality.

UNIT V

Risk management: Reactive vs Proactive Risk strategies, software risks, Risk identification, Risk projection, Risk refinement, RMMM, RMMM Plan.

Quality Management: Quality concepts, Software quality assurance, Software Reviews, Formal technical reviews, Statistical Software quality Assurance, Software reliability, The ISO 9000 quality standards.

Configuration Management: Configuration Management planning, Change management, Version and release management, System building, CASE tools for configuration management.

TEXT BOOKS:

1. Software Engineering: A practitioner's Approach, Roger S Pressman, sixth edition. McGraw Hill International Edition, 2005
2. Software Engineering, Ian Sommerville, seventh edition, Pearson education, 2004.

REFERENCE BOOKS:

1. Software Engineering, A Precise Approach, Pankaj Jalote, Wiley India, 2010.
2. Software Engineering : A Primer, Waman S Jawadekar, Tata McGraw-Hill, 2008
3. Fundamentals of Software Engineering, Rajib Mall, PHI, 2005
4. Software Engineering, Principles and Practices, Deepak Jain, Oxford University Press.
5. Software Engineering1: Abstraction and modeling, Diner Bjorner, Springer International edition, 2006.
6. Software Engineering2: Specification of systems and languages, Diner Bjorner, Springer International edition, 2006.
7. Software Engineering Foundations, Yingxu Wang, Auerbach Publications, 2008.
8. Software Engineering 3: Domains, Requirements and Software Design, D.Bjorner, Springer, International Edition.
9. Software Engineering Principles and Practice, Hans Van Vliet, 3rd edition, Wiley India edition.
10. Introduction to Software Engineering, R.J.Leach, CRC Press.
11. Software Engineering Fundamentals, Ali Behforooz and Frederick J.Hudson, Oxford University Press, 2009.
12. Software Engineering Handbook, Jessica Keyes, Auerbach, 2003.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

**SECURE SOFTWARE ENGINEERING
(ELECTIVE-I)****Objectives:**

- Students will demonstrate knowledge of the distinction between critical and non-critical systems.
- Students will demonstrate the ability to manage a project including planning, scheduling and risk assessment/management.
- Students will author a software requirements document.
- Students will demonstrate an understanding of the proper contents of a software requirements document.
- Students will author a formal specification for a software system.
- Students will demonstrate an understanding of distributed system architectures and application architectures.
- Students will demonstrate an understanding of the differences between real-time and non-real time systems.
- Students will demonstrate proficiency in rapid software development techniques.
- Students will be able to identify specific components of a software design that can be targeted for reuse.
- Students will demonstrate proficiency in software development cost estimation.
- Students will author a software testing plan.

UNIT – I**Security a software Issue:**

Introduction, the problem, Software Assurance and Software Security, Threats to software security, Sources of software insecurity, Benefits of Detecting Software Security

What Makes Software Secure:

Properties of Secure Software, Influencing the security properties of software, Asserting and specifying the desired security properties?

UNIT – II**Requirements Engineering for secure software:**

Introduction, the SQUARE process Model, Requirements elicitation and prioritization

UNIT – III**Secure Software Architecture and Design:**

Introduction, software security practices for architecture and design: architectural risk analysis, software security knowledge for architecture and design: security principles, security guidelines and attack patterns

Secure coding and Testing:

Code analysis, Software Security testing, Security testing considerations throughout the SDLC

UNIT – IV**Security and Complexity:**

System Assembly Challenges: introduction, security failures, functional and attacker perspectives for security

analysis, system complexity drivers and security

UNIT – V**Governance and Managing for More Secure Software:**

Governance and security, Adopting an enterprise software security framework, How much security is enough?, Security and project management, Maturity of Practice

TEXT BOOK:

1. Software Security Engineering: Julia H. Allen, Pearson Education

REFERENCE BOOKS:

1. Developing Secure Software: Jason Grembi, Cengage Learning
2. Software Security : Richard Sinn, Cengage Learning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

**CYBER SECURITY AND CYBER LAWS
(ELECTIVE-I)****Objectives:**

- To learn Internet, E-commerce and E-governance with reference to Free Market Economy
- To learn International Efforts relating to Cyberspace laws and Cyber crimes
- To learn Law relating to electronic records and intellectual property rights in India
- To learn Penalties, Compensation and Offences under the Cyberspace and Internet in India
- To learn Miscellaneous provisions of IT Act and Conclusions

UNIT-I**Internet, E-commerce and E-governance with reference to Free Market Economy**

Understanding Computers, Internet and Cyber laws, Conceptual Framework of E-commerce: E-governance, the role of Electronic Signatures in E-commerce with Reference to Free Market Economy in India.

UNIT-II**Law relating to electronic records and intellectual property rights in India**

Legal aspects of Electronic records / Digital signatures, The roles and regulations of Certifying Authorities in India, Protection of Intellectual Property Rights in Cyberspace in India.

UNIT-III**International Efforts relating to Cyberspace laws and Cyber crimes**

International efforts related to Cyber laws, Council of Europe (COE) convention on Cyber Crimes.

UNIT-IV**Penalties, Compensation and Offences under the Cyberspace and Internet in India**

Penalties, Compensation and Adjudication of violations of provisions of IT Act and Judicial review, Some important offences under the Cyberspace law and the Internet in India, Other offences under the Information Technology Act in India.

UNIT-V: Miscellaneous provisions of IT Act and Conclusions

The role of Electronic Evidence and miscellaneous provisions of the IT Act.

TEXT BOOK:

1. Cyber Laws and IT Protection, Harish Chander, PHI, 2012

REFERENCE BOOKS:

1. Cyberspace and Cybersecurity, George Kostopoulos, Auerbach Publications, 2012.
2. Cyber Forensics: A Field Manual for Collecting, Examining, and Preserving Evidence of Computer Crimes, Second Edition, Albert Marcella, Jr., Doug Menendez, Auerbach Publications, 2007.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

**INFORMATION SECURITY AND AUDIT
(ELECTIVE- I)****Objectives:**

To introduce the fundamental concepts and techniques in computer and network security, giving students an overview of information security and auditing, and to expose students to the latest trend of computer attack and defense. Other advanced topics on information security such as mobile computing security, security and privacy of cloud computing, as well as secure information system development will also be discussed.

UNIT I

A model for Internetwork security, Conventional Encryption Principles & Algorithms (DES, AES, RC4, Blowfish), Block Cipher Modes of Operation, Location of Encryption Devices, Key Distribution.

Public key cryptography principles, public key cryptography algorithms (RSA, Diffie-Hellman, ECC), public Key Distribution.

UNIT II

Approaches of Message Authentication, Secure Hash Functions (SHA-512, MD5) and HMAC, Digital Signatures, Kerberos, X.509 Directory Authentication Service,

Email Security: Pretty Good Privacy (PGP)

IP Security: Overview, IP Security Architecture, Authentication Header, Encapsulating Security Payload, Combining Security Associations and Key Management.

UNIT III

Web Security: Requirements, Secure Socket Layer (SSL) and Transport Layer Security (TLS), Secure Electronic Transaction (SET).

Firewalls: Firewall Design principles, Trusted Systems, Intrusion Detection Systems

UNIT IV

Auditing For Security:

Introduction, Basic Terms Related to Audits, Security audits, The Need for Security Audits in Organization, Organizational Roles and Responsibilities for Security Audit, Auditors Responsibility In Security Audits, Types Of Security Audits.

UNIT V**Auditing For Security:**

Approaches to Audits, Technology Based Audits Vulnerability Scanning And Penetration Testing, Resistance to Security Audits, Phase in security audit, Security audit Engagement Costs and other aspects, Budgeting for security audits, Selecting external Security Consultants, Key Success factors for security audits.

TEXT BOOKS:

1. Cryptography and Network Security by William Stallings, Fourth Edition, Pearson Education 2007.
2. Network Security Essentials (Applications and Standards) by William Stallings Pearson Education, 2008.
3. Cryptography & Network Security by Behrouz A. Forouzan, TMH 2007.
4. Information Systems Security by Nina Godbole, WILEY 2008.

REFERENCE BOOKS:

1. Information Security by Mark Stamp, Wiley – INDIA, 2006.
2. Fundamentals of Computer Security, Springer.
3. Network Security: The complete reference, Robert Bragg, Mark Rhodes, TMH
4. Computer Security Basics by Rick Lehtinen, Deborah Russell & G.T.Gangemi Sr., SPD O'REILLY 2006.
5. Modern Cryptography by Wenbo Mao, Pearson Education 2007.
6. Principles of Information Security, Whitman, Thomson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

**PARALLEL ALGORITHMS
(ELECTIVE-II)****Objectives:**

- To understand the role of computation models in parallel computation;
- To understand the circuit and comparison network models;
- To design Parallel Matrix Transportation and Multiplication Algorithm
- To understand the PRAM and BSP models and their theoretical foundations;

UNIT-I:

Sequential model, need of alternative model, parallel computational models such as PRAM, LMCC, Hypercube, Cube Connected Cycle, Butterfly, Perfect Shuffle Computers, Tree model, Pyramid model, Fully Connected model, PRAM-CREW, EREW models, simulation of one model from another one.

UNIT-II:

Performance Measures of Parallel Algorithms, speed-up and efficiency of PA, Cost optimality, Example to illustrate Cost-optimal algorithms- such as summation, Min/Max on various models.

UNIT-III:

Parallel Sorting Networks, Parallel Merging Algorithms on CREW/EREW/MCC/, Parallel Sorting Networks on CREW/EREW/MCC/, linear array

UNIT-IV:

Parallel Searching Algorithm, Kth element, Kth element in X+Y on PRAM, Parallel Matrix Transportation and Multiplication Algorithm on PRAM, MCC, Vector-Matrix Multiplication, Solution of Linear Equation, Root finding.

UNIT-V:

Graph Algorithms - Connected Graphs, search and traversal, Combinatorial Algorithms- Permutation, Combinations, Derangements.

TEXT BOOK:

1. M.J. Quinn, "Designing Efficient Algorithms for Parallel Computer" by Mc Graw Hill.

REFERENCE BOOKS:

1. Algorithms, K.A.Berman and J.L.Paul, Cengage Learning.
2. Distributed Algorithms, N.A.Lynch, Morgan Kaufmann Publishers, Elsevier.
3. Parallel Algorithms, Henri Casanova, A.Legrand, Y.Robert, Chapman &Hall/CRC, Taylor and Francis Group.
4. Handbook of Parallel Computing, S.Rajasekaran, John Reif, Chapman & Hall/CRC, Taylor and Francis Group.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

**ADVANCED DATA MINING
(ELECTIVE-II)****Objectives:**

- To develop the abilities of critical analysis to data mining systems and applications.
- To implement practical and theoretical understanding of the technologies for data mining
- To understand the strengths and limitations of various data mining models;

UNIT-I**Data mining Overview and Advanced Pattern Mining**

Data mining tasks – mining frequent patterns, associations and correlations, classification and regression for predictive analysis, cluster analysis, outlier analysis; advanced pattern mining in multilevel, multidimensional space – mining multilevel associations, mining multidimensional associations, mining quantitative association rules, mining rare patterns and negative patterns.

UNIT-II**Advance Classification**

Classification by back propagation, support vector machines, classification using frequent patterns, other classification methods – genetic algorithms, roughset approach, fuzzy set approach;

UNIT-III**Advance Clustering**

Density - based methods – DBSCAN, OPTICS, DENCLUE; Grid-Based methods – STING, CLIQUE; Exception – maximization algorithm; clustering High- Dimensional Data; Clustering Graph and Network Data.

UNIT-IV**Web and Text Mining**

Introduction, web mining, web content mining, web structure mining, web usage mining, Text mining – unstructured text, episode rule discovery for texts, hierarchy of categories, text clustering.

UNIT-V**Temporal and Spatial Data Mining**

Introduction; Temporal Data Mining – Temporal Association Rules, Sequence Mining, GSP algorithm, SPADE, SPIRIT Episode Discovery, Time Series Analysis, Spatial Mining – Spatial Mining Tasks, Spatial Clustering. Data Mining Applications.

TEXT BOOKS:

1. Data Mining Concepts and Techniques, Jiawei Han, Micheline Kamber, Jian pei, Morgan Kaufmann.
2. Data Mining Techniques – Arun K. Pujari, Universities Press.

REFERENCE BOOKS:

1. Introduction to Data Mining – Pang-Ning Tan, Vipin kumar, Michael Steinbach, Pearson.
2. Data Mining Principles & Applications – T.V Suresh Kumar, B.Esware Reddy, Jagadish S Kalimani, Elsevier.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

**OBJECT ORIENTED MODELING
(ELECTIVE-II)****Objectives:**

- Concisely define the following key terms: class, object, state, behavior, object class, class diagram, object diagram, operation, encapsulation, constructor operation, query operation, update operation, scope operation, association, association role, multiplicity, association class, abstract class, concrete class, class-scope attribute, abstract operation, method, polymorphism, overriding, multiple classification, aggregation, and composition.
- To describe the activities in the different phases of the object-oriented development life cycle.
- State the advantages of object-oriented modeling vis-à-vis structured approaches.
- Compare and contrast the object-oriented model with the E-R and EER models.
- Model a real-world application by using a UML class diagram.
- Provide a snapshot of the detailed state of a system at a point in time using a UML (Unified Modeling Language) object diagram.
- Recognize when to use generalization, aggregation, and composition relationships.
- Specify different types of business rules in a class diagram.

UNIT I

Introduction to UML: The meaning of Object Orientation, object identity, Encapsulation, information hiding, polymorphism, generosity, importance of modeling, principles of modeling, object oriented modeling, conceptual model of the UML, Architecture.

Basic Structural Modeling: Classes, Relationships, common Mechanisms, and diagrams.

Class & Object Diagrams: Terms, concepts, modeling techniques for Class & Object Diagrams.

Collaboration Diagrams: Terms, Concepts, depicting a message, polymorphism in collaboration diagrams, iterated messages, use of self in messages.

Sequence Diagrams: Terms, concepts, depicting asynchronous messages with/without priority, callback mechanism, broadcast messages.

UNIT II

Basic Behavioral Modeling: Use cases, Use case Diagrams, Activity Diagrams.

Advanced Behavioral Modeling: Events and signals, state machines, processes and Threads, time and space, state chart diagrams.

Architectural Modeling: Component, Deployment, Component diagrams and Deployment diagrams.

UNIT III

The Unified process: use case driven, architecture centric, iterative, and incremental

The Four Ps: people, project, product, and process

Use case driven process: why use case, capturing use cases, analysis, design, and implementation to realize the use cases, testing the use cases

Architecture-centric process: architecture in brief, why we need architecture, use cases and architecture, the steps to architecture, an architecture description.

UNIT IV

Iterative incremental process: iterative incremental in brief, why iterative incremental development? The iterative approach is risk driven, the generic iteration.

The Generic Iteration workflow: phases are the first division workflow, planning proceeds doing, risks affect project planning, use case prioritization, resource needed, assess the iteration and phases

Inception phase: early in the inception phase, the archetypal inception iteration workflow, execute the core workflows, requirements to test.

UNIT V

Elaboration Phase: elaboration phase in brief, early in the elaboration phase, the architectural elaboration iteration workflow, execute the core workflows-Requirements to test.

Construction phase: early in the construction phase, the archetypal construction iteration workflow, execute the core workflow.

Transition phase: early in the transition phase, activities in transition phase

Case Studies: Automation of a Library, Software Simulator application (2-floor elevator simulator)

TEXT BOOKS:

1. The Unified Modeling Language User Guide, Grady Booch, James Rumbaugh, Ivar Jacobson 2nd Edition, Pearson Education.
2. UML 2 Toolkit by Hans-Erik Eriksson, Magnus Penker, Brian Lyons, David Fado WILEY-Dreamtech India Pvt. Ltd.
3. The Unified Software Development Process by Ivar Jacobson, Grady Booch, James Rumbaugh, Pearson Education

REFERENCE BOOKS:

1. Fundamentals of Object Oriented Design in UML By Meilir Page-Jones, Pearson Education
2. Object Oriented Analysis & Design By Atul Kahate, The McGraw-Hill.
3. Practical Object-Oriented Design with UML By Mark Priestley, TATA Mc Graw Hill
4. Object Oriented Analysis & Design By Brett D McLaughlin, Gary Pollice and David West, O'REILY.
5. Object-Oriented Analysis and Design using UML by Simon Bennet, Steve McRobb and Ray Farmer, 2nd Edition, TATA Mc Graw Hill.
6. Object-Oriented Analysis and Design with the Unified Process By John W. Satzinger, Robert B Jackson and Stephen D Burd, Cengage Learning.
7. UML and C++, R.C.Lee, and W.M.Tepfenhart, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Software Engg.)

WEB TECHNOLOGIES AND SERVICES LAB**Objectives:**

- Write syntactically correct HTTP messages and describe the semantics of common HTTP methods and header fields
- Discuss differences between URIs, URNs, and URLs, and demonstrate a detailed understanding of http-scheme URLs, both relative and absolute
- Describe the actions, including those related to the cache, performed by a browser in the process of visiting a Web address
- Install a web server and perform basic administrative procedures, such as tuning communication parameters, denying access to certain domains, and interpreting an access log
- Write a valid standards-conformant HTML document involving a variety of element types, including hyperlinks, images, lists, tables, and forms
- Use CSS to implement a variety of presentation effects in HTML and XML documents, including explicit positioning of elements
- Demonstrate techniques for improving the accessibility of an HTML document

List of Sample Problems:**i) Web Technologies**

1. Develop static pages (using Only HTML) of an online Book store. The pages should resemble: www.amazon.com the website should consist the following pages.
Home page, Registration and user Login
User Profile Page, Books catalog
Shopping Cart, Payment By credit card
Order Conformation
2. Validate the Registration, user login, user profile and payment by credit card pages using JavaScript.
3. Create and save an XML document at the server, which contains 10 users information. Write a program, which takes User Id as an input and returns the user details by taking the user information from the XML document.
4. Install TOMCAT web server. Convert the static web pages of assignments 2 into dynamic web pages using Servlets and cookies. Hint: Users information (user id, password, credit card number) would be stored in web.xml. Each user should have a separate Shopping Cart.
5. Redo the previous task using JSP by converting the static web pages of assignments 2 into dynamic web pages. Create a database with user information and books information. The books catalogue should be dynamically loaded from the database. Follow the MVC architecture while doing the website.
6. Implement the “Hello World!” program using JSP Struts Framework.

ii) Additional Assignment Problems

Write an HTML page including any required Javascript that takes a number from one text field in the range of 0 to 999 and shows it in another text field in words. If the number is out of range, it should show “out of range” and if it is not a number, it should show “not a number” message in the result box.

Write a java swing application that takes a text file name as input and counts the characters, words and lines in the file. Words are separated with white space characters and lines are separated with new line character.

Write a simple calculator servlet that takes two numbers and an operator (+, -, /, * and %) from an HTML page and returns the result page with the operation performed on the operands. It should check in a database if the same expression is already computed and if so, just return the value from database. Use MySQL or PostgreSQL.

Write an HTML page that contains a list of 5 countries. When the user selects a country, its capital should be printed next to the list. Add CSS to customize the properties of the font of the capital (color, bold and font size).

Write a servlet that takes name and age from an HTML page. If the age is less than 18, it should send a page with “Hello <name>, you are not authorized to visit this site” message, where <name> should be replaced with the entered name. Otherwise it should send “Welcome <name> to this site” message.

Write a calculator program in HTML that performs basic arithmetic operations (+, -, /, * and %). Use CSS to change the foreground and background color of the values, buttons and result display area separately. Validate the input strings using JavaScript regular expressions. Handle any special cases like division with zero reasonably. The screen may look similar to the following:

Value 1	Operator		Value 2		Result
	+	▼		=	

Color Scheme Black on White ▼

Result

Write a Java program that creates a calculator GUI, as shown in figure. Extra components may be added for convenience:

The Color Scheme may be Black on White or Blue on Yellow (selectable) and accordingly all components colors must be changed. The values can be either entered or increased or decreased by a step of 10. The operators are +, -, / and * (selectable). Once any change takes place, the result must be automatically computed by the

program.

Write a Java Application that will read an XML file that contains personal information (Name, Mobile Number, age and place). It reads the information using SAX parser. After reading the information, it shows two input Text Fields in a window, one for tag name and the other for value. Once these two values are given, it should list all the records in the XML file that match the value of the given field in a text area (result box). For example, if the two text boxes are entered with “name” and “ABCD” then it should show all the records for which name is “ABCD”? An Illustration is given below that takes a mobile number and lists all the records that have the same mobile number.

Field	mobile	
Value	94494449449	OK
Result	abc, 22, Hyd def, 23, Delhi xxx, 44, Chennai	

Consider the following web application for implementation:

The user is first served a login page which takes user's name and password. After submitting the details the server checks these values against the data from a database and takes the following decisions.

If name and password matches, serves a welcome page with user's full name.

If name matches and password doesn't match, then serves "password mismatch" page

If name is not found in the database, serves a registration page, where users full name, present user name (used to login) and password are collected. Implement this application in:

1. Pure JSP
2. Pure Servlets
3. Struts Framework

Implement a simple arithmetic calculator with +, -, /, *, % and = operations using Struts Framework The number of times the calculator is used should be displayed at the bottom (use session variable).

iii) Web Technologies and Services Lab - Additional Problems

Create a web Service in Java that takes two city names from the user and returns the distance between these two from data available from a table in MySql.

Write a java and a C# client which use the above service

Write a Java program that takes a file as input and encrypts it using DES encryption. The program should check if the file exists and its size is not zero.

Write a Java program that generates a key pair and encrypts a given file using RSA algorithm.

Write a Java program that finds digest value of a given string.

Consider the following xml file for encryption

```
<?xml version="1.0"> <transaction> <from>12345</from> <to>54321</to> <amount>10000</amount>
<secretcode>abc123</secretcode> <checksum></checksum> </transaction>
```

Replace <from> and <to> values with the RSA encrypted values represented with base64 encoding assuming that the public key is available in a file in local directory "pubkey.dat". Encrypt <secretcode> with AES algorithm with a password 'secret'. The checksum of all the field values concatenated with a delimiter character '+' will be inserted in the checksum and the xml file is written to encrypted.xml file.

Assume that a file 'config.xml', which has the following information:

```
<users>
<user> <name>abc</name> <pwd>pwd123</pwd> <role>admin</role> <md5>xxx</md5> </user>
<user> <name>def</name> <pwd>pwd123</pwd> <role>guest</role> <md5>xxx</md5> </user>
</users>
```

Replace name and role with DES encrypted values and pwd with RSA encrypted values (represent the values with base64 encoding). The public key is available in "public.key" file in current directory. Replace xxx with respective MD5 values of all the fields for each user. Write the resulting file back to config.xml.

Write an HTML page that gives 3 multiple choice (a,b,c and d) questions from a set of 5 preloaded questions randomly. After each question is answered change the color of the question to either green or blue using CSS. Finally on clicking OK button that is provided, the score should be displayed as a pop-up window. Use Java Script for dynamic content.

Write an HTML page that has 3 countries on the left side ("USA", "UK" and "INDIA") and on the right side of each country, there is a pull-down menu that contains the following entries: ("Select Answer", "New Delhi",

"Washington" and "London"). The user will match the Countries with their respective capitals by selecting an item from the menu. The user chooses all the three answers (whether right or wrong). Then colors of the countries should be changed either to green or to red depending on the answer. Use CSS for changing color.

Write an HTML Page that can be used for registering the candidates for an entrance test. The fields are: name, age, qualifying examination (diploma or 10+2), stream in qualifying examination. If qualifying examination is "diploma", the stream can be "Electrical", "Mechanical" or "Civil". If the qualifying examination is 10+2, the stream can be "MPC" or "BPC". Validate the name to accept only characters and spaces.

Write an HTML page that has two selection menus. The first menu contains the states ("AP", "TN" and "KN") and depending on the selection the second menu should show the following items: "Hyderabad", "Vijayawada", "Kurnool" for AP, "Chennai", "Salem", "Madurai" for TN and "Bangalore", "Bellary", "Mysore" for KN.

Write an HTML page that has phone buttons 0 to 9 and a text box that shows the dialed number. If 00 is pressed at the beginning, it should be replaced with a + symbol in the text box. If the number is not a valid international number (+ followed by country code and 10 digit phone number) the color of the display should be red and it should turn to green when the number is valid. Consider only "+91, +1 and +44 as valid country codes. Use CSS for defining colors.

Write an HTML page that has a text box for phone number or Name. If a number is entered in the box the name should be displayed next to the number. If 00 is pressed at the beginning, it should be replaced with a + symbol in the text box. If a name is entered in the text box, it should show the number next to the name. If the corresponding value is not found, show it in red and show it in green otherwise. Use CSS for colors. Store at least 5 names and numbers in the script for testing.

A library consists of 10 titles and each title has a given number of books initially. A student can take or return a book by entering his/her HTNo as user ID and a given password. If there are at least two books, the book is issued and the balance is modified accordingly.

- (a) Use RDBMS and implement it with JSP.
- (b) Use XML File for data and Implement it with JSP
- (c) Use RDBMS and implement it with Servlets
- (d) Use XML File for data and Implement it with Servlets

A Bus Reservation System contains the details of a bus seat plan for 40 seats in 2x2 per row arrangement, where the seats are numbered from 1 to 40 from first row to last row. The customer can visit the website and can reserve a ticket of his choice if available by entering his details (Name, Address, Gender and Age). The customer can cancel the ticket by entering the seat number and his name as entered for reservation.

- (a) Use RDBMS and implement it with JSP.
- (b) Use XML File for data and Implement it with JSP
- (c) Use RDBMS and implement it with Servlets
- (d) Use XML File for data and Implement it with Servlets.

Implement a simple messaging system with the following details:

When a student logs in with his/her HTNO and a given password, they should get all the messages posted to him/her giving the ID of sender and the actual message. Each message may be separated with a ruler. There should be a provision for the user to send a message to any number of users by giving the IDs separated with commas in the "To" text box.

- (a) Use RDBMS and implement it with JSP.

- (b) Use XML File for data and Implement it with JSP
- (c) Use RDBMS and implement it with Servlets
- (d) Use XML File for data and Implement it with Servlets.

There is an image of 600x100 size which can be logically divided into 12 button areas with labels (0-9, +, =). Write a javascript calculator program that uses this image as input virtual keyboard and three text areas for two input numbers and result of sum of these numbers. Add a CSS that can be used to change the colors of text and background of text areas and the page. The input numbers can be up to 4 digits each.

Develop a web application that takes user name and password as input and compares them with those available in an xml user database. If they match, it should display the welcome page that contains the user's full name and last used date and time retrieved from a client cookie. On logout it stores new time to the cookie and displays a goodbye page. If authentication fails, it should store the attempt number to the client cookie and displays an error page. Add necessary CSS that takes care of the font, color of foreground and background.

A web application has the following specifications:

The first page (Login page) should have a login screen where the user gives the login name and password. Both fields must be validated on client side for a minimum length of 4 characters, name should be lower case a-z characters only and password should contain at least one digit. On submitting these values, the server should validate them with a MySQL database and if failed, show the login page along with a message saying "Login Name or Password Mismatch" in Red color below the main heading and above the form. If successful, show a welcome page with the user's full name (taken from database) and a link to Logout. On logout, a good bye page is displayed with the total time of usage (Logout time – login time). Specify the Schema details of table and web.xml file contents.

Implement it using (a) JSP Pages (b) Servlets (c) Struts

Design a struts based web portal for an international conference with following specifications:

The welcome page should give the details of the conference and a link to login. If login fails, direct them back for re-login and also provide a link for registration. On successful registration/login, the user will be directed to a page where s/he can see the status (accepted/rejected) of their already submitted papers followed by a form for submitting a doc file to the conference. Provide a logout button on all pages including the home page, once the user logs in. Implement validation framework to check that the user name is in the form of CCDDCC and password is in the form of (CCSDDD) (C for character, S for special character (one of @, #, \$, %, ^, & and !)) and D for digit)., Database should be accessed through Connection Pool for MySql for user information. Provide scope for internationalization in future. Assume any missing information and mention it first.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

SOFTWARE ARCHITECTURE AND DESIGN PATTERNS**Objectives:**

After completing this course, the student should be able to:

- To understand the concept of patterns and the Catalog.
- To discuss the Presentation tier design patterns and their affect on: sessions, client access, validation and consistency.
- To understand the variety of implemented bad practices related to the Business and Integration tiers.
- To highlight the evolution of patterns.
- To learn how to add functionality to designs while minimizing complexity
- To learn what design patterns really are, and are not
- To know about specific design patterns.
- To learn how to use design patterns to keep code quality high without over design.

UNIT I

Envisioning Architecture: The Architecture Business Cycle, What is Software Architecture, Architectural patterns, reference models, reference architectures, architectural structures and views.

Creating an Architecture: Quality Attributes, Achieving qualities, Architectural styles and patterns, designing the Architecture, Documenting software architectures, Reconstructing Software Architecture.

UNIT II

Analyzing Architectures: Architecture Evaluation, Architecture design decision making, ATAM, CBAM.

Moving from one system to many: Software Product Lines, Building systems from off the shelf components, Software architecture in future.

UNIT III

Patterns: Pattern Description, Organizing catalogs, role in solving design problems, Selection and usage.

Creational and Structural patterns: Abstract factory, builder, factory method, prototype, singleton, adapter, bridge, composite, façade, flyweight.

UNIT IV

Behavioural patterns: Chain of responsibility, command, Interpreter, iterator, mediator, memento, observer, state, strategy. template method, visitor.

UNIT V

Case Studies: A-7E – A case study in utilizing architectural structures, The World Wide Web - a case study in interoperability, Air Traffic Control – a case study in designing for high availability, Celsius Tech – a case study in product line development,

TEXT BOOKS:

1. Software Architecture in Practice, second edition, Len Bass, Paul Clements & Rick Kazman, Pearson Education, 2003.
2. Design Patterns, Erich Gamma, Pearson Education.

REFERENCE BOOKS:

1. Beyond Software architecture, Luke Hohmann, Addison wesley, 2003.

2. Software architecture, David M. Dikel, David Kane and James R. Wilson, Prentice Hall PTR, 2001
3. Software Design, David Budgen, second edition, Pearson education, 2003
4. Head First Design patterns, Eric Freeman & Elisabeth Freeman, O'REILLY, 2007.
5. Design Patterns in Java, Steven John Metsker & William C. Wake, Pearson education, 2006
6. J2EE Patterns, Deepak Alur, John Crupi & Dan Malks, Pearson education, 2003.
7. Design Patterns in C#, Steven John metsker, Pearson education, 2004.
8. Pattern Oriented Software Architecture, F.Buschmann & others, John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

SOFTWARE PROCESS AND PROJECT MANAGEMENT

Objectives:

At the end of the course, the student shall be able to:

- To describe and determine the purpose and importance of project management from the perspectives of planning, tracking and completion of project.
- To compare and differentiate organization structures and project structures.
- To implement a project to manage project schedule, expenses and resources with the application of suitable project management tools.

UNIT I

Software Process Maturity : Software maturity Framework, Principles of Software Process Change, Software Process Assessment, The Initial Process, The Repeatable Process, The Defined Process, The Managed Process, The Optimizing Process.

Process Reference Models Capability Maturity Model (CMM), CMMi, PCMM, PSP, TSP.

UNIT II

Software Project Management Renaissance Conventional Software Management, Evolution of Software Economics, Improving Software Economics, The old way and the new way.

Life-Cycle Phases and Process artifacts Engineering and Production stages, inception phase, elaboration phase, construction phase, transition phase, artifact sets, management artifacts, engineering artifacts and pragmatic artifacts, model based software architectures.

UNIT III

Workflows and Checkpoints of process Software process workflows, Iteration workflows, Major milestones, Minor milestones, Periodic status assessments.

Process Planning Work breakdown structures, Planning guidelines, cost and schedule estimating process, iteration planning process, Pragmatic planning.

UNIT IV

Project Organizations Line-of- business organizations, project organizations, evolution of organizations, process automation.

Project Control and process instrumentation The seven core metrics, management indicators, quality indicators, life-cycle expectations, Pragmatic software metrics, metrics automation.

UNIT V

CCPDS-R Case Study and Future Software Project Management Practices Modern Project Profiles, Next-Generation software Economics, Modern Process Transitions.

TEXT BOOKS:

1. Managing the Software Process, *Watts S. Humphrey*, Pearson Education.
2. Software Project Management, *Walker Royce*, Pearson Education.

REFERENCE BOOKS:

1. Effective Project Management: Traditional, Agile, Extreme, Robert Wysocki, Sixth edition, Wiley India, rp2011.
2. An Introduction to the Team Software Process, Watts S. Humphrey, Pearson Education, 2000

3. Process Improvement essentials, James R. Persse, O'Reilly, 2006
4. Software Project Management, Bob Hughes & Mike Cotterell, fourth edition, TMH, 2006
5. Applied Software Project Management, Andrew Stellman & Jennifer Greene, O'Reilly, 2006.
6. Head First PMP, Jennifer Greene & Andrew Stellman, O'Reilly, 2007
7. Software Engineering Project Managent, Richard H. Thayer & Edward Yourdon, 2nd edition, Wiley India, 2004.
8. The Art of Project Management, Scott Berkun, SPD, O'Reilly, 2011.
9. Applied Software Project Management, Andrew Stellman & Jennifer Greene, SPD, O'Reilly, rp2011.
10. Agile Project Management, Jim Highsmith, Pearson education, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

SOFTWARE QUALITY ASSURANCE AND TESTING

Objectives:

The student should be able to:

- To understand software testing and quality assurance as a fundamental component of software life cycle
- To define the scope of SW T&QA projects
- To efficiently perform T&QA activities using modern software tools
- To estimate cost of a T&QA project and manage budgets
- To prepare test plans and schedules for a T&QA project
- To develop T&QA project staffing requirements
- To effectively manage a T&QA project

UNIT I

Software Quality Assurance and Standards: The Software Quality challenge, What is Software Quality, Software Quality factors, The components of Software Quality Assurance system, Software Quality Metrics, Costs of Software Quality, Quality Management Standards, Management and its role in Software Quality Assurance, SQA unit and other actors in SQA system. - **(Chapters: 1-4, 21-23, 25, 26) of T3**

Quality Standards: ISO 9000 and Companion ISO Standards, CMM, CMMI, PCMM, Malcom Balridge, 3 Sigma, 6 Sigma and other latest quality standards **(Refer Internet and R11, R12, R13).**

UNIT II

Software Testing Strategy and Environment: Minimizing Risks, Writing a Policy for Software Testing, Economics of Testing, Testing-an organizational issue, Management Support for Software Testing, Building a Structured Approach to Software Testing, Developing a Test Strategy

Building Software Testing Process: Software Testing Guidelines, workbench concept, Customizing the Software Testing Process, Process Preparation checklist - **(Chapters: 2,3) of T1**

Software Testing Techniques: Dynamic Testing – Black Box testing techniques, White Box testing techniques, Static testing, Validation Activities, Regression testing -**(Chapters: 4, 5, 6, 7, 8) of T2**

UNIT III

Software Testing Tools: Selecting and Installing Software Testing tools – **(Chapter 4) of T1.**

Automation and Testing Tools - **(Chapter 15) of T2**

Load Runner, Win runner and Rational Testing Tools, Silk test, Java Testing Tools, JMetra, JUNIT and Cactus. **(Refer Internet and R9, R10)**

UNIT IV**Testing Process**

Seven Step Testing Process – I: Overview of the Software Testing Process, Organizing of Testing, Developing the Test Plan, Verification Testing, Validation Testing. **(Chapters 6, 7, 8, 9, 10) of T1**

UNIT V

Seven Step Testing Process – II: Analyzing and Reporting Test results, Acceptance and Operational Testing, Post-Implementation Analysis.

Specialized Testing Responsibilities: Software Development Methodologies, Testing Client/Server Systems (Chapters 12, 13, 14, 15) of T1.

TEXT BOOKS:

1. Effective Methods for Software Testing, Third edition, *William E. Perry*, Wiley India, 2009
2. Software Testing – Principles and Practices, *Naresh Chauhan*, Oxford University Press, 2010.
3. Software Quality Assurance – From Theory to Implementation, *Daniel Galin*, Pearson Education, 2009.

REFERENCE BOOKS:

1. Testing Computer Software, Cem Kaner, Jack Falk, Hung Quoc Nguyen, Wiley India, rp2012.
2. Software Testing – Principles, Techniques and Tools, *M.G.Limaye*, Tata McGraw-Hill, 2009.
3. Software Testing - A Craftsman's approach, *Paul C. Jorgensen*, Third edition, Auerbach Publications, 2010.
4. Foundations of Software Testing, *Aditya P. Mathur*, Pearson Education, 2008.
5. Software Testing and Quality Assurance – Theory and Practice, *Kshirasagar Naik, Priyadashi Tripathy*, Wiley India, 2010.
6. Software Testing, *Ron Patton*, Second edition, Pearson Education, 2006.
7. Software Testing and Analysis – Process, Principles and Techniques, *Mauro Pezze, Michal Young*, Wiley India, 2008.
7. Software Testing Techniques, Boris Beizer, Second edition, Wiley India, 2006
8. Foundations of Software Testing, Dorothy Graham, et al., Cengage learning, 2007, rp 2010.
9. Software Testing - Effective Methods, Tools and Techniques, *Renu Rajani, Pradeep Oak*, Tata McGraw-Hill, rp2011.
10. Software Automation Testing Tools for Beginners, *Rahul Shende*, Shroff Publishers and Distributors, 2012.
11. Software Testing Tools, *K.V.K.K. Prasad*, Dream Tech Press, 2008.
12. Software Testing Concepts and Tools, *Nageswara Rao Pusuluri*, Dream Tech press, 2007.
13. Software Quality Assurance, *Milind Limaye*, Tata McGraw-Hill, 2011.
14. Software Quality – Theory and Management, *Alan C. Gillies*, Second edition, Cengage Learning, 2009.
15. Software Quality – A Practitioner's approach, *Kamna Malik, Praveen Choudhary*, Tata McGraw-Hill, 2008.
16. Software Quality Models and Project Management in a Nutshell, *Shailesh Mehta*, Shroff Publishers and Distributors, 2010.
17. Software Quality Engineering – Testing, Quality Assurance and Quantifiable Improvement, *Jeff Tian*, Wiley India, 2006.
18. Software Quality, *Mordechai Ben-Menachem/Garry S. Marliss*, Cengage Learning, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

COMPONENT BASED SOFTWARE ENGINEERING**Objectives:**

- To understand the essentials of component-based software engineering
- To know the main characteristics of components and component models
- To be aware of software development processes for component-based systems
- To be aware of the mutual relations between software architecture and component models

UNIT I

Component definition - Definition of a Software Component and its elements, The Component Industry Metaphor, Component Models and Component Services, An example specification for implementing a temperature regulator Software Component.

The Case for Components- The Business Case for components, COTS Myths and Other Lessons Learned in Component-Based Software Development.

UNIT II

Planning Team Roles for CBD, Common High-Risk Mistakes, CBSE Success Factors: Integrating Architecture, Process, and Organization.

Software Engineering Practices - Practices of Software Engineering, From Subroutines to Subsystems: Component-Based Software Development, Status of CBSE in Europe.

UNIT III

The Design of Software Component Infrastructures - Software Components and the UML, Component Infrastructures, Business Components, Components and Connectors, An OPEN process for CBD, Designing Models of Modularity and Integration.

Software Architecture, Software Architecture Design Principles, Product-Line Architectures.

UNIT IV

The Management of Component-Based Software Systems - Measurement and Metrics for Software Components, Implementing a Practical Reuse Program for Software Components, Selecting the Right COTS Software, Building instead of Buying, Software Component Project Management, The Trouble with Testing Components, Configuration Management and Component Libraries, The Evolution, Maintenance, and Management of CBS.

UNIT V

Component Technologies - Overview of the CORBA Component Model, Overview of COM+, Overview of the EJB Component Model, Bonobo and Free Software GNOME Components, Choosing between COM+, EJB, and CCM, Software Agents as Next Generation Software Components.

TEXT BOOK:

1. Component - Based Software Engineering, G.T. Heineman and W.T. Councill, Addison- Wesley, Pearson Education.

REFERENCE BOOKS:

1. Component Software, C.Szyperski, D.Gruntz and S.Murer, Pearson Education.
2. Software Engineering, Roger S. Pressman, 6th edition, Tata McGraw-Hill.
3. Software Engineering, Ian Sommerville, seventh edition, Pearson education, 2004.
4. Software Engineering Principles and Practice, Hans Van Vliet, 3rd edition, Wiley India edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

**SCRIPTING LANGUAGES
(ELECTIVE-III)****Objectives:**

- The course demonstrates an in depth understanding of the tools and the scripting languages necessary for design and development of applications dealing with Bio-information/ Bio-data. The instructor is advised to discuss examples in the context of Bio-data/ Bio-information application development.

UNIT I**Introduction to PERL and Scripting**

Scripts and Programs, Origin of Scripting , Scripting Today, Characteristics of Scripting Languages, Web Scripting, and the universe of Scripting Languages. PERL- Names and Values, Variables, Scalar Expressions, Control Structures, arrays, list, hashes, strings, pattern and regular expressions, subroutines, advance perl - finer points of looping, pack and unpack, filesystem, eval, data structures, packages, modules, objects, interfacing to the operating system, Creating Internet ware applications, Dirty Hands Internet Programming, security Issues.

UNIT II**PHP Basics**

PHP Basics- Features, Embedding PHP Code in your Web pages, Outputting the data to the browser, Datatypes, Variables, Constants, expressions, string interpolation, control structures, Function, Creating a Function, Function Libraries, Arrays, strings and Regular Expressions.

UNIT III**Advanced PHP Programming**

Php and Web Forms, Files, PHP Authentication and Methodologies -Hard Coded, File Based, Database Based, IP Based, Login Administration, Uploading Files with PHP, Sending Email using PHP, PHP Encryption Functions, the Mcrypt package, Building Web sites for the World – Translating Websites- Updating Web sites Scripts, Creating the Localization Repository, Translating Files, text, Generate Binary Files, Set the desired language within your scripts, Localizing Dates, Numbers and Times.

UNIT IV**TCL – Tk**

TCL Structure, syntax, Variables and Data in TCL, Control Flow, Data Structures, input/output, procedures, strings, patterns, files, Advance TCL- eval, source, exec and up level commands, Name spaces, trapping errors, event driven programs, making applications internet aware, Nuts and Bolts Internet Programming, Security Issues, C Interface. Tk-Visual Tool Kits, Fundamental Concepts of Tk, Tk by example, Events and Binding, Perl-Tk.

UNIT V**Python**

Introduction to Python language, python-syntax, statements, functions, Built-in-functions and Methods, Modules in python, Exception Handling, Integrated Web Applications in Python – Building Small, Efficient Python Web Systems ,Web Application Framework.

TEXT BOOKS:

1. The World of Scripting Languages, David Barron, Wiley Publications.

2. Python Web Programming, Steve Holden and David Beazley, New Riders Publications.
3. Beginning PHP and MySQL, 3rd Edition, Jason Gilmore, Apress Publications (Dreamtech)

REFERENCE BOOKS:

1. Open Source Web Development with LAMP using Linux, Apache, MySQL, Perl and PHP, J.Lee and B.Ware (Addison Wesley) Pearson Education.
2. Programming Python, M.Lutz, SPD.
3. PHP 6 Fast and Easy Web Development, Julie Meloni and Matt Telles, Cengage Learning Publications.
4. PHP 5.1, I.Bayross and S.Shah, The X Team, SPD.
5. Core Python Programming, Chun, Pearson Education.
6. Guide to Programming with Python, M.Dawson, Cengage Learning.
7. Perl by Example, E.Quigley, Pearson Education.
8. Programming Perl, Larry Wall, T.Christiansen and J.Orwant, O'Reilly, SPD.
9. Tcl and the Tk Tool kit, Ousterhout, Pearson Education.
10. PHP and MySQL by Example, E.Quigley, Prentice Hall(Pearson).
11. Perl Power, J.P.Flynt, Cengage Learning.
12. PHP Programming solutions, V.Vaswani, TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

**INFORMATION RETRIEVAL SYSTEMS
(ELECTIVE III)****Objectives:**

On completion of this course you should have gained a good understanding of the foundation concepts of information retrieval techniques and be able to apply these concepts into practice. Specifically, you should be able to:

- To use different information retrieval techniques in various application areas
- To apply IR principles to locate relevant information large collections of data
- To analyse performance of retrieval systems when dealing with unmanaged data sources
- To implement retrieval systems for web search tasks.

UNIT I

Boolean retrieval. The term vocabulary and postings lists. Dictionaries and tolerant retrieval. Index construction. Index compression.

UNIT II

Scoring, term weighting and the vector space model. Computing scores in a complete search system. Evaluation in information retrieval. Relevance feedback and query expansion.

UNIT III

XML retrieval. Probabilistic information retrieval. Language models for information retrieval. Text classification. Vector space classification.

UNIT IV

Support vector machines and machine learning on documents, flat clustering, Hierarchical clustering, Matrix decompositions and latent semantic indexing.

UNIT V

Web search basics, Web crawling and indexes, Link analysis.

TEXT BOOK:

1. Introduction to Information Retrieval , Christopher D. Manning and Prabhakar Raghavan and Hinrich Schütze, Cambridge University Press, 2008.

REFERENCE BOOKS:

1. Information Storage and Retrieval Systems: Theory and Implementation, Kowalski, Gerald, Mark T Maybury, Springer.
2. Modern Information Retrieval, Ricardo Baeza-Yates, Pearson Education, 2007.
3. Information Retrieval: Algorithms and Heuristics, David A Grossman and Ophir Frieder, 2nd Edition, Springer, 2004.
4. Information Retrieval Data Structures and Algorithms, William B Frakes, Ricardo Baeza-Yates, Pearson Education, 1992.
5. Information Storage & Retrieval, Robert Korfhage , John Wiley & Sons.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

SEMANTIC WEB AND SOCIAL NETWORKS

(ELECTIVE-III)

Objectives:

- To learn Web Intelligence
- To learn Knowledge Representation for the Semantic Web
- To learn Ontology Engineering
- To learn Semantic Web Applications, Services and Technology
- To learn Social Network Analysis and semantic web

Unit -I:**Web Intelligence**

Thinking and Intelligent Web Applications, The Information Age, The World Wide Web, Limitations of Today's Web, The Next Generation Web, Machine Intelligence, Artificial Intelligence, Ontology, Inference engines, Software Agents, Berners-Lee www, Semantic Road Map, Logic on the semantic Web.

Unit -II:**Knowledge Representation for the Semantic Web**

Ontologies and their role in the semantic web, Ontologies Languages for the Semantic Web –Resource Description Framework(RDF) / RDF Schema, Ontology Web Language(OWL),UML,XML/XML Schema.

Unit-III:**Ontology Engineering**

Ontology Engineering, Constructing Ontology, Ontology Development Tools, Ontology Methods, Ontology Sharing and Merging, Ontology Libraries and Ontology Mapping, Logic, Rule and Inference Engines.

Unit-IV:**Semantic Web Applications, Services and Technology**

Semantic Web applications and services, Semantic Search, e-learning, Semantic Bioinformatics, Knowledge Base, XML Based Web Services, Creating an OWL-S Ontology for Web Services, Semantic Search Technology, Web Search Agents and Semantic Methods,

Unit-V:**Social Network Analysis and semantic web**

What is social Networks analysis, development of the social networks analysis, Electronic Sources for Network Analysis – Electronic Discussion networks, Blogs and Online Communities, Web Based Networks. Building Semantic Web Applications with social network features.

TEXT BOOKS:

1. Thinking on the Web - Berners Lee, Godel and Turing, Wiley interscience,2008.
2. Social Networks and the Semantic Web, Peter Mika, Springer,2007.

REFERENCE BOOKS:

1. Semantic Web Technologies, Trends and Research in Ontology Based Systems, J.Davies, R.Studer, P.Warren, John Wiley & Sons.
2. Semantic Web and Semantic Web Services -Liyang Lu Chapman and Hall/CRC Publishers,(Taylor & Francis Group)
3. Information Sharing on the semantic Web - Heiner Stuckenschmidt; Frank Van Harmelen, Springer Publications.
4. Programming the Semantic Web, T.Segaran, C.Evans, J.Taylor, O'Reilly, SPD.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

**CLOUD COMPUTING
(ELECTIVE-IV)****Objectives:****Prerequisite:** Computer Networks and Operating Systems**Course Description:**

Cloud computing has evolved as a very important computing model, which enables information, software, and shared resources to be provisioned over the network as services in an on-demand manner. This course provides an insight into what is cloud computing and the various services cloud is capable.

UNIT I**Systems Modeling, Clustering and Virtualization**

Distributed System Models and Enabling Technologies, Computer Clusters for Scalable Parallel Computing, Virtual Machines and Virtualization of Clusters and Data centers.

UNIT II**Foundations**

Introduction to Cloud Computing, Migrating into a Cloud, Enriching the 'Integration as a Service' Paradigm for the Cloud Era, The Enterprise Cloud Computing Paradigm.

UNIT III**Infrastructure as a Service (IAAS) & Platform and Software as a Service (PAAS / SAAS)**

Virtual machines provisioning and Migration services, On the Management of Virtual machines for Cloud Infrastructures, Enhancing Cloud Computing Environments using a cluster as a Service, Secure Distributed Data Storage in Cloud Computing.

Aneka, Comet Cloud, T-Systems, Workflow Engine for Clouds, Understanding Scientific Applications for Cloud Environments.

UNIT IV**Monitoring, Management and Applications**

An Architecture for Federated Cloud Computing, SLA Management in Cloud Computing, Performance Prediction for HPC on Clouds, Best Practices in Architecting Cloud Applications in the AWS cloud, Building Content Delivery networks using Clouds, Resource Cloud Mashups.

UNIT V**Governance and Case Studies**

Organizational Readiness and Change management in the Cloud age, Data Security in the Cloud, Legal Issues in Cloud computing, Achieving Production Readiness for Cloud Services.

TEXT BOOKS:

1. Cloud Computing: Principles and Paradigms by Rajkumar Buyya, James Broberg and Andrzej M. Goscinski, Wiley, 2011.
2. Distributed and Cloud Computing, Kai Hwang, Geoffery C.Fox, Jack J.Dongarra, Elsevier, 2012.

REFERENCE BOOKS:

1. Cloud Computing : A Practical Approach, Anthony T.Velte, Toby J.Velte, Robert Elsenpeter, Tata

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- McGraw Hill, rp2011.
 2. Enterprise Cloud Computing, Gautam Shroff, Cambridge University Press, 2010.
 3. Cloud Computing: Implementation, Management and Security, John W. Rittinghouse, James F.Ransome, CRC Press, rp2012.
 4. Cloud Application Architectures: Building Applications and Infrastructure in the Cloud, George Reese, O'Reilly, SPD, rp2011.
 5. Cloud Security and Privacy: An Enterprise Perspective on Risks and Compliance, Tim Mather, Subra Kumaraswamy, Shahed Latif, O'Reilly, SPD, rp2011.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

ADVANCED DATABASES**(ELECTIVE-IV)****Objectives:**

By the end of the course, you will know:

- History and Structure of databases
- How to design a database
- How to convert the design into the appropriate tables
- Handling Keys appropriately
- Enforcing Integrity Constraints to keep the database consistent
- Normalizing the tables to eliminate redundancies
- Querying relational data
- and processing the queries
- Storage Optimizing Strategies for easy retrieval of data through index
- Triggers, Procedures and Cursors ,Transaction Management
- Distributed databases management system concepts and Implementation

UNIT I

Database System Applications, Purpose of Database Systems, View of Data – Data Abstraction, Instances and Schemas, Data Models – the ER Model, Relational Model, Other Models – Database Languages – DDL,DML, Database Access from Applications Programs, Transaction Management, Data Storage and Querying, Database Architecture, Database Users and Administrators, ER diagrams,. Relational Model: Introduction to the Relational Model – Integrity Constraints Over Relations, Enforcing Integrity constraints, Querying relational data, Logical data base Design, Introduction to Views –Altering Tables and Views, Relational Algebra, Basic SQL Queries, Nested Queries, Complex Integrity Constraints in SQL, Triggers

UNIT II

Introduction to Schema Refinement – Problems Caused by redundancy, Decompositions – Problem related to decomposition, Functional Dependencies - Reasoning about FDS, Normal Forms – FIRST, SECOND, THIRD Normal forms – BCNF –Properties of Decompositions- Loss less- join Decomposition, Dependency preserving Decomposition, Schema Refinement in Data base Design – Multi valued Dependencies – FOURTH Normal Form, Join Dependencies, FIFTH Normal form.

UNIT III

Transaction Management: The ACID Properties, Transactions and Schedules, Concurrent Execution of Transactions – Lock Based Concurrency Control, Deadlocks – Performance of Locking – Transaction Support in SQL.

Concurrency Control: Serializability, and recoverability – Introduction to Lock Management – Lock Conversions, Dealing with Deadlocks, Specialized Locking Techniques – Concurrency Control without Locking.

Crash recovery: Introduction to Crash recovery, Introduction to ARIES, the Log, and Other Recovery related Structures, the Write-Ahead Log Protocol, Check pointing, recovering from a System Crash, Media recovery

UNIT IV

Overview of Storage and Indexing: Data on External Storage, File Organization and Indexing – Clustered Indexes, Primary and Secondary Indexes, Index data Structures – Hash Based Indexing, Tree based Indexing

Storing data: Disks and Files: -The Memory Hierarchy – Redundant Arrays of Independent Disks.

Tree Structured Indexing: Intuitions for tree Indexes, Indexed Sequential Access Methods (ISAM)

B+ Trees: A Dynamic Index Structure, Search, Insert, Delete.

Hash Based Indexing: Static Hashing, Extendable hashing, Linear Hashing, Extendable Vs Linear Hashing.

UNIT V

Distributed databases: Introduction to distributed databases, Distributed DBMS architectures, Storing data in a distributed DBMS, Distributed catalog management, Distributed query processing Updating distributed data, Distributed transactions, Distributed concurrency control, Distributed recovery

TEXT BOOKS:

1. Data base Management Systems, Raghu Ramakrishnan, Johannes Gehrke, TMH, 3rd Edition, 2003.
2. Data base System Concepts, A.Silberschatz, H.F. Korth, S.Sudarshan, McGraw hill, VI edition, 2006.
3. Fundamentals of Database Systems 5th edition, Ramez Elmasri, Shamkant B.Navathe, Pearson Education, 2008.

REFERENCE BOOKS:

1. Introduction to Database Systems, C.J.Date, Pearson Education.
2. Database Management System Oracle SQL and PL/SQL, P.K.Das Gupta, PHI.
3. Database System Concepts, Peter Rob & Carlos Coronel, Cengage Learning, 2008.
4. Database Systems, A Practical approach to Design Implementation and Management Fourth edition, Thomas Connolly, Carolyn Begg, Pearson education.
5. Database-Principles, Programming, and Performance, P.O'Neil & E.O'Neil, 2nd ed, ELSEVIER
6. Fundamentals of Relational Database Management Systems, S.Sumathi, S.Esakkirajan, Springer.
7. Introduction to Database Management, M.L.Gillenson and others, Wiley Student Edition.
8. Database Development and Management, Lee Chao, Auerbach publications, Taylor & Francis Group.
9. Distributed Databases Principles & Systems, Stefano Ceri, Giuseppe Pelagatti, TMH.
10. Principles of Distributed Database Systems, M. Tamer Ozsu, Patrick Valduriez, Pearson Education, 2nd Edition.
11. Distributed Database Systems, Chhanda Ray, Pearson.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – II Sem. (Software Engg.)****BUSINESS PROCESS MANAGEMENT****(ELECTIVE-IV)****Objectives:**

- To recognize the role of business processes within an Infinity based application
- To understand the importance of parameter sets to a business process
- To learn common patterns and best practices for formatting and restricting the output from a business process
- To understand the difference between a business process and a business process instance
- To learn how data processing occurs within a business process
- To list the Infinity SDK software developer responsibilities for building and supporting the functionality required for a business process
- To describe the database tables used by Blackbaud Enterprise CRM to manage business processes
- To describe how a QueryViewSpec can be used to define the output format for a business process
- To describe how a selection can be used to limit the rows processed by a business process

UNIT I**UNDERSTANDING BPM - I:**

How can we demystify business process management?

What is business process management?

Why is it important to improve business process before automating them?

When should you do BPM – what are the main drivers and triggers?

Who should be involved in BPM?

UNIT II**UNDERSTANDING BPM - II:**

Why are organizational strategy and process architecture important in BPM implementation?

How do you sell BPM technology to the organization?

What are the critical success factors in a BPM project?

What are the critical implementation aspects for a BPM solution?

UNIT III**FRAMEWORK - I:**

Framework overview, Guidelines on how to use the framework, Organization strategy phase, Process architecture phase, Launch pad phase, Understand phase, Innovate phase.

UNIT IV**FRAMEWORK – II:**

People phase, Develop phase, Implement phase, Realize value phase, Sustainable performance phase, Essentials introduction, Project management, People change management, Leadership.

UNIT V**BPM AND THE ORGANIZATION:**

BPM maturity, Embedding BPM within the organization.

TEXT BOOKS:

1. Business Process Management, Practical guidelines to successful implementations, John Jeston and Johan Nelis, Second edition, Elsevier, 2009.
2. Management by Process, A roadmap to sustainable Business Process Management, John Jeston and Johan Nelis, Elsevier, 2009.

REFERENCE BOOK:

1. Business Process Management Systems, Strategy and Implementation, James F. Chang, Auerbach Publications, Taylor and Francis group, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Software Engg.)

SOFTWARE TESTING LAB**Objectives:**

The student should be able to:

- To understand software testing and quality assurance as a fundamental component of software life cycle
- To define the scope of SW T&QA projects
- To efficiently perform T&QA activities using modern software tools
- To estimate cost of a T&QA project and manage budgets
- To prepare test plans and schedules for a T&QA project
- To develop T&QA project staffing requirements
- To effectively manage a T&QA project

Software Testing Objectives:

To learn to use the following (or Similar) automated testing tools to automate testing:

- a. Win Runner/QTP for functional testing.
- b. LoadRunner for Load/Stress testing.
- c. Test Director for test management.
- d. JUnit,HTMLUnit,CppUnit.

Sample problems on testing:

1. Write programs in 'C' Language to demonstrate the working of the following constructs:
i) do...while ii) while....do iii) if...else iv) switch v) for
2. "A program written in 'C' language for Matrix Multiplication fails" Introspect the causes for its failure and write down the possible reasons for its failure.
3. Take any system (e.g. ATM system) and study its system specifications and report the various bugs.
4. Write the test cases for any known application (e.g. Banking application)
5. Create a test plan document for any application (e.g. Library Management System)
6. Study of any testing tool (e.g. Win runner)
7. Study of any web testing tool (e.g. Selenium)
8. Study of any bug tracking tool (e.g. Bugzilla, bugbit)
9. Study of any test management tool (e.g. Test Director)
10. Study of any open source-testing tool (e.g. Test Link)
11. Take a mini project (e.g. University admission, Placement Portal) and execute it. During the Life cycle of the mini project create the various testing documents* and final test report document.

Additional problems on testing:

1. Test the following using JUnit and CppUnit:
i) Sorting problems ii) Searching problems
iii) Finding gcd of two integers iv) Finding factorial of a number.

2. Test web based forms using HTMLUnit.
3. Test database stored procedures using SQLUnit.

(Use sufficient number of test cases in solving above Problems)

***Note: To create the various testing related documents refer to the text “Effective Software Testing Methodologies by William E. Perry”**

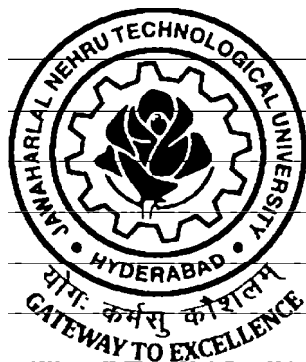
REFERENCE BOOKS:

1. Software Testing Concepts and Tools, P.Nageswara Rao, Dream Tech Press, 2007.
2. Software Testing Tools, K.V.K.K. Prasad, Dream Tech Press, 2008.
3. Software Testing with Visual Studio Team System 2008, S.Subashini, N.Satheesh kumar, Shroff Publishers Distributors.
4. Software Automation Testing Tools for Beginners, *Rahul Shende*, Shroff Publishers and Distributors, 2012.

ACADEMIC REGULATIONS COURSE STRUCTURE AND DETAILED SYLLABUS

M.TECH STRUCTURAL ENGINEERING

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/ year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

- Punishments to the candidates as per the above guidelines.
- Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - A show cause notice shall be issued to the college.
 - Impose a suitable fine on the college.
 - Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - STRUCTURAL ENGINEERING

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		Computer Oriented Numerical Methods	3	-	3
		Theory of Elasticity and Plasticity	3	-	3
		Theory and Analysis of Plates	3	-	3
		Advanced Reinforced Concrete Design	3	-	3
	Elective –I	Concrete Technology Experimental Stress Analysis Optimization Techniques in Structural Engineering	3	-	3
	Elective –II	Advanced Structural Analysis Soil Dynamics and foundation Engineering Composite Materials	3	-	3
	Lab	Advanced Concrete Laboratory	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Finite Element Methods	3	-	3
		Structural Dynamics	3	-	3
		Analysis and Design of Shells and Folded Plates	3	-	3
		Advanced. Steel Design	3	-	3
	Elective –III	Pre stressed Concrete Advanced Foundation Engineering Computer Aided Design in Structural Engineering (CAD)	3	-	3
	Elective –IV	Principles of Bridge Engineering Earthquake Resistant Design of Buildings Plastic Analysis and Design. Stability of Structures	3	-	3
	Lab	CAD Laboratory	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	-	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

COMPUTER ORIENTED NUMERICAL METHODS**Unit I:**

Solutions of linear equations: Direct method – Cramer's rule, Gauss – Elimination method- Gauss – Jordan elimination – Triangulation (LU Decomposition) method – Iterative methods Jacobi – Iteration method – Gauss – Seidel iteration, Successive over –relaxation method.

Eigen values and eigen vectors: Jacobi method for symmetric matrices- Given's method for symmetric matrices-Householder's method for symmetric matrices-Rutishauser method of arbitrary matrices – Power method.

UNIT II:

Interpolation: Linear Interpolation - Higher order Interpolation - Lagrange Interpolation – Interpolating polynomials using finite differences- Hermite Interpolation -piece-wise and spline Interpolation.

Unit III

Finite Difference and their Applications: Introduction- Differentiation formulas by Interpolating parabolas – Backward and forward and central differences- Derivation of Differentiation formulae using Taylor series- Boundary conditions- Beam deflection – Solution of characteristic value problems- Richardson's extrapolation- Use of unevenly spaced pivotal points- Integration formulae by interpolating parabolas- Numerical solution to spatial differential equations

UNIT IV.

Numerical Differentiation: Difference methods based on undetermined coefficients- optimum choice of step length– Partial differentiation.

Numerical Integration: Method based on interpolation-method based on undetermined coefficient – Gauss – Lagrange interpolation method- Radaua integration method- composite integration method – Double integration using Trapezoidal and Simpson's method.

UNIT V

Ordinary Differential Equation: Euler's method – Backward Euler method – Mid point method – single step method, Taylor's series method- Boundary value problems.

REFERENCES:

1. Numerical methods for scientific and engineering computations. M.K.Jain-S.R.K.Iyengar – R.K.Jain Willey Eastern Limited.
2. Numerical methods by S.S.Shastry.
3. Applied numerical analysis by – Curtis I.Gerala- Addison Wasley – published campus.
4. Numerical methods for Engineers Stevan C.Chopra, Raymond P.Canal Mc. Graw Hill book company.
5. C Language and Numerical methods by C.Xavier – New age international publisher.
6. Computer based numerical analysis by Dr. M.Shanta Kumar, Khanna Book publishers, New Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

THEORY OF ELASTICITY AND PLASTICITY**UNIT-I**

Introduction: Elasticity - notation for forces and stresses - components of stresses - components of strain - Hooks law. Plane stress and plane strain analysis - plane stress - plane strain - differential equations of equilibrium - boundary conditions - compatibility equations - stress function - boundary condition.

UNIT II.

Two dimensional problems in rectangular coordinates - solution by polynomials - Saint- Venant's principle - determination of displacements - bending of simple beams - application of corier series for two dimensional problems - gravity loading. Two dimensional problems in polar coordinates - stress distribution symmetrical about an axis - pure bending of curved bars - strain components in polar coordinates - displacements for symmetrical stress distributions - simple symmetric and asymmetric problems - general solution of two-dimensional problem in polar coordinates - application of general solution in polar coordinates.

UNIT III.

Analysis of stress and strain in three dimensions - principal stresses - stress ellipsoid - director surface - determination of principal stresses - max shear stresses – homogeneous deformation - principal axes of strain rotation. General Theorems: Differential equations of equilibrium - conditions of compatibility - determination of displacement - equations of equilibrium in terms of displacements - principle of super position - uniqueness of solution - the reciprocal theorem.

UNIT IV.

Torsion of Prismatic Bars - torsion of prismatic bars - bars with elliptical cross sections - other elementary solution - membrane analogy - torsion of rectangular bars - solution of torsion problems by energy method - use of soap films in solving torsion problems - hydro dynamical analogies - torsion of shafts, tubes , bars etc. Bending of Prismatic Bars: Stress function - bending of cantilever - circular cross section - elliptical cross section - rectangular cross section - bending problems by soap film method - displacements.

UNIT V.

Theory of Plasticity: Introduction - concepts and assumptions - yield criterions.

REFERENCES

1. Theory of Elasticity by Timeshanko, McGrawhill Publications.
2. Theory of Plasticity by J.Chakarbarthy, McGrawhill Publications.
3. Theory of Elasticity by Y.C.Fung.
4. Theory of Elasticity by Gurucharan Singh.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

THEORY AND ANALYSIS OF PLATES

UNIT I

Cylindrical Bending : Different kind of plates – Assumptions - Derivation of differential equation for cylindrical bending of long rectangular plates - Analysis of uniformly loaded rectangular plates with edges simply supported and fixed subjected to uniform load.

Pure Bending of Plates : Slope and curvature of slightly bent plates – Relations between moments and curvature - Particular cases of pure bending - Strain energy in pure bending –Energy methods like Ritz and Galerkin Methods to rectangular plates subjected to simple loadings.

UNIT II

Small Deflection Theory of Thin Rectangular Plates : Assumptions – Derivation of governing differential equation for thin plates – Boundary conditions – simply supported plate under sinusoidal load – Navier's solution – Application to different cases – Levy's solution for various boundary conditions subjected to different loadings like uniform and hydrostatic pressure.

UNIT III

Circular Plates : Symmetrical loading – Relations between slope, deflection, moments and curvature – Governing differential equation – Uniformly loaded plates with clamped and simply supported edges – Central hole – bending by moments and shearing forces uniformly distributed.

Orthotropic Plates : Introduction – Bending of anisotropic plates - Derivation of governing differential equation – Determination of Rigidities in various cases like R.C. slabs, corrugated sheet – Application to the theory of grid works.

UNIT IV

Plates on Elastic Foundations : Governing differential equation – deflection of uniformly loaded simply supported rectangular plate – Navier and Levy type solutions - Large plate loaded at equidistant points by concentrated forces P.

UNIT V

Buckling of Plates: Governing equation for Bending of plate under the combined action of in-plane loading and lateral loads – Buckling of rectangular plates by compressive forces acting in one and two directions in the middle plane of plate

Finite Difference Methods: Introduction - Application to rectangular plates subjected to simple loading.

REFERENCES:

1. Theory of Plates and Shells by Timoshenko, McGraw Hill Book Co., New York.
2. Theory and Analysis of Plates by P. Szilard, Prentice Hall.
3. Theory of Plates by Chandrasekhar, University Press.
4. Plate Analysis by N. K. Bairagi, Khanna Publishers. New Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED REINFORCED CONCRETE DESIGN

UNIT I

Basic Design Concepts: Behaviour in flexure, Design of singly reinforced rectangular sections, Design of doubly reinforced rectangular sections, Design of flanged beams, Design of shear, Design for Torsion, Limit state of Serviceability: Deflections of Reinforced concrete beams and slabs, short term deflection and long term deflection, estimation of crack width in RCC members, calculation of crack widths.

UNIT II

Limit Analysis of R.C.Structures: Rotation of a plastic hinge, Redistribution of moments, moment rotation characteristics of RC member, I.S. code provisions, applications for fixed and continuous beam. Yield line analysis for slabs: Upper bound and lower bound theorems – yield line criterion – Virtual work and equilibrium methods of analysis for square and circular slabs with simple and continuous end conditions.

UNIT III

Design of Ribbed slabs, Flat slabs: Analysis of the Slabs for Moment and Shears, Ultimate Moment of Resistance, Design for shear, Deflection, Arrangement of Reinforcements.

Flat slabs: Direct design method – Distribution of moments in column strips and middle strip-moment and shear transfer from slabs to columns – Shear in Flat slabs-Check for one way and two way shears - Introduction to Equivalent frame method. Limitations of Direct design method, Distribution of moments in column strips and middle strip

UNIT IV

Design of Reinforced Concrete Deep Beams & Corbels: Steps of Designing Deep Beams, Design by IS 456, Checking for Local Failures, Detailing of Deep Beams, Analysis of Forces in a Corbels, Design of Procedure of Corbels, Design of Nibs.

UNIT V

Design of Compression members: Estimation of effective length of a column-Code requirements on Slenderness Limits, Design of Short Columns under Axial Compression, Design of Short Columns with Uniaxial Bending, Design of Short Columns under Biaxial Bending, Design of Slender Columns.

Design of Combined Footings- Distribution of soil Pressure – Geometry of Two Column Combined Footing – Design Considerations in Combined Footing for Two – Columns.

TEXT BOOKS:

1. Reinforced concrete design by S. Unnikrishna Pillai & Menon, Tata Mc. Graw Hill, 2nd Edition, 2004
2. Advanced Reinforced Concrete Design – P.C. Varghese, Practice Hall, 2008.
3. Limit state theory and design of reinforced concrete by Dr. S.R. Karve and Dr. V.L. Shah, Standard publishers, Pune, 3rd Edition, 1994.

REFERENCE BOOKS:

1. Reinforced concrete design by Kenneth Leet, Tata Mc. Graw-Hill International, editions, 2nd edition, 1991.
2. Reinforced concrete structural elements – behaviour, Analysis and design by P.Purushotham, Tata Mc.Graw-Hill, 1994.
3. Design of concrete structures – Arthus H. Nilson, David Darwin, and Chorles W. Dolar, Tata Mc. Graw-Hill, 3rd Edition, 2005.
4. Reinforced concrete structures, Vol.1, by B.C. Punmia, Ashok Kumar Jain and Arun Kumar Jain, Laxmi Publications, 2004.
5. Reinforced concrete structures – I.C. Syal & A.K. Goel, S. Chand, 2004..

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

CONCRETE TECHNOLOGY**(ELECTIVE -I)****UNIT – I**

Concrete Making Materials: Cement- Bogue compounds – Hydration Process– Types of cement – Aggregates – Gradation Charts – Combined aggregate-Alkali Silica Reaction -Admixtures – Chemical and Mineral admixtures..

UNIT – II

Fresh and Hardened Concrete: Fresh Concrete - workability tests on Concrete Setting times of Fresh Concrete - Segregation and bleeding.

Hardened Concrete : Abram's law- Gel space ratios, Maturity Concept – Stress Behaviour – Creep and Shrinkage – Durability tests on concrete - Non destructive testing of concrete.

UNIT - III

High Strength Concrete – Micro structure – Manufacturing and Properties- Design of HSC Using Eirintroy Shaklok Method- Ultra High Strength Concrete.

High Performance Concrete- Requirements and properties of High Performance Concrete- Design Considerations.

UNIT –IV

Special Concrete: Self Compacting concrete – Polymer concrete – Fiber reinforced concrete –Reactive Powder concrete – Requirements and Guidelines – Advantages and Applications. Light weight concrete.

Concrete mix design : Quality Control - Quality assurance - Quality audit- Mix Design method - BIS method, ACI method, DOE method.

UNIT –V

Form work – materials – structural requirements – form work systems – connections – specifications – design of form work – shores – removal of forms – reshoring – failure of form work.

TEXT BOOKS:

1. Properties of Concrete by A.M.Neville, ELBS publications.
2. Concrete Technology by A.K. Santhakumar, Oxford Press.
3. Concrete Technology by M.S.Shetty, S.Chand & Co.

REFERENCES:

1. Special Structural concretes by Rajat Siddique, Galgotia Publications.
2. Design of Concrete Mixes by N.Krishna Raju, CBS Publications.
3. Concrete: Micro Structure by P.K.Mehta, ICI, Chennai.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

EXPERIMENTAL STRESS ANALYSIS**(ELECTIVE -I)****UNIT I**

Basic equations and Plane Elasticity Theory: Introduction, Strain equations of Transformation, Compatibility, Stress-Strain Relations-Two dimensional State of Stress. The Plane-Elastic problem, The Plane-Strain Approach, Plane Stress, Airy's Stress function-Cartesian Co-ordinates-Two dimensional problems in Polar Co-ordinates, Polar Components of Stress in terms of Airy's Stress function, Forms.

Principles of Experimental Approach: Merit of Experimental Analysis introduction, uses of experimental stress analysis-Advantages of experimental stress analysis, Different methods, Simplification of problems.

UNIT II

Strain Measurement using Strain Gauges: Definition of strain and its relation to Experimental Determinations, properties of strain-gauge systems, Types of strain gauges, Mechanical and Optical strain gauges. Electrical Strain Gauges - Introduction, LVDT - resistance strain gauge - various types - gauge factor, Materials for adhesion base, etc.

Strain Rosettes: Introduction, The three element rectangular Rosette - The delta rosette - Corrections for Transverse strain effects.

UNIT III

Brittle Coating Method: Introduction, Coating stresses - Failure theories - Brittle coating Crack pattern - Crack detection - Types of Brittle coating - Test procedures for brittle coating analysis - Calibration procedures - Analysis of brittle coating data.

UNIT IV

Theory of Photo Elasticity: Introduction, Temporary double refraction - The stress optic law - Effects of stressed model in a Polaris cope for various arrangements - Fringe sharpening, Brewster stress optic law.

UNIT V

Two Dimensional Photo Elasticity: Introduction, Isochromatic Fringe patterns - Isoclinic fringe patterns, passage of light through plane Polaris cope and circular Polaris cope, Isoclinic fringe pattern - Compensation techniques - calibration methods, separation methods, scaling Model to Proto type stress- Materials for photo - elasticity, properties of photo elastic materials.

REFERENCES :

1. Experimental Stress Analysis by J.W.Dally and W.F.Riley.
2. Experimental Stress Analysis by Dr. Sadhu Singh.
3. Experimental Stress Analysis by Dove and Adams.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

**OPTIMIZATION TECHNIQUES IN STRUCTURAL ENGINEERING
(ELECTIVE -I)****UNIT I**

Introduction to Optimization: Introduction - Historical developments - Engineering applications of Optimization - Statement of an Optimization problem - Classification of Optimization problems - Optimization Techniques. Optimization by calculus: Introduction - Unconstrained functions of a single variable - Problems involving simple constraints - Unconstrained functions of several variables - treatment of equality constraints - Extension to multiple equality constraints - Optimization with inequality constraints - The generalized Newton-Raphson method.

UNIT II

Linear Programming: Introduction - Applications of linear programming - standard form of a linear programming problem - Geometry of linear programming problems - Definitions and theorems - Solution of a system of Linear simultaneous equations - Pivotal reduction of a general system of equations - Motivation of the Simplex Method - Simplex Algorithm - Two phases of the simplex method. non-Linear Programming: Introduction - Unimodal Function - Unrestricted search - Exhaustive search - Dichotomous search - Interval Halving method - Fibonacci method - Golden section method - Comparison of elimination methods - Unconstrained optimization techniques - Direct search methods - Random search methods - grid search method - Univariate method - Powell's method - Simplex method - Indirect search methods - Gradient of a function - Steepest descent method - Conjugate gradient - Newton's method.

UNIT III

Dynamic Programming: Introduction - Multistage decision processes - concept of sub-optimization and the principle of optimality - computational procedure in dynamic programming - example illustrating the Calculus method of solution - example illustrating the Tabular of solution - conversion of a final value problem into an initial value problem - continuous dynamic programming - Additional applications.

UNIT IV

Network Analysis: Introduction - Elementary graph theory - Network variables and problem types - Minimum-cost route - Network capacity problems - Modification of the directional sense of the network.

UNIT V

Application of Optimization techniques to trusses, Beams and Frames.

REFERENCES

1. Optimization: Theory and Applications by S.S.Rao.
2. Numerical Optimization Techniques for Engineering Design with applications by G.N.Vanderplaats.
3. Elements of Structural Optimization by R.T.Haftka and Z.Gurdal.
4. Optimum Structural Design by U.Kirsch.
5. Optimum Design of Structures by K.I.Majid.
6. Introduction to Optimum Design by J.S.Arora.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED STRUCTURAL ANALYSIS**(ELECTIVE-II)****UNIT I**

Introduction to matrix methods of analysis - static indeterminacy and kinematic indeterminacy - degree of freedom - coordinate system - structure idealization stiffness and flexibility matrices - suitability element stiffness equations - elements flexibility equations - mixed force - displacement equations - for truss element, beam element and tensional element.

Transformation of coordinates - element stiffness matrix - and load vector - local and global coordinates.

UNIT II

Assembly of stiffness matrix from element stiffness matrix - direct stiffness method - general procedure - banded matrix - semi bandwidth - computer algorithm for assembly by direct stiffness matrix method.

UNIT III

Analysis of plane truss - continuous beam - plane frame and grids by flexibility methods.

UNIT IV

Analysis of plane truss - continuous beam - plane frame and grids by stiffness methods.

UNIT V.

Special analysis procedures - static condensation and sub structuring - initial and thermal stresses.

Shear walls- Necessity - structural behaviour of large frames with and without shear walls - approximate methods of analysis of shear walls.

REFERENCES

1. Matrix Analysis of Frames structures by William Weaver J.R and James M.Geve, CBS publications.
2. Advanced Structural Analysis by Ashok.K.Jain, New Channel Brothers.
3. Structural Analysis by C.S.Reddy.
4. Matrix Structural Analysis by Kanchi.
5. Matrix Methods of Structural Analysis by J.Meek.
6. Structural Analysis by Ghali and Neyveli.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

**SOIL DYNAMICS AND FOUNDATION ENGINEERING
(ELECTIVE-II)****UNIT I**

Types of machine foundations – general requirements design – criteria for machine foundations, permissible amplitudes and bearing pressure. Resonance and its effect – free and forced Vibrations with and without damping – constant force and rotating mass type excitation – magnification steady state vibrations – logarithmic decrement.

UNIT II

Natural frequency of foundation – soil system – Barkan's and I.S. methods of determining natural frequency.

UNIT III

Elastic properties of soil for dynamical purpose and their experimental determination – Elastic waves and their characteristics – Experimental determination of shear modulus from wave theory.

UNIT IV

Apparent soil mass – bulb of pressure concept – Pauw's analogy of foundation – soil systems (Concept only) - Theory of elastic half space – lamb and the dynamic Boussinesq's problem – Relsner's solution and its limitations – Quinlan and Sung's modifications – Hsiegh's equations for vertical vibration.

UNIT V

Principles of design of foundations for reciprocating and impact type of machine – as per I.S. Codes. Vibration isolation – types and methods of isolation – isolating materials and their properties.

REFERENCES:

1. Hand Book of Machine Foundations by S. Srinivasulu and Vaidganathan.
2. Soil Mechanics & Foundation Engineering by B.C. Punmia.
3. Analysis and Design of Foundation and retaining structures-Sham Sher Prakets, Etal.
4. Vibration of Soils & Foundations – Richant Hall & Woods.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

COMPOSITE MATERIALS**(ELECTIVE-II)****UNIT - I**

Introduction: Requirements of structural materials, influence of nature of materials in structural form, Nature of structural materials- Homogeneous materials, composite materials.

UNIT - II

Macro mechanical Properties of composite Laminae: Introduction, Assumptions and Idealizations, Stress Strain relationships for composite Laminae- Isotropic, Orthotropic laminae, Strength Characteristics- Basic concepts, Strength hypothesis for isotropic and Orthotropic laminae. Macro mechanical Analysis of composite Laminae: Introduction, Assumptions and Limitations, Stiffness characteristics of glass reinforced laminae- Stress- Strain relationships in continuous, discontinuous fibre laminae, Strength characteristics of glass reinforced laminae- Strengths in continuous, discontinuous fibre aminaе.

UNIT - III

Behaviour of Glass Fibre-Reinforced laminates: Introduction, Stiffness characteristics of Laminated composites-Behaviour of Laminated beams and plates, Strength characteristics of Laminated composites- Strength analysis and failure criteria, Effect of inter laminar structures. Glass Reinforced Composites: Introduction, Continuously reinforced laminates- uni-directionally and multi directionally continuously reinforced laminates, Discontinuously reinforced laminates – Stiffness and Strength properties.

UNIT - IV

GRP properties relevant to structural Design: Introduction, Short-term strength and stiffness-Tensile, Compressive, Flexural and Shearing. Long term strength and stiffness properties, Temperature effects, Effect of fire, Structural joints- Adhesive, mechanical, Combinational, Transformed sections.

UNIT - V

Design of GRP Box Beams: Introduction, loading, span and cross-sectional shape, Selection of material, Beam manufacture, Beam stresses, Experimental Behaviour, Effect on Beam performance- Modulus of Elasticity, Compressive Strength, I value, prevention of compression buckling failure, Behaviour under long term loading.

Design of Stressed skinned roof structure: Introduction, loading and material properties, preliminary design, and computer analysis.

REFERENCES:

1. GRP in Structural Engineering M.Holmes and D.J.Just.
2. Mechanics of Composite materials and Structures by Manjunath Mukhopadhyay; Universities Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (Structural Engg.)

ADVANCED CONCRETE LABORATORY

1. Tests on cement - Consistency, Setting times, Soundness, Compressive Strength.
2. Gradation Charts of Aggregates.
3. Bulking of fine Aggregate.
4. Aggregate Crushing and Impact value
5. Workability Tests on Fresh self compacting concrete
6. Air Entrainment Test on fresh concrete.
7. Marsh cone test.
8. Permeability of Concrete.
9. Non Destructive Testing of Concrete.
10. Accelerated Curing of Concrete.
11. Influence of W/C ratio on strength and Aggregate / Cement ratio on workability and Strength
12. Influence of Different Chemical Admixtures on concrete.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

FINITE ELEMENT METHODS**UNIT I**

Introduction: Concepts of FEM - steps involved - merits and demerits - energy principles – discrimination - Raleigh - Ritz method of functional approximation.

Principles of Elasticity: Stress equations - strain displacement relationships in matrix form plane stress, plane strain and axi-symmetric bodies of revolution with axi-symmetric loading.

UNIT II

One dimensional FEM: Stiffness matrix for beam and bar elements - shape functions for 1D elements.

Two dimensional FEM: Different types of elements for plane stress and plane strain analysis - displacement models - generalized coordinates - shape functions - convergent and compatibility requirements - geometric invariance - natural coordinate system - area and volume coordinates - generation of element stiffness and nodal load matrices

UNIT III

Isoparametric formulation: Concept - different isoparametric elements for 2D analysis - formulation of 4-noded and 8-noded isoparametric quadrilateral elements - Lagrange elements - serendipity elements.

Axi Symmetric Analysis: bodies of revolution - axi symmetric modeling - strain displacement relationship - formulation of axi symmetric elements.

Three dimensional FEM: Different 3-D elements-strain-displacement relationship – formulation of hexahedral and isoparametric solid element.

UNIT IV

Introduction to Finite Element Analysis of Plates: basic theory of plate bending - thin plate theory - stress resultants - Mindlin's approximations - formulation of 4-noded isoperimetric quadrilateral plate element – Shell Element.

UNIT V

Introduction to non – linear analysis – basic methods – application to Special structures.

REFERENCES:

1. Concepts and Applications of Finite Element Analysis by Robert D.Cook, David S. Malkus and Michael E. Plesha, John Wiley & Sons.
2. Finite element Methods by OC Zienkiewicz.
3. Finite element analysis, theory and programming by GS Krishna Murthy.
4. Introduction to Finite element Method by Tirupathi Chandra Patila and Belugunudu.
5. Introduction to Finite element Method by JN Reddy.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

STRUCTURAL DYNAMICS

UNIT I:

Theory of vibrations: Introduction - Elements of vibratory system - Degrees of Freedom - Continuous System - Lumped mass idealization - Oscillatory motion - Simple Harmonic motion - Vectorial representation of S.H.M. - Free vibrations of single degree of freedom system - undamped and damped vibrations - critical damping - Logarithmic decrement - Forced vibration of SDOF systems - Harmonic excitation - Dynamic magnification factor – Phase angle – Bandwidth.

UNIT II

Introduction to Structural Dynamics : Fundamental objectives of dynamic analysis -Types of prescribed loading - Methods of discretization - Formulation of equations of motion by different methods – Direct equilibration using Newton's law of motion / D'Alembert's principle, Principle of virtual work and Hamilton principle.

Single Degree of Freedom Systems : Formulation and solution of the equation of motion - Free vibration response - Response to Harmonic, Periodic, Impulsive and general dynamic loadings - Duhamel integral.

UNIT III

Multi Degree of Freedom Systems : Selection of the degrees of Freedom - Evaluation of structural property matrices - Formulation of the MDOF equations of motion -Undamped free vibrations - Solutions of Eigen value problem for natural frequencies and mode shapes - Analysis of Dynamic response – Normal co-ordinates - Uncoupled equations of motion - Orthogonal properties of normal modes - Mode superposition procedure.

UNIT IV

Practical Vibration Analysis: Introduction - Stodola method - Fundamental mode analysis - Analysis of second and higher modes - Holzer method - Basic procedure.

Continuous Systems: Introduction - Flexural vibrations of beams - Elementary case – Derivation of governing differential equation of motion - Analysis of undamped free vibrations of beams in flexure - Natural frequencies and mode-shapes of simple beams with different end conditions - Principles of application to continuous beams.

UNIT V

Introduction to Earthquake Analysis: Introduction - Excitation by rigid base translation - Lumped mass approach - SDOF and MDOF systems - I. S. Code methods of analysis for obtaining response of multi storeyed buildings.

REFERENCES:

1. Dynamics of Structures by Clough & Penzien, McGraw Hill, New york
2. Structural Dynamics by Mario Paz, C.B.S Publishers, New Delhi.
3. Dynamics of Structures by Anil K. Chopra, Pearson Education (Singapore), Delhi.
4. I.S: 1893 - 1984, "Code of practice for Earthquake resistant design of Structures" and latest I.S: 1893 - 2002 (version) Part-1

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

ANALYSIS AND DESIGN OF SHELLS AND FOLDED PLATES**UNIT I**

Shells – functional behaviour – examples – structural behaviour of shells classification of shells – Definitions – various methods of analysis of shells – merits and demerits of each method – 2D. Membrane equation. Equations of equilibrium: Derivation of stress resultants – cylindrical shells – Flugges simulations equations.

UNIT II

Derivation of the governing DKJ equation for bending theory, - Schorer's theory - Application to the analysis and design of short and long shells.

Beam theory of cylindrical shells: Beam and arch action, Analysis using beam theory.

UNIT III

Introduction to the shells of Double curvatures: Geometry, analysis and design of elliptic paraboloid, conoid and hyperbolic parabolic shapes, inverted umbrella type.

UNIT IV

Axi- Symmetrical shells: General equation - Analysis and axi-symmetrical by membrane theory. Application to spherical shells and hyperboloid of revolution cooling towers.

UNIT V

Folded plates – Introduction – Types of folded plates – structural behaviour of folded plates – advantages – Assumptions Whitney method of analysis – Edge shear equation - Analysis of folded plates of Whitney's method.

Simpsons method of Analysis of folded plates – moment and stress distribution – no rotation and rotation solutions – continuous folded plates – pre stressed continuous folded plates.

TEXT BOOKS:

1. Analysis and design of concrete shell roofs By G.S.Ramaswami.
2. Design of concrete shell roofs By Chaterjee.

REFERENCES:

- 1 Design of concrete shell roofs By Billington
- 2 Shell Analysis By N.K.Bairagi.
- 3 Advanced R.C Design By Dr.N.Krishna Raju.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

ADVANCED STEEL DESIGN

UNIT-I:

Simple Connections –Riveted, Bolted Pinned and Welded Connections: Riveted connections-Bolted Connections- Load Transfer Mechanism – Failure of Bolted Joints – Specifications for Bolted Joints – Bearing – Type Connections – Tensile Strength of Plate – Strength and Efficiency of the Joint – Combined Shear and Tension – Slip – Critical Connections – Praying Action – Combined Shear and Tension for Slip-Critical Connections. Design of Groove welds- Design of Fillet Welds- Design of Intermittent fillet welds- Failure of Welds.

UNIT –II

Eccentric and Moment Connections: Introduction – Beams – Column Connections- Connections Subjected to Eccentric Shear – Bolted Framed Connections- Bolted Seat Connections – Bolted Brackete Connections. Bolted Moment Connections – Welded Framed Connections – Welded Brackete Connections - Moment Resistant Connections.

UNIT III

Analysis and Design of Industrial Buildings : Dead loads, live loads and wind loads on roofs. Design wind speed and pressure, wind pressure on roofs; wind effect on cladding and louvers; Design of angular roof truss, tubular truss, truss for a railway platform. Design of purlins for roofs, design of built up purlins, design of knee braced trusses and stanchions. Design of bracings.

UNIT IV :

Design of Steel Truss Girder Bridges : Types of truss bridges, component parts of a truss bridge, economic proportions of trusses, self weight of truss girders, design of bridge compression members, tension members; wind load on truss girder bridges; wind effect on top lateral bracing; bottom lateral bracing; portal Bracing; sway bracing.

UNIT V :

Design of Steel Bunkers and Soils : Introduction – Janseen's Theory – Airy's Theory – Design of Parameters – Design Criteria – Analysis of Bins – Hopper Bottom –Design of Bins.

REFERENCES:

1. Design of Steel Structures. P. Dayaratnam, Publisher : S. Chand, Edition 2011 – 12.
2. Design Steel Structures Volume – II, Dr. Ramachandra & Vivendra Gehlot Scientitic Publishes Journals Department.
3. Limit State Design of Steel Structures S.K. Duggal Mc Graw Hill Education Private Ltd. New Delhi.
4. Design of Steel Structures Galyord & Gaylord, Publisher ; Tata Mc Graw Hill, Education. Edition 2012.
5. Indian Standard Code – IS – 800-2007.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

PRE-STRESSED CONCRETE

(ELECTIVE -III)

UNIT I.

General Principles of Prestressed Concrete : Pre-tensioning and post – tensioning – Prestressing by straight, concentric, eccentric, bent and parabolic tendons – Different methods and systems of prestressing like Hoyer system, Freyssinet system, Magnel Blaton system – Lee-Mc call system.

Losses of Prestress : Loss of prestress in pre-tensioned and post-tensioned members due to various causes like elastic shortening of concrete, shrinkage of concrete, creep of concrete, relaxation of steel, slip in anchorage, bending of member and frictional loss – Analysis of sections for flexure.

UNIT II.

Design of Section for Flexure : Allowable stresses – Elastic design of simple beams having rectangular and I-section for flexure – kern lines – cable profile and cable layout.

Design of Sections for Shear : Shear and Principal stresses – Improving shear resistance by different prestressing techniques – horizontal, sloping and vertical prestressing – Analysis of rectangular and I-beam – Design of shear reinforcement – Indian code provisions.

UNIT III.

Deflections of Prestressed Concrete Beams : Short term deflections of uncracked members– Prediction of long-time deflections – load – deflection curve for a PSC beam – IS code requirements for max. deflections.

UNIT IV

Transfer of Prestress in Pretensioned Members : Transmission of prestressing force by bond – Transmission length – Flexural bond stresses – IS code provisions – Anchorage zone stresses in post tensioned members – stress distribution in End block – Analysis by approximate, Guyon and Magnel methods – Anchorage zone reinforcement.

UNIT V.

Statically Indeterminate Structures : Advantages & disadvantages of continuous PSC beams – Primary and secondary moments – P and C lines – Linear transformation concordant and non-concordant cable profiles – Analysis of continuous beams and simple portal frames (single bay and single story)

REFERENCES :

1. Prestressed concrete by Krishna Raju, Tata Mc Graw Hill Book – Co., New Delhi.
2. Design of prestress concrete structures by T.Y. Lin and Burn, John Wiley, New York.
3. Prestressed concrete by S. Ramamrutham Dhanpat Rai & Sons, Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

**ADVANCED FOUNDATION ENGINEERING
(ELECTIVE -III)****UNIT – I**

Bearing capacity of Footings subjected to Eccentric and Inclined Loading – Meyrhoﬀ's and Hanse's theories – elastic settlement of Footings embedded in sands and clays of Infinite thickness – Footings on soils of Finite thickness-Schmertamaunn's method, Jaubu and Morgenstern method.

UNIT - II

Pile Foundations – settlement of Pile groups resting in sands and clays – Negative skin friction – in single piles and groups of piles – under – reamed piles – specifications – load – carrying capacity in sands and clays.

UNIT – III

Caissons and well foundations : Types of caissons – well foundation Different shapes of wells – Components of wells – functions and Design – Design Criteria – Sinking of wells – lateral stability by Terzaghi's analysis.

UNIT – IV

Cantilever sheet piles and anchored bulkheads Earth pressure diagram – Determination of Depth of embedment in sands and clays – Timbering of trenches- Earth pressure diagrams – Forces in struts.

UNIT - V

Foundations in Expansive soils – Problems in Expansive soils – Mechanism of swelling – Swell Pressure and Swelling potential – Heave foundation practices – Sand cushion – CNS cushion – under –reamed pile Foundations – Granular pile – anchor technique, stabilization of expansive soils.

REFERENCES:

1. Analysis and Design of Substractenes – Swami Saran.
2. Basic and Applied Soil Mechanics – Gopal Ranjan and A.S.R.Rao.
3. Soil Mechanics & Foundation Engineering, Foundation Engineering – II V.N.S. Murthy.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – II Sem. (Structural Engg.)****COMPUTER AIDED DESIGN IN STRUCTURAL ENGINEERING****(ELECTIVE -III)****UNIT I**

Introduction to computer aided design-An over view-computer as a design medium hardware components of a computer -programming languages.

C - Programming language-Introduction-An over view of programming in C-variables and data types-Declaration of variables-Initialization of variables-operators-arithmetic operators- precedence and associability-Input and output-Character I/O-Formatted output. Print f ()-Formatted input scan f ()-Examples.

UNIT II

C Programming Language-Control structures-If statement-Switch statement-loops-nested loops-while and for ,Do-While-continue statement-Go to statement-Examples.

C Programming Language-Arrays-One dimensional Arrays-Two Dimensional Arrays-pointer operators-pointer arithmetic-pointers and arrays-Matrix manipulations using arrays and pointers-pointers to functions-data files-basic operations-reading and writing and file accessing files-examples.

UNIT III

Computer Graphics-introduction-applications graphic devices-display devices-output and input devices-two dimensional geometric transformations-homogeneous co-ordinates-world co-ordinates-device co-ordinates-window to view port-transformations-clipping operations.

UNIT IV

Data base management system-introduction-data base systems-hardware-software-users-operational data independence-architecture of data base system-distributed databases.

UNIT V

Knowledge based expert system-introduction-artificial intelligence-components of an expert system-stages in expert system development-knowledge representation-inference mechanisms-applications.

REFERENCES

1. Computer Aided Design by C.S.Krishnamoorthy and S.Rajeev.
2. Computational Structures by S.Rajasekharan.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

**PRINCIPLES OF BRIDGE ENGINEERING
(ELECTIVE-IV)****UNIT I :**

Introduction-Types of Bridges-Economic span length-Types of loading-Dead load-live load-Impact Effect-Centrifugal force-wind loads-Lateral loads-Longitudinal forces-Seismic loads- Frictional resistance of expansion bearings-Secondary Stresses-Temperature Effect-Erection Forces and effects-Width of roadway and footway-General Design Requirements.

UNIT II :

Solid Slab Bridges: Introduction-Method of Analysis and Design.

UNIT III :

Girder Bridges: Introduction-Method of Analysis and Design-Courbon's Theory, Grillage analogy

UNIT IV :

Pre-Stressed Concrete Bridges: Basic principles-General Design requirements-Mild steel reinforcement in prestressed concrete member-Concrete cover and spacing of pre-stressing steel-Slender beams-Composite Section-Propped-Design of Propped Composite Section-Unpropped composite section-Two-stage Prestressing-Shrinking stresses-General Design requirements for Road Bridges.

UNIT V :

Analysis of Bridge Decks: Harmonic analysis and folded plate theory-Grillage analogy- Finite strip method and FEM. Sub-structure of bridges: Substructure- Bed of block-Piers- Pier Dimensions- Design loads for piers- Abutments- Design loads for Abutments.

REFERENCES

1. Design of Concrete Bridges by M.G.Aswani, V.N.Vazirani and M.M.Ratwani.
2. Bridge Deck Behaviour by E.C.Hambly.
3. Concrete Bridge Design and Practice by V.K.Raina.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

**EARTHQUAKE RESISTANT DESIGN OF BUILDINGS
(ELECTIVE -IV)****UNIT - I**

Engineering Seismology: Earthquake phenomenon cause of earthquakes-Faults- Plate tectonics- Seismic waves- Terms associated with earthquakes-Magnitude/Intensity of an earthquake-scales-Energy released- Earthquake measuring instruments-Seismoscope, Seismograph, accelerograph-Characteristics of strong ground motions- Seismic zones of India.

UNIT - II

Conceptual design: Introduction-Functional planning-Continuous load path-Overall form-simplicity and symmetry-elongated shapes-stiffness and strength-Horizontal and Vertical members-Twisting of buildings-Ductility-definition-ductility relationships-flexible buildings-framing systems-choice of construction materials-unconfined concrete-confined concrete-masonry-reinforcing steel. Introduction to earthquake resistant design: Seismic design requirements-regular and irregular configurations-basic assumptions-design earthquake loads-basic load combinations-permissible stresses-seismic methods of analysis-factors in seismic analysis-equivalent lateral force method-dynamic analysis-response spectrum method-Time history method.

UNIT - III

Reinforced Concrete Buildings: Principles of earthquake resistant design of RC members- Structural models for frame buildings- Seismic methods of analysis- Seismic design methods- IS code based methods for seismic design- Seismic evaluation and retrofitting- Vertical irregularities- Plan configuration problems- Lateral load resisting systems- Determination of design lateral forces- Equivalent lateral force procedure- Lateral distribution of base shear. Masonry Buildings: Introduction- Elastic properties of masonry assemblage- Categories of masonry buildings- Behaviour of unreinforced and reinforced masonry walls- Behaviour of walls- Box action and bands- Behaviour of infill walls- Improving seismic behaviour of masonry buildings- Load combinations and permissible stresses- Seismic design requirements- Lateral load analysis of masonry buildings.

UNIT - IV

Structural Walls and Non-Structural Elements: Strategies in the location of structural walls- sectional shapes- variations in elevation- cantilever walls without openings – Failure mechanism of non-structures- Effects of non-structural elements on structural system- Analysis of non-structural elements- Prevention of non-structural damage- Isolation of non-structures.

UNIT - V

Ductility Considerations in Earthquake Resistant Design of RC Buildings: Introduction- Impact of Ductility- Requirements for Ductility- Assessment of Ductility- Factors affecting Ductility- Ductile detailing considerations as per IS 13920. Behaviour of beams, columns and joints in RC buildings during earthquakes- Vulnerability of open ground storey and short columns during earthquakes.

Capacity Based Design: Introduction to Capacity Design, Capacity Design for Beams and Columns-Case studies.

REFERENCE BOOKS:

1. Earthquake Resistant Design of structures – S. K. Duggal, Oxford University Press.
2. Earthquake Resistant Design of structures – Pankaj Agarwal and Manish Shrikhande, Prentice Hall of India Pvt. Ltd.

3. Seismic Design of Reinforced Concrete and Masonry Building – T. Paulay and M.J.N. Priestly, John Wiley & Sons.
4. Masonry and Timber structures including earthquake Resistant Design –Anand S.Arya, Nem chand & Bros.
5. Earthquake –Resistant Design of Masonry Building –Miha Tomazevic, Imperial college Press.
6. Earthquake Tips – Learning Earthquake Design and Construction C.V.R. Murty.

REFERENCE CODES:

1. IS: 1893 (Part-1) -2002. "Criteria for Earthquake Resistant – Design of structures." B.I.S., New Delhi.
2. IS:4326-1993, " Earthquake Resistant Design and Construction of Building", Code of Practice B.I.S., New Delhi.
3. IS:13920-1993, " Ductile detailing of concrete structures subjected to seismic force" – Guidelines, B.I.S., New Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – II Sem. (Structural Engg.)****PLASTIC ANALYSIS AND DESIGN****(ELECTIVE -IV)****UNIT – I**

Analysis of Structures for Ultimate Load: Fundamental Principles – statical method of Analysis – Mechanism method of analysis – Method of analysis, Moment check – Carry over factor – Moment Balancing Method.

UNIT - II

Design of Continuous Beams: Continuous Beams of uniform section throughout – Continuous Beams with different cross-sections.

UNIT - III

Secondary Design Problems: Introduction – Influence of Axial force on the plastic moment – influence of shear force – local buckling of flanges and webs – lateral buckling – column stability.

UNIT - IV

Design of Connections: Introduction – requirement for connections – straight corner connections – Haunched connection – Interior Beam-Column connections.

UNIT - V

Design of Steel Frames: Introduction – Single span frames – simplified procedures for Single span frames – Design of Gable frames with Haunched Connection. Ultimate Deflections: Introduction – Deflection at ultimate load – Deflection at working load – Deflections of Beams and Single span frames.

REFERENCES:

1. Plastic Design of Steel Frames, L.S.Beedle.
2. Plastic Analysis, B.G.Neal.
3. Plastic Analysis, Horve.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

STABILITY OF STRUCTURES**(ELECTIVE -IV)****UNIT – I**

Beam Columns: Differential equations for beam columns- beam columns with concentrated loads – continuous lateral loads-couples- beam columns with built in ends – continuous beams with axial load – application of trigonometrically series – Effects of initial curvature on deflections – Determination of allowable stresses.

UNIT - II

Elastic Buckling of bars and frames: Elastic Buckling of straight columns – Effect of shear stress on buckling – Eccentrically and laterally loaded columns- Buckling of frames-large deflections of buckled bars- Energy methods- Buckling of bars on elastic foundations- Buckle line of bar with intermediate compressive forces - Buckling of bars with change in cross-section – Effect of shear force on critical load- built up columns.

UNIT - III

In Elastic Buckling: Buckle line of straight bar- Double modulus theory – Tangent modulus theory, Inelastic lateral Buckling. Experiments and design formulae: Experiments on columns – Critical stress diagram – Empirical formulae for design – various end conditions

UNIT - IV

Torsion Buckling: Pure torsion of thin walled bars of open cross section – Non-uniform torsion of thin walled bars of open cross section- Torsional buckling – Buckling by torsion and flexure.

UNIT – V

Lateral buckling of simply supported Beams: Beams of Rectangular cross-section subjected to pure bending. Buckling of simply supported Rectangular plates: Derivation of equation of plate subjected to constant compression in one and two directions.

REFERENCES

1. Theory of elastic Stability by Timshenko & Gere-Mc Graw Hill.
2. Stability of metallic structures by Blunch- Mc Graw Hill.
3. Theory of Beam- Columns Vol I by Chem. & Atste Mc. Graw Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (Structural Engg.)

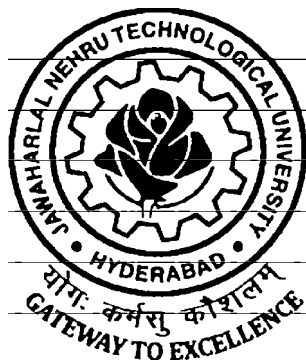
CAD LAB

1. Program using arrays and functions for matrix manipulation.
2. Programs to draw bending moment and shear force diagrams. Using graphic in C.
3. Program for design of slabs. Using Excel.
4. Program for design of beams. Using Excel.
5. Program for design of column and footing using Excel.
6. Analysis of truss using STAAD Pro.
7. Analysis of multistoreyed space frame, using STAAD Pro.
8. Analysis of Bridge deck slab.

ACADEMIC REGULATIONS COURSE STRUCTURE AND DETAILED SYLLABUS

M.TECH VLSI/ VLSI DESIGN/ VLSI SYSTEM DESIGN

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/ School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory
- The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.
- If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/ year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - VLSI/ VLSI DESIGN/VLSI SYSTEM DESIGN

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		VLSI Technology and Design	3	-	3
		CMOS Analog Integrated Circuit Design	3	-	3
		CPLD and FPGA Architectures and Applications	3	-	3
		CMOS Digital Integrated Circuit Design	3	-	3
	Elective -I	Digital System Design Hardware Software Co-Design Device Modeling	3	-	3
	Elective -II	Advanced Operating Systems Micro Controllers for Embedded System Design Advanced Computer Architecture	3	-	3
	Lab	VLSI Laboratory – I	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Low Power VLSI Design	3	-	3
		CAD for VLSI Circuits	3	-	3
		CMOS Mixed Signal Circuit Design	3	-	3
		Design for Testability	3	-	3
	Elective - III	Scripting Languages Digital Signal Processors and Architectures VLSI Signal Processing	3	-	3
	Elective – IV	Optimization Techniques in VLSI Design System On Chip Architecture Semiconductor Memory Design and Testing	3	-	3
	Lab	VLSI Laboratory – II	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	0	3	2
		Project work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI TECHNOLOGY AND DESIGN

UNIT –I:

Review of Microelectronics and Introduction to MOS Technologies:

MOS, CMOS, BiCMOS Technology.

Basic Electrical Properties of MOS, CMOS & BiCMOS Circuits: $I_{ds} - V_{ds}$ relationships, Threshold Voltage V_T , G_m , G_{ds} and ω_o , Pass Transistor, MOS, CMOS & Bi CMOS Inverters, Z_{pu}/Z_{pd} , MOS Transistor circuit model, Latch-up in CMOS circuits.

UNIT –II:

Layout Design and Tools:

Transistor structures, Wires and Vias, Scalable Design rules, Layout Design tools.

Logic Gates & Layouts:

Static Complementary Gates, Switch Logic, Alternative Gate circuits, Low power gates, Resistive and Inductive interconnect delays.

UNIT –III:

Combinational Logic Networks:

Layouts, Simulation, Network delay, Interconnect design, Power optimization, Switch logic networks, Gate and Network testing.

UNIT –IV:

Sequential Systems:

Memory cells and Arrays, Clocking disciplines, Design, Power optimization, Design validation and testing.

UNIT –V:

Floor Planning:

Floor planning methods, Global Interconnect, Floor Plan Design, Off-chip connections.

TEXT BOOKS:

1. Essentials of VLSI Circuits and Systems, K. Eshraghian Eshraghian. D, A. Pucknell, 2005, PHI.
2. Modern VLSI Design – Wayne Wolf, 3rd Ed., 1997, Pearson Education.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011.
2. Principals of CMOS VLSI Design – N.H.E Weste, K. Eshraghian, 2nd Ed., Addison Wesley.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSi System Design)

CMOS ANALOG INTEGRATED CIRCUIT DESIGN

UNIT -I:

MOS Devices and Modeling:

The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

UNIT -II:

Analog CMOS Sub-Circuits:

MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

UNIT -III:

CMOS Amplifiers:

Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

UNIT -IV:

CMOS Operational Amplifiers:

Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.

UNIT -V:

Comparators:

Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

TEXT BOOKS:

1. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.
2. Analysis and Design of Analog Integrated Circuits- Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, Wiley India, Fifth Edition, 2010.

REFERENCE BOOKS:

1. Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edn, 2013.
2. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition.
3. CMOS: Circuit Design, Layout and Simulation- Baker, Li and Boyce, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSi System Design)****CPLD AND FPGA ARCHITECTURES AND APPLICATIONS****UNIT-I:****Introduction to Programmable Logic Devices:**

Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation.

UNIT-II:**Field Programmable Gate Arrays:**

Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, Applications of FPGAs.

UNIT -III:**SRAM Programmable FPGAs:**

Introduction, Programming Technology, Device Architecture, The Xilinx XC2000, XC3000 and XC4000 Architectures.

UNIT -IV:**Anti-Fuse Programmed FPGAs:**

Introduction, Programming Technology, Device Architecture, The Actel ACT1, ACT2 and ACT3 Architectures.

UNIT -V:**Design Applications:**

General Design Issues, Counter Examples, A Fast Video Controller, A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.

TEXT BOOKS:

1. Field Programmable Gate Array Technology - Stephen M. Trimberger, Springer International Edition.
2. Digital Systems Design - Charles H. Roth Jr, Lizy Kurian John, Cengage Learning.

REFERENCE BOOKS:

1. Field Programmable Gate Arrays - John V. Oldfield, Richard C. Dorf, Wiley India.
2. Digital Design Using Field Programmable Gate Arrays - Pak K. Chan/Samiha Mourad, Pearson Low Price Edition.
3. Digital Systems Design with FPGAs and CPLDs - Ian Grout, Elsevier, Newnes.
4. FPGA based System Design - Wayne Wolf, Prentice Hall Modern Semiconductor Design Series.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSi System Design)

CMOS DIGITAL INTEGRATED CIRCUIT DESIGN**UNIT –I:****MOS Design:**

Pseudo NMOS Logic – Inverter, Inverter threshold voltage, Output high voltage, Output Low voltage, Gain at gate threshold voltage, Transient response, Rise time, Fall time, Pseudo NMOS logic gates, Transistor equivalency, CMOS Inverter logic.

UNIT –II:**Combinational MOS Logic Circuits:**

MOS logic circuits with NMOS loads, Primitive CMOS logic gates – NOR & NAND gate, Complex Logic circuits design – Realizing Boolean expressions using NMOS gates and CMOS gates, AOI and OIA gates, CMOS full adder, CMOS transmission gates, Designing with Transmission gates.

UNIT –III:**Sequential MOS Logic Circuits:**

Behavior of bistable elements, SR Latch, Clocked latch and flip flop circuits, CMOS D latch and edge triggered flipflop.

UNIT –IV:**Dynamic Logic Circuits:**

Basic principle, Voltage Bootstrapping, Synchronous dynamic pass transistor circuits, Dynamic CMOS transmission gate logic, High performance Dynamic CMOS circuits.

UNIT –V:**Semiconductor Memories:**

Types, RAM array organization, DRAM – Types, Operation, Leakage currents in DRAM cell and refresh operation, SRAM operation Leakage currents in SRAM cells, Flash Memory- NOR flash and NAND flash.

TEXT BOOKS:

1. Digital Integrated Circuit Design – Ken Martin, Oxford University Press, 2011.
2. CMOS Digital Integrated Circuits Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 3rd Ed., 2011.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011
2. Digital Integrated Circuits – A Design Perspective, Jan M. Rabaey, Anantha Chandrakasan, Borivoje Nikolic, 2nd Ed., PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSi System Design)

DIGITAL SYSTEM DESIGN

(ELECTIVE -I)

UNIT -I:

Minimization and Transformation of Sequential Machines:

The Finite State Model – Capabilities and limitations of FSM – State equivalence and machine minimization – Simplification of incompletely specified machines.

Fundamental mode model – Flow table – State reduction – Minimal closed covers – Races, Cycles and Hazards.

UNIT -II:

Digital Design:

Digital Design Using ROMs, PALs and PLAs , BCD Adder, 32 – bit adder, State graphs for control circuits, Scoreboard and Controller, A shift and add multiplier, Array multiplier, Keypad Scanner, Binary divider.

UNIT -III:

SM Charts:

State machine charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, dice game controller.

UNIT -IV:

Fault Modeling & Test Pattern Generation:

Logic Fault model – Fault detection & Redundancy- Fault equivalence and fault location –Fault dominance – Single stuck at fault model – Multiple stuck at fault models –Bridging fault model.

Fault diagnosis of combinational circuits by conventional methods – Path sensitization techniques, Boolean Difference method – Kohavi algorithm – Test algorithms – D algorithm, PODEM, Random testing, Transition count testing, Signature analysis and test bridging faults.

UNIT -V:

Fault Diagnosis in Sequential Circuits:

Circuit Test Approach, Transition Check Approach – State identification and fault detection experiment, Machine identification, Design of fault detection experiment

TEXT BOOKS:

1. Fundamentals of Logic Design – Charles H. Roth, 5th Ed., Cengage Learning.
2. Digital Systems Testing and Testable Design – Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman- John Wiley & Sons Inc.
3. Logic Design Theory – N. N. Biswas, PHI.

REFERENCE BOOKS:

1. Switching and Finite Automata Theory – Z. Kohavi , 2nd Ed., 2001, TMH.
2. Digital Design – Morris Mano, M.D.Ciletti, 4th Edition, PHI.
3. Digital Circuits and Logic Design – Samuel C. Lee , PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSi System Design)****HARDWARE - SOFTWARE CO-DESIGN****(ELECTIVE -I)****UNIT –I:****Co- Design Issues:**

Co- Design Models, Architectures, Languages, A Generic Co-design Methodology.

Co- Synthesis Algorithms:

Hardware software synthesis algorithms: hardware – software partitioning distributed system co-synthesis.

UNIT –II:**Prototyping and Emulation:**

Prototyping and emulation techniques, prototyping and emulation environments, future developments in emulation and prototyping architecture specialization techniques, system communication infrastructure

Target Architectures:

Architecture Specialization techniques, System Communication infrastructure, Target Architecture and Application System classes, Architecture for control dominated systems (8051-Architectures for High performance control), Architecture for Data dominated systems (ADSP21060, TMS320C60), Mixed Systems.

UNIT –III:**Compilation Techniques and Tools for Embedded Processor Architectures:**

Modern embedded architectures, embedded software development needs, compilation technologies, practical consideration in a compiler development environment.

UNIT –IV:**Design Specification and Verification:**

Design, co-design, the co-design computational model, concurrency coordinating concurrent computations, interfacing components, design verification, implementation verification, verification tools, interface verification

UNIT –V:**Languages for System – Level Specification and Design-I:**

System – level specification, design representation for system level synthesis, system level specification languages.

Languages for System – Level Specification and Design-II:

Heterogeneous specifications and multi language co-simulation, the cosyma system and lycos system.

TEXT BOOKS:

1. Hardware / Software Co- Design Principles and Practice – Jorgen Staunstrup, Wayne Wolf – 2009, Springer.
2. Hardware / Software Co- Design - Giovanni De Micheli, Mariagiovanna Sami, 2002, Kluwer Academic Publishers.

REFERENCE BOOK:

1. A Practical Introduction to Hardware/Software Co-design -Patrick R. Schaumont - 2010 – Springer.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

DEVICE MODELING

(ELECTIVE -I)

UNIT -I:

Introduction to Semiconductor Physics:

Review of Quantum Mechanics, Boltzman transport equation, Continuity equation, Poisson equation.

Integrated Passive Devices:

Types and Structures of resistors and capacitors in monolithic technology, Dependence of model parameters on structures

UNIT -II:

Integrated Diodes:

Junction and Schottky diodes in monolithic technologies – Static and Dynamic behavior – Small and large signal models – SPICE models

Integrated Bipolar Transistor:

Types and structures in monolithic technologies – Basic model (Eber-Moll) – Gummel - Poon model- dynamic model, Parasitic effects – SPICE model –Parameter extraction

UNIT -III:

Integrated MOS Transistor:

NMOS and PMOS transistor – Threshold voltage – Threshold voltage equations – MOS device equations – Basic DC equations second order effects – MOS models – small signal AC characteristics – MOS FET SPICE model level 1, 2, 3 and 4

UNIT -IV:

VLSI Fabrication Techniques: An overview of wafer fabrication, Wafer Processing – Oxidation – Patterning – Diffusion – Ion Implantation – Deposition – Silicon gate nMOS process – CMOS processes – n-well- p-well- twin tub- Silicon on insulator – CMOS process enhancements – Interconnects circuit elements

UNIT -V:

Modeling of Hetero Junction Devices: Band gap Engineering, Band gap Offset at abrupt Hetero Junction, Modified current continuity equations, Hetero Junction bipolar transistors (HBTs), SiGe

TEXT BOOKS:

1. Introduction to Semiconductor Materials and Devices – Tyagi M. S, 2008, John Wiley Student Edition.
2. Solid State Circuits – Ben G. Streetman, Prentice Hall, 1997

REFERENCE BOOKS:

1. Physics of Semiconductor Devices – Sze S. M, 2nd Edition, Mcgraw Hill, New York, 1981.
2. Introduction to Device Modeling and Circuit Simulation – Tor A. Fijedly, Wiley-Interscience, 1997.
3. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

ADVANCED OPERATING SYSTEMS**(ELECTIVE -II)****UNIT –I:****Introduction to Operating Systems:**

Overview of computer system hardware, Instruction execution, I/O function, Interrupts, Memory hierarchy, I/O Communication techniques, Operating system objectives and functions, Evaluation of operating System

UNIT –II:**Introduction to UNIX and LINUX:**

Basic Commands & Command Arguments, Standard Input, Output, Input / Output Redirection, Filters and Editors, Shells and Operations

UNIT –III:**System Calls:**

System calls and related file structures, Input / Output, Process creation & termination.

Inter Process Communication:

Introduction, File and record locking, Client – Server example, Pipes, FIFOs, Streams & Messages, Name Spaces, Systems V IPC, Message queues, Semaphores, Shared Memory, Sockets & TLI.

UNIT –IV:**Introduction to Distributed Systems:**

Goals of distributed system, Hardware and software concepts, Design issues.

Communication in Distributed Systems:

Layered protocols, ATM networks, Client - Server model, Remote procedure call and Group communication.

UNIT –V:**Synchronization in Distributed Systems:**

Clock synchronization, Mutual exclusion, E-tech algorithms, Bully algorithm, Ring algorithm, Atomic transactions

Deadlocks:

Dead lock in distributed systems, Distributed dead lock prevention and distributed dead lock detection.

TEXT BOOKS:

1. The Design of the UNIX Operating Systems – Maurice J. Bach, 1986, PHI.
2. Distributed Operating System - Andrew. S. Tanenbaum, 1994, PHI.
3. The Complete Reference LINUX – Richard Peterson, 4th Ed., McGraw – Hill.

REFERENCE BOOKS:

1. Operating Systems: Internal and Design Principles - Stallings, 6th Ed., PE.
2. Modern Operating Systems - Andrew S Tanenbaum, 3rd Ed., PE.
3. Operating System Principles - Abraham Silberchatz, Peter B. Galvin, Greg Gagne, 7th Ed., John Wiley
4. UNIX User Guide – Ritchie & Yates.
5. UNIX Network Programming - W.Richard Stevens, 1998, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

**MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN
(ELECTIVE -II)**

UNIT –I:

ARM Architecture:

ARM Design Philosophy, Registers, Program Status Register, Instruction Pipeline, Interrupts and Vector Table, Architecture Revision, ARM Processor Families.

UNIT –II:

ARM Programming Model – I:

Instruction Set: Data Processing Instructions, Addressing Modes, Branch, Load, Store Instructions, PSR Instructions, Conditional Instructions.

UNIT –III:

ARM Programming Model – II:

Thumb Instruction Set: Register Usage, Other Branch Instructions, Data Processing Instructions, Single-Register and Multi Register Load-Store Instructions, Stack, Software Interrupt Instructions

UNIT –IV:

ARM Programming:

Simple C Programs using Function Calls, Pointers, Structures, Integer and Floating Point Arithmetic, Assembly Code using Instruction Scheduling, Register Allocation, Conditional Execution and Loops.

UNIT –V:

Memory Management:

Cache Architecture, Policies, Flushing and Caches, MMU, Page Tables, Translation, Access Permissions, Context Switch.

TEXT BOOK:

1. ARM Systems Developer's Guides- Designing & Optimizing System Software – Andrew N. Sloss, Dominic Symes, Chris Wright, 2008, Elsevier.

REFERENCE BOOK:

1. Embedded Microcomputer Systems, Real Time Interfacing – Jonathan W. Valvano – Brookes / Cole, 1999, Thomas Learning.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

ADVANCED COMPUTER ARCHITECTURE

(ELECTIVE -II)

UNIT -I:**Fundamentals of Computer Design:**

Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, measuring and reporting performance, Quantitative principles of computer design, Amdahl's law.

Instruction set principles and examples- Introduction, classifying instruction set- memory addressing- type and size of operands, Operations in the instruction set.

UNIT –II:**Pipelines:**

Introduction, basic RISC instruction set, Simple implementation of RISC instruction set, Classic five stage pipe lined RISC processor, Basic performance issues in pipelining, Pipeline hazards, Reducing pipeline branch penalties.

Memory Hierarchy Design:

Introduction, review of ABC of cache, Cache performance, Reducing cache miss penalty, Virtual memory.

UNIT -III:**Instruction Level Parallelism (ILP) - The Hardware Approach:**

Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, High performance instruction delivery- Hardware based speculation.

ILP Software Approach:

Basic compiler level techniques, Static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues - Hardware verses Software.

UNIT –IV:**Multi Processors and Thread Level Parallelism:**

Multi Processors and Thread level Parallelism- Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – Memory architecture, Synchronization.

UNIT –V:**Inter Connection and Networks:**

Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.

Intel Architecture: Intel IA-64 ILP in embedded and mobile markets Fallacies and pit falls.

TEXT BOOK:

1. John L. Hennessy, David A. Patterson - Computer Architecture: A Quantitative Approach, 3rd Edition, an Imprint of Elsevier.

REFERENCE BOOKS:

1. John P. Shen and Miikko H. Lipasti -, Modern Processor Design : Fundamentals of Super Scalar Processors
2. Computer Architecture and Parallel Processing - Kai Hwang, Faye A.Brigs., MC Graw Hill.
3. Advanced Computer Architecture - A Design Space Approach, Dezso Sima, Terence Fountain, Peter Kacsuk, Pearson Ed.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI LABORATORY – I

Note: All the following digital circuits are to be designed and implemented using Cadence / Mentor Graphics / Synopsys / Equivalent CAD tools.

VLSI Front End Design programs:

Programming can be done using any HDL compiler, Verification of the Functionality of the module using functional Simulator, Timing Simulation for Critical Path time Calculation, Synthesis of module, Place & Route and implementation of design using FPGA/CPLD Devices.

1. HDL code to realize all the logic gates
2. Design and Simulation of Half and Full adders, Serial Binary Adder, Multi Precision Adder, Carry Look Ahead Adder.
3. Design of 2-to-4 decoder
4. Design of 8-to-3 encoder (without and with priority)
5. Design of 8-to-1 multiplexer and 1x8 Demultiplexer
6. Design of 4 bit binary to gray code converter
7. Design of 4-bit comparator
8. Design of flip flops: SR, D, JK, T
9. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset).
10. Design of a N- bit shift register of Serial- in Serial –out, Serial in parallel out, Parallel in Serial out and Parallel in Parallel Out.
11. Design of Sequence Detector (Finite State Machine- Mealy and Moore Machines).
12. Design of 4- Bit Multiplier and 4-bit Divider.
13. Design of ALU to Perform –ADD, SUB, AND, OR, 1's compliment, 2's Compliment, Multiplication and Division.
14. Design of Finite State Machine.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

LOW POWER VLSI DESIGN**UNIT –I:****Fundamentals:**

Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects – Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.

UNIT –II:**Low-Power Design Approaches:**

Low-Power Design through Voltage Scaling – VTCMOS circuits, MTCMOS circuits, Architectural Level Approach – Pipelining and Parallel Processing Approaches.

Switched Capacitance Minimization Approaches:

System Level Measures, Circuit Level Measures, Mask level Measures.

UNIT –III:**Low-Voltage Low-Power Adders:**

Introduction, Standard Adder Cells, CMOS Adder's Architectures – Ripple Carry Adders, Carry Look-Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques – Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

UNIT –IV:**Low-Voltage Low-Power Multipliers:**

Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh-Wooley Multiplier, Booth Multiplier, Introduction to Wallace Tree Multiplier.

UNIT –V:**Low-Voltage Low-Power Memories:**

Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Precharge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

TEXT BOOKS:

1. CMOS Digital Integrated Circuits – Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 2011.
2. Low-Voltage, Low-Power VLSI Subsystems – Kiat-Seng Yeo, Kaushik Roy, TMH Professional Engineering.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011
2. Low Power CMOS Design – AnanthaChandrakasan, IEEE Press/Wiley International, 1998.
3. Low Power CMOS VLSI Circuit Design – Kaushik Roy, Sharat C. Prasad, John Wiley & Sons, 2000.
4. Practical Low Power Digital VLSI Design – Gary K. Yeap, Kluwer Academic Press, 2002.
5. Low Power CMOS VLSI Circuit Design – A. Bellamour, M. I. Elamasri, Kluwer Academic Press, 1995.
6. Leakage in Nanometer CMOS Technologies – Siva G. Narendran, AnathaChandrakasan, Springer, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

CAD FOR VLSI CIRCUITS

UNIT -I:

VLSI Physical Design Automation:

VLSI Design Cycle, New Trends in VLSI Design Cycle, Physical Design Cycle, New Trends in Physical Design Cycle, Design Styles, System Packaging Styles;

UNIT -II:

Partitioning, Floor Planning, Pin Assignment and Placement:

Partitioning – Problem formulation, Classification of Partitioning algorithms, Kernighan-Lin Algorithm, Simulated Annealing, Floor Planning – Problem formulation, Classification of floor planning algorithms, constraint based floor planning, Rectangular Dualization, Pin Assignment – Problem formulation, Classification of pin assignment algorithms, General and channel Pin assignments, Placement – Problem formulation, Classification of placement algorithms, Partitioning based placement algorithms;

UNIT -III:

Global Routing and Detailed Routing:

Global Routing – Problem formulation, Classification of global routing algorithms, Maze routing algorithms, Detailed Routing – Problem formulation, Classification of routing algorithms, Single layer routing algorithms;

UNIT -IV:

Physical Design Automation of FPGAs:

FPGA Technologies, Physical Design cycle for FPGAs, Partitioning, Routing – Routing Algorithm for the Non-Segmented model, Routing Algorithms for the Segmented Model;

Physical Design Automation of MCMs:

Introduction to MCM Technologies, MCM Physical Design Cycle.

UNIT -V:

Chip Input and Output Circuits:

ESD Protection, Input Circuits, Output Circuits and $L \left(\frac{di}{dt} \right)$ noise, On-chip clock Generation and Distribution, Latch-up and its prevention.

TEXT BOOKS:

1. Algorithms for VLSI Physical Design Automation by Naveed Shervani, 3rd Edition, 2005, Springer International Edition.
2. CMOS Digital Integrated Circuits Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 3rd Ed., 2011.

REFERENCE BOOKS:

1. VLSI Physical Design Automation-Theory and Practice by Sadiq M Sait, Habib Youssef, World Scientific.
2. Algorithms for VLSI Design Automation, S. H. Gerez, 1999, Wiley student Edition, John Wiley and Sons (Asia) Pvt. Ltd.
3. VLSI Physical Design Automation by Sung Kyu Lim, Springer International Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

CMOS MIXED SIGNAL CIRCUIT DESIGN**UNIT -I:****Switched Capacitor Circuits:**

Introduction to Switched Capacitor circuits- basic building blocks, Operation and Analysis, Non-ideal effects in switched capacitor circuits, Switched capacitor integrators first order filters, Switch sharing, biquad filters.

UNIT -II:**Phased Lock Loop (PLL):**

Basic PLL topology, Dynamics of simple PLL, Charge pump PLLs-Lock acquisition, Phase/Frequency detector and charge pump, Basic charge pump PLL, Non-ideal effects in PLLs-PFD/CP non-idealities, Jitter in PLLs, Delay locked loops, applications

UNIT -III:**Data Converter Fundamentals:**

DC and dynamic specifications, Quantization noise, Nyquist rate D/A converters- Decoder based converters, Binary-Scaled converters, Thermometer-code converters, Hybrid converters

UNIT -IV:**Nyquist Rate A/D Converters:**

Successive approximation converters, Flash converter, Two-step A/D converters, Interpolating A/D converters, Folding A/D converters, Pipelined A/D converters, Time-interleaved converters.

UNIT -V:**Oversampling Converters:**

Noise shaping modulators, Decimating filters and interpolating filters, Higher order modulators, Delta sigma modulators with multibit quantizers, Delta sigma D/A

TEXT BOOKS:

1. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition, 2002
2. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.
3. Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edition, 2013

REFERENCE BOOKS:

1. CMOS Integrated Analog-to- Digital and Digital-to-Analog converters-Rudy Van De Plassche, Kluwer Academic Publishers, 2003
2. Understanding Delta-Sigma Data converters-Richard Schreier, Wiley Interscience, 2005.
3. CMOS Mixed-Signal Circuit Design - R. Jacob Baker, Wiley Interscience, 2009.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

DESIGN FOR TESTABILITY**UNIT -I:****Introduction to Testing:**

Testing Philosophy, Role of Testing, Digital and Analog VLSI Testing, VLSI Technology Trends affecting Testing, Types of Testing, Fault Modeling: Defects, Errors and Faults, Functional Versus Structural Testing, Levels of Fault Models, Single Stuck-at Fault.

UNIT -II:**Logic and Fault Simulation:**

Simulation for Design Verification and Test Evaluation, Modeling Circuits for Simulation, Algorithms for True-value Simulation, Algorithms for Fault Simulation, ATPG.

UNIT -III:**Testability Measures:**

SCOAP Controllability and Observability, High Level Testability Measures, Digital DFT and Scan Design: Ad-Hoc DFT Methods, Scan Design, Partial-Scan Design, Variations of Scan.

UNIT -IV:**Built-In Self-Test:**

The Economic Case for BIST, Random Logic BIST: Definitions, BIST Process, Pattern Generation, Response Compaction, Built-In Logic Block Observers, Test-Per-Clock, Test-Per-Scan BIST Systems, Circular Self Test Path System, Memory BIST, Delay Fault BIST.

UNIT -V:**Boundary Scan Standard:**

Motivation, System Configuration with Boundary Scan: TAP Controller and Port, Boundary Scan Test Instructions, Pin Constraints of the Standard, Boundary Scan Description Language: BSDL Description Components, Pin Descriptions.

TEXT BOOK:

1. Essentials of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits - M.L. Bushnell, V. D. Agrawal, Kluwer Academic Publishers.

REFERENCE BOOKS:

1. Digital Systems and Testable Design - M. Abramovici, M.A. Breuer and A.D Friedman, Jaico Publishing House.
2. Digital Circuits Testing and Testability - P.K. Lala, Academic Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

SCRIPTING LANGUAGES

(ELECTIVE -III)

UNIT -I:**Introduction to Scripts and Scripting:**

Characteristics and uses of scripting languages, Introduction to PERL, Names and values, Variables and assignment, Scalar expressions, Control structures, Built-in functions, Collections of Data, Working with arrays, Lists and hashes, Simple input and output, Strings, Patterns and regular expressions, Subroutines, Scripts with arguments.

UNIT -II:**Advanced PERL:**

Finer points of Looping, Subroutines, Using Pack and Unpack, Working with files, Navigating the file system, Type globs, Eval, References, Data structures, Packages, Libraries and modules, Objects, Objects and modules in action, Tied variables, Interfacing to the operating systems, Security issues.

UNIT -III:**TCL:**

The TCL phenomena, Philosophy, Structure, Syntax, Parser, Variables and data in TCL, Control flow, Data structures, Simple input/output, Procedures, Working with Strings, Patterns, Files and Pipes, Example code.

UNIT -IV:**Advanced TCL:**

The eval, source, exec and up-level commands, Libraries and packages, Namespaces, Trapping errors, Event-driven programs, Making applications 'Internet-aware', 'Nuts-and-bolts' internet programming, Security issues, running untrusted code, The C interface.

UNIT -V:**TK and JavaScript:**

Visual tool kits, Fundamental concepts of TK, TK by example, Events and bindings, Geometry managers, PERL-TK.

JavaScript – Object models, Design Philosophy, Versions of JavaScript, The Java Script core language, Basic concepts of Pythan.

Object Oriented Programming Concepts (Qualitative Concepts Only): Objects, Classes, Encapsulation, Data Hierarchy.

TEXT BOOKS:

1. The World of Scripting Languages- David Barron, Wiley Student Edition, 2010.
2. Practical Programming in Tcl and Tk - Brent Welch, Ken Jones and Jeff Hobbs., Fourth edition.
3. Java the Complete Reference - Herbert Schildt, 7th Edition, TMH.

REFERENCE BOOKS:

1. Tcl/Tk: A Developer's Guide- Clif Flynt, 2003, Morgan Kaufmann Series.
2. Tcl and the Tk Toolkit- John Ousterhout, 2nd Edition, 2009, Kindel Edition.
3. Tcl 8.5 Network Programming book- Wojciech Kocjan and Piotr Beltowski, Packt Publishing.
4. Tcl/Tk 8.5 Programming Cookbook- Bert Wheeler.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES**(ELECTIVE -III)****UNIT –I:****Introduction to Digital Signal Processing:**

Introduction, a Digital signal-processing system, the sampling process, discrete time sequences. Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear time-invariant systems, Digital filters, Decimation and interpolation.

Computational Accuracy in DSP Implementations:

Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter.

UNIT –II:**Architectures for Programmable DSP Devices:**

Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation UNIT, Programmability and Program Execution, Speed Issues, Features for External interfacing.

UNIT -III:**Programmable Digital Signal Processors:**

Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX Instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX Processors, Pipeline Operation of TMS320C54XX Processors.

UNIT –IV:**Analog Devices Family of DSP Devices:**

Analog Devices Family of DSP Devices – ALU and MAC block diagram, Shifter Instruction, Base Architecture of ADSP 2100, ADSP-2181 high performance Processor.

Introduction to Black fin Processor - The Black fin Processor, Introduction to Micro Signal Architecture, Overview of Hardware Processing Units and Register files, Address Arithmetic Unit, Control Unit, Bus Architecture and Memory, Basic Peripherals.

UNIT –V:**Interfacing Memory and I/O Peripherals to Programmable DSP Devices:**

Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA).

TEXT BOOKS:

1. Digital Signal Processing – Avtar Singh and S. Srinivasan, Thomson Publications, 2004.
2. A Practical Approach To Digital Signal Processing - K Padmanabhan, R. Vijayarajeswaran, Ananthi. S, New Age International, 2006/2009.
3. Embedded Signal Processing with the Micro Signal Architecture: Woon-Seng Gan, Sen M. Kuo, Wiley-IEEE Press, 2007.

REFERENCE BOOKS:

1. Digital Signal Processors, Architecture, Programming and Applications – B. Venkataramani and M. Bhaskar, 2002, TMH.
2. Digital Signal Processing – Jonatham Stein, 2005, John Wiley.
3. DSP Processor Fundamentals, Architectures & Features – Lapsley et al. 2000, S. Chand & Co.
4. Digital Signal Processing Applications Using the ADSP-2100 Family by The Applications Engineering Staff of Analog Devices, DSP Division, Edited by Amy Mar, PHI.
5. The Scientist and Engineer's Guide to Digital Signal Processing by Steven W. Smith, Ph.D., California Technical Publishing, ISBN 0-9660176-3-3, 1997.
6. Embedded Media Processing by David J. Katz and Rick Gentile of Analog Devices, Newnes , ISBN 0750679123, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI SIGNAL PROCESSING

(ELECTIVE -III)

UNIT -I:**Introduction to DSP:**

Typical DSP algorithms, DSP algorithms benefits, Representation of DSP algorithms.

Pipelining and Parallel Processing:

Introduction, Pipelining of FIR Digital filters, Parallel Processing, Pipelining and Parallel Processing for Low Power.

Retiming:

Introduction – Definitions and Properties – Solving System of Inequalities – Retiming Techniques.

UNIT –II:**Folding and Unfolding:**

Folding: Introduction -Folding Transform - Register minimization Techniques – Register minimization in folded architectures – folding of multirate systems.

Unfolding: Introduction – An Algorithm for Unfolding – Properties of Unfolding – critical Path, Unfolding and Retiming – Applications of Unfolding.

UNIT -III:**Systolic Architecture Design:**

Introduction – Systolic Array Design Methodology – FIR Systolic Arrays – Selection of Scheduling Vector – Matrix Multiplication and 2D Systolic Array Design – Systolic Design for Space Representations contain Delays.

UNIT -IV:**Fast Convolution:**

Introduction – Cook-Toom Algorithm – Winograd algorithm – Iterated Convolution – Cyclic Convolution – Design of Fast Convolution algorithm by Inspection.

UNIT -V:**Low Power Design:**

Scaling Vs Power Consumption –Power Analysis, Power Reduction techniques – Power Estimation Approaches.

Programmable DSP: Evaluation of Programmable Digital Signal Processors, DSP Processors for Mobile and Wireless Communications, Processors for Multimedia Signal Processing.

TEXT BOOKS:

1. VLSI Digital Signal Processing- System Design and Implementation – Keshab K. Parhi, 1998, Wiley Inter Science.
2. VLSI and Modern Signal Processing – Kung S. Y, H. J. White House, T. Kailath, 1985, Prentice Hall.

REFERENCE BOOKS:

1. Design of Analog – Digital VLSI Circuits for Telecommunications and Signal Processing – Jose E. France, Yannis Tsividis, 1994, Prentice Hall.

2. VLSI Digital Signal Processing – Mediseti V. K, 1995, IEEE Press (NY), USA.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

OPTIMIZATION TECHNIQUES IN VLSI DESIGN

(ELECTIVE -IV)

UNIT –I:

Statistical Modeling:

Modeling sources of variations, Monte Carlo techniques, Process variation modeling- Pelgrom's model, Principle component based modeling, Quad tree based modeling, Performance modeling-Response surface methodology, delay modeling, interconnect delay models.

UNIT –II:

Statistical Performance, Power and Yield Analysis

Statistical timing analysis, parameter space techniques, Bayesian networks Leakage models, High level statistical analysis, Gate level statistical analysis, dynamic power, leakage power, temperature and power supply variations, High level yield estimation and gate level yield estimation.

UNIT –III:

Convex Optimization:

Convex sets, convex functions, geometric programming, trade-off and sensitivity analysis, Generalized geometric programming, geometric programming applied to digital circuit gate sizing, Floor planning, wire sizing, Approximation and fitting- Monomial fitting, Maxmonomial fitting, Posynomial fitting.

UNIT –IV:

Genetic Algorithm:

Introduction, GA Technology-Steady State Algorithm-Fitness Scaling-Inversion GA for VLSI Design, Layout and Test automation- partitioning-automatic placement, routing technology, Mapping for FPGA- Automatic test generation- Partitioning algorithm Taxonomy-Multi-way Partitioning Hybrid genetic-encoding-local improvement-WDFR Comparison of CAS-Standard cell placement-GASP algorithm-unified algorithm.

UNIT –V:

GA Routing Procedures and Power Estimation:

Global routing-FPGA technology mapping-circuit generation-test generation in a GA frame work-test generation procedures, Power estimation-application of GA-Standard cell placement-GA for ATG-problem encoding-fitness function-GA Vs Conventional algorithm.

TEXT BOOKS / REFERENCE BOOKS:

1. Statistical Analysis and Optimization for VLSI: Timing and Power - Ashish Srivastava, Dennis Sylvester, David Blaauw, Springer, 2005.
2. Genetic Algorithm for VLSI Design, Layout and Test Automation - Pinaki Mazumder, E.Mrudnick, Prentice Hall,1998.
3. Convex Optimization - Stephen Boyd, Lieven Vandenberghe, Cambridge University Press, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

SYSTEM ON CHIP ARCHITECTURE

(ELECTIVE -IV)

UNIT –I:**Introduction to the System Approach:**

System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT –II:**Processors:**

Introduction , Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT –III:**Memory Design for SOC:**

Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation , SOC Memory System, Models of Simple Processor – memory interaction.

UNIT -IV:**Interconnect Customization and Configuration:**

Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses , Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance- Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT –V:**Application Studies / Case Studies:**

SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

TEXT BOOKS:

1. Computer System Design System-on-Chip - Michael J. Flynn and Wayne Luk, Wiley India Pvt. Ltd.
2. ARM System on Chip Architecture – Steve Furber –2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer
2. Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology) – Jason Andrews – Newnes, BK and CDROM.
3. System on Chip Verification – Methodologies and Techniques –Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

SEMICONDUCTOR MEMORY DESIGN AND TESTING

(ELECTIVE -IV)

UNIT -I:

Random Access Memory Technologies:

SRAM – SRAM Cell structures, MOS SRAM Architecture, MOS SRAM cell and peripheral circuit operation, Bipolar SRAM technologies, SOI technology, Advanced SRAM architectures and technologies, Application specific SRAMs, DRAM – DRAM technology development, CMOS DRAM, DRAM cell theory and advanced cell structures, BICMOS DRAM, soft error failure in DRAM, Advanced DRAM design and architecture, Application specific DRAM

UNIT -II:

Non-volatile Memories:

Masked ROMs, High density ROM, PROM, Bipolar ROM, CMOS PROMS, EPROM, Floating gate EPROM cell, One time programmable EPROM, EEPROM, EEPROM technology and architecture, Non-volatile SRAM, Flash Memories (EPROM or EEPROM), advanced Flash memory architecture

UNIT -III:

Memory Fault Modeling Testing and Memory Design for Testability and Fault Tolerance: RAM fault modeling, Electrical testing, Pseudo Random testing, Megabit DRAM Testing, non-volatile memory modeling and testing, IDDQ fault modeling and testing, Application specific memory testing, RAM fault modeling, BIST techniques for memory

UNIT -IV:

Semiconductor Memory Reliability and Radiation Effects:

General reliability issues RAM failure modes and mechanism, Non-volatile memory reliability, reliability modeling and failure rate prediction, Design for Reliability, Reliability Test Structures, Reliability Screening and qualification, Radiation effects, Single Event Phenomenon (SEP), Radiation Hardening techniques, Radiation Hardening Process and Design Issues, Radiation Hardened Memory characteristics, Radiation Hardness Assurance and Testing, Radiation Dosimetry, Water Level Radiation Testing and Test structures

UNIT -V:

Advanced Memory Technologies and High-density Memory Packing Technologies: Ferroelectric RAMs (FRAMs), GaAs FRAMs, Analog memories, magneto resistive RAMs (MRAMs), Experimental memory devices, Memory Hybrids and MCMs (2D), Memory Stacks and MCMs (3D), Memory MCM testing and reliability issues, Memory cards, High Density Memory Packaging Future Directions

TEXT BOOKS:

1. Semiconductor Memories Technology – Ashok K. Sharma, 2002, Wiley.
2. Advanced Semiconductor Memories – Architecture, Design and Applications - Ashok K. Sharma- 2002, Wiley.
3. Modern Semiconductor Devices for Integrated Circuits – Chenming C Hu, 1st Ed., Prentice Hall.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (VLSI/VLSI Design/VLSI System Design)

VLSI LABORATORY - II

Note: All the following digital/analog circuits are to be designed and implemented using Cadence / Mentor Graphics / Synopsys / Equivalent CAD tools.

The design shall include Gate-level design/Transistor-level design/Hierarchical design/Verilog HDL or VHDL design, Logic synthesis, Simulation and verification, Scaling of CMOS Inverter for different technologies, study of secondary effects (temperature, power supply and process corners), Circuit optimization with respect to area, performance and/or power, Layout, Extraction of parasitics and back annotation, modifications in circuit parameters and layout consumption, DC/transient analysis, Verification of layouts (DRC, LVS).

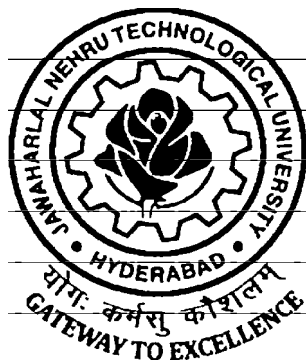
VLSI Back End Design programs:

1. Introduction to layout design rules
2. Layout, physical verification, placement & route for complex design, static timing analysis, IR drop analysis and crosstalk analysis of the following:
 - CMOS inverter
 - CMOS NOR/ NAND gates
 - CMOS XOR and MUX gates
 - CMOS half adder and full adder
 - Static / Dynamic logic circuits (register cell)
 - Latch
 - Pass transistor
3. Layout of any combinational circuit (complex CMOS logic gate)- Learning about data paths
4. Introduction to SPICE simulation and coding of NMOS/CMOS circuit
5. SPICE simulation of basic analog circuits: Inverter / Differential amplifier
6. Analog Circuit simulation (AC analysis) – CS & CD amplifier
7. System level design using PLL

ACADEMIC REGULATIONS COURSE STRUCTURE AND DETAILED SYLLABUS

M.TECH WIRELESS AND MOBILE COMMUNICATIONS

(Applicable for the batches admitted from 2013-14)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085.**

ACADEMIC REGULATIONS R13 FOR M. TECH. (REGULAR) DEGREE COURSE

Applicable for the students of M. Tech. (Regular) Course from the Academic Year 2013-14 and onwards

The M. Tech. Degree of Jawaharlal Nehru Technological University Hyderabad shall be conferred on candidates who are admitted to the program and who fulfil all the requirements for the award of the Degree.

1.0 ELIGIBILITY FOR ADMISSIONS

Admission to the above program shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

2.0 AWARD OF M. TECH. DEGREE

- 2.1 A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work.
- 2.2 A student, who fails to fulfill all the academic requirements for the award of the degree within four academic years from the year of his admission, shall forfeit his seat in M. Tech. course.
- 2.3 The student shall register for all 88 credits and secure all the 88 credits.
- 2.4 The minimum instruction days in each semester are 90.

3.0 A. COURSES OF STUDY

The following specializations are offered at present for the M. Tech. course of study.

1. Advanced Manufacturing Systems
2. Aerospace Engineering/ Aeronautical Engineering
3. Automation
4. Biomedical Signal Processing and Instrumentation
5. Bio-Technology
6. CAD/CAM
7. Chemical Engineering
8. Communication Systems
9. Computer Networks
10. Computer Networks and Information Security
11. Computer Science
12. Computer Science and Engineering
13. Computers and Communication Engineering.
14. Construction Management
15. Control Engineering
16. Control Systems
17. Cyber Forensic / Cyber Security & Information Technology
18. Design for Manufacturing/ Design and Manufacturing
19. Digital Electronics and Communication Engineering.
20. Digital Electronics and Communication Systems
21. Digital Systems and Computer Electronics
22. Electrical Power Engineering
23. Electrical Power Systems
24. Electronics & Instrumentation

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25. Electronics and Communication Engineering
 26. Embedded Systems
 27. Embedded Systems and VLSI Design
 28. Energy Systems
 29. Engineering Design
 30. Environmental Engineering
 31. Geoinformatics and Surveying Technology
 32. Geotechnical Engineering.
 33. Heating Ventilation & Air Conditioning.
 34. Highway Engineering
 35. Image Processing
 36. Industrial Engineering and Management
 37. Information Technology
 38. Infrastructure Engineering
 39. Machine Design
 40. Mechatronics.
 41. Microwave & Radar Engineering
 42. Nano Technology
 43. Neural Networks
 44. Parallel Computing
 45. Power and Industrial Drives
 46. Power Electronics
 47. Power Electronics and Electrical Drives
 48. Power Engineering and Energy Systems
 49. Power Plant Engineering & Energy Management
 50. Power System Control and Automation
 51. Power System with Emphasis H.V. Engineering / H.V. Engineering
 52. Production Engineering.
 53. Real Time Systems
 54. Software Engineering
 55. Structural Engineering
 56. Systems & Signal Processing
 57. Thermal Engineering.
 58. Transportation Engineering
 59. VLSI
 60. VLSI and Embedded System/ Electronics Design Technology
 61. VLSI Design
 62. VLSI System Design
 63. Web Technologies
 64. Wireless and Mobile Communication

and any other course as approved by the University from time to time.

3.0 B. Departments offering M. Tech. Programmes with specializations are noted below:

Civil Engg.	Construction Management Environmental Engineering Geoinformatics and Surveying Technology Geotechnical Engineering Highway Engineering Infrastructure Engineering Structural Engineering Transportation Engineering
EEE	Control Engineering Control Systems Electrical Power Engineering Electrical Power Systems Power and Industrial Drives Power Electronics Power Electronics and Electrical Drives Power Engineering and Energy Systems Power Plant Engineering & Energy Management Power System Control and Automation Power System with Emphasis H.V. Engineering / H.V. Engineering
ME	Advanced Manufacturing Systems Automation CAD/CAM Design for Manufacturing/ Design and Manufacturing Energy Systems Engineering Design Heating Ventilation & Air Conditioning Industrial Engineering and Management Machine Design Mechatronics. Power Plant Engineering & Energy Management Production Engineering Thermal Engineering.
ECE	Biomedical Signal Processing and Instrumentation Communication Systems Computers and Communication Engineering. Digital Electronics and Communication Engineering. Digital Electronics and Communication Systems Digital Systems and Computer Electronics Electronics & Instrumentation Electronics and Communication Engineering Embedded Systems Embedded Systems and VLSI Design

	Microwave & Radar Engineering Systems & Signal Processing VLSI VLSI and Embedded System/ Electronics Design Technology VLSI Design VLSI System Design Wireless and Mobile Communication
CSE	Computer Networks Computer Networks and Information Security Computer Science Computer Science and Engineering Cyber Forensic / Cyber Security & Information Technology Image Processing Information Technology Neural Networks Parallel Computing Real Time Systems Software Engineering Web Technologies
Aeronautical Engg.	Aerospace Engineering / Aeronautical Engineering
Bio-technology	Bio-Technology
Chemical Engg.	Chemical Engineering
Nano Technology	Nano Technology

4.0 ATTENDANCE

The programs are offered on a unit basis with each subject being considered a unit.

- 4.1 A student shall be eligible to write University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects.
- 4.2 Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 4.3 Shortage of Attendance below 65% in aggregate shall not be condoned.
- 4.4 Students whose shortage of attendance is not condoned in any semester are not eligible to write their end semester examination of that class and their registration shall stand cancelled.
- 4.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 4.6 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.
- 4.7 A candidate shall put in a minimum required attendance at least in three (3) theory subjects in the present semester to get promoted to the next semester. In order to qualify for the award of the M. Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 4.8 A student shall not be promoted to the next semester unless he satisfies the attendance requirements of the previous semester including the days of attendance in sports, games, NCC and NSS activities.

5.0 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

- 5.1 For the theory subjects 60 marks shall be awarded based on the performance in the End Semester Examination and 40 marks shall be awarded based on the Internal Evaluation. The internal evaluation shall be made based on the average of the marks secured in the two Mid Term-Examinations conducted-one in the middle of the Semester and the other immediately after the completion of instruction. Each mid term examination shall be conducted for a total duration of 120 minutes with Part A as compulsory question (16 marks) which consists of four sub-questions and carries 4 marks each and Part B with 3 questions to be answered out of 5 questions each question for 8 marks. If any candidate is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University. The details of the Question Paper pattern for End Examination (Theory) is given below:
- The End semesters Examination will be conducted for 60 marks which consists of two parts viz. i).Part-A for 20 marks, ii). Part –B for 40 marks.
 - Part-A is compulsory question where it consists of five questions one from each unit and carries four marks each. This will be treated as Question 1.
 - Part-B consists of five Questions (numbered from 2 to 6) carries 8 marks each. Each of these questions is from one unit and may contain sub-questions. For each question there will be an “either” “or” choice (that means there will be two questions from each unit and the student should answer only one question)
- 5.2 For practical subjects, 60 marks shall be awarded based on the performance in the End Semester Examinations and 40 marks shall be awarded based on the day-to-day performance as Internal Marks.
- 5.3 There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful.
- 5.4 There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of Head of the Department and two Senior Faculty members of the Department. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Comprehensive Viva-Voce is evaluated for 100 marks by the Committee. There are no internal marks for the Comprehensive Viva-Voce.
- 5.5 A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the End semester Examination and a minimum aggregate of 50% of the total marks in the End Semester Examination and Internal Evaluation taken together.
- 5.6 In case the candidate does not secure the minimum academic requirement in any subject (as specified in 5.5) he has to reappear for the End semester Examination in that subject. A candidate shall be given one chance to re-register for each subject provided the internal marks secured by a candidate are less than 50% and so has failed in the end examination. In such a case, the candidate must re-register for the subject(s) and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the end examination in those subject(s). In the event of the student taking another chance, his internal marks and end examination marks obtained in the previous attempt stand cancelled.
- 5.7 In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the End Examination in that subject. He shall re-register the subject when next

offered.

- 5.8 Laboratory examination for M. Tech. courses must be conducted with two Examiners, one of them being the Laboratory Class Teacher and the second examiner shall be another Laboratory Teacher.

6.0 EVALUATION OF PROJECT/DISSERTATION WORK

Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.

- 6.1 A Project Review Committee (PRC) shall be constituted with Principal as Chairperson, Heads of all the Departments offering the M. Tech. programs and two other senior faculty members.
- 6.2 Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 6.3 After satisfying 6.2, a candidate has to submit, in consultation with his project supervisor, the title, objective and plan of action of his project work to the Departmental Academic Committee for approval. Only after obtaining the approval of the Departmental Academic Committee can the student initiate the Project work.
- 6.4 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the Departmental Academic Committee. However, the Departmental Academic Committee shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 6.5 A candidate shall submit his status report in a bound-form in two stages at least with a gap of 3 months between them.
- 6.6 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of theory and practical course with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Principal through Head of the Department and make an oral presentation before the PRC.
- 6.7 Three copies of the Project Thesis certified by the supervisor shall be submitted to the College/School/Institute.
- 6.8 The thesis shall be adjudicated by one examiner selected by the University. For this, the Principal of the College shall submit a panel of 5 examiners, eminent in that field, with the help of the guide concerned and head of the department.
- 6.9 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis, in the time frame as decided by the PRC. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.
- 6.10 If the report of the examiner is favourable, Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the examiner who adjudicated the Thesis. The Board shall jointly report the candidate's work as one of the following:
- A. Excellent
 - B. Good
 - C. Satisfactory
 - D. Unsatisfactory

The Head of the Department shall coordinate and make arrangements for the conduct of Viva- Voce examination.

If the report of the Viva-Voce is unsatisfactory, the candidate shall retake the Viva-Voce examination only after three months. If he fails to get a satisfactory report at the second Viva- Voce examination, he will not be eligible for the award of the degree.

7.0 AWARD OF DEGREE AND CLASS

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of M. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured
First Class with Distinction	70% and above
First Class	Below 70% but not less than 60%
Second Class	Below 60% but not less than 50%
Pass Class	Below 50% but not less than 40%

The marks in internal evaluation and end examination shall be shown separately in the memorandum of marks.

8.0 WITHHOLDING OF RESULTS

If the student has not paid the dues, if any, to the university or if any case of indiscipline is pending against him, the result of the student will be withheld and he will not be allowed into the next semester. His degree will be withheld in such cases.

9.0 TRANSITORY REGULATIONS

- 9.1 Discontinued, detained, or failed candidates are eligible for admission to two earlier or equivalent subjects at a time as and when offered.
- 9.2 The candidate who fails in any subject will be given two chances to pass the same subject; otherwise, he has to identify an equivalent subject as per R13 academic regulations.

10. GENERAL

- 10.1 Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- 10.2 The academic regulation should be read as a whole for the purpose of any interpretation.
- 10.3 In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- 10.4 The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.

MALPRACTICES RULES**DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS**

	Nature of Malpractices/Improper conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/ year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.

4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.

8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)
 - (i) A show cause notice shall be issued to the college.
 - (ii) Impose a suitable fine on the college.
 - (iii) Shifting the examination centre from the college to another college for a specific period of not less than one year.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH - WIRELESS AND MOBILE COMMUNICATIONS

COURSE STRUCTURE AND SYLLABUS

I Year I Semester

Code	Group	Subject	L	P	Credits
		Advanced Data Communications	3	-	3
		TCP/IP and ATM Networks	3	-	3
		Advanced Digital Signal Processing	3	-	3
		Image and Video Processing	3	-	3
	Elective -I	Detection and Estimation Theory Random Processes and Time Series Analysis RF Circuit Design	3	-	3
	Elective -II	Voice Over IP Satellite Communications Spread Spectrum Communications	3	-	3
	Lab	Communication Lab-I	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

I Year II Semester

Code	Group	Subject	L	P	Credits
		Coding Theory and Techniques	3	-	3
		Wireless Communications & Networks	3	-	3
		Optical Communications Technology	3	-	3
		Wireless LANs and PANs	3	-	3
	Elective - III	Optical Networks Mobile Computing Technologies Ad-hoc and Wireless Sensor Networks	3	-	3
	Elective – IV	Network Security And Cryptography Propagation Models for Wireless Communications Software Radio	3	-	3
	Lab	Communication Lab-II	-	3	2
		Seminar	-	-	2
		Total Credits	18	3	22

II Year - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	-	3	2
		Project Work	-	-	18
		Total Credits	-	3	22

II Year - II Semester

Code	Group	Subject	L	P	Credits
		Project Work and Seminar	-	-	22
		Total Credits	-	-	22

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

ADVANCED DATA COMMUNICATIONS

UNIT -I:

Digital Modulation Schemes: BPSK, QPSK, 8PSK, 16PSK, 8QAM, 16QAM, DPSK – Methods, Band Width Efficiency, Carrier Recovery, Clock Recovery.

UNIT -II:

Basic Concepts of Data Communications, Interfaces and Modems: Data Communication Networks, Protocols and Standards, UART, USB, I2C, I2S, Line Configuration, Topology, Transmission Modes, Digital Data Transmission, DTE-DCE interface, Categories of Networks – TCP/IP Protocol suite and Comparison with OSI model.

UNIT -III:

Error Correction: Types of Errors, Vertical Redundancy Check (VRC), LRC, CRC, Checksum, Error Correction using Hamming code

Data Link Control: Line Discipline, Flow Control, Error Control

Data Link Protocols: Asynchronous Protocols, Synchronous Protocols, Character Oriented Protocols, Bit-Oriented Protocol, Link Access Procedures.

UNIT -IV:

Multiplexing: Frequency Division Multiplexing (FDM), Time Division Multiplexing (TDM), Multiplexing Application, DSL.

Local Area Networks: Ethernet, Other Ether Networks, Token Bus, Token Ring, FDDI.

Metropolitan Area Networks: IEEE 802.6, SMDS.

Switching: Circuit Switching, Packet Switching, Message Switching.

Networking and Interfacing Devices: Repeaters, Bridges, Routers, Gateway, Other Devices.

UNIT -V:

Multiple Access Techniques: Random Access, Aloha- Carrier Sense Multiple Access (CSMA)- Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA), Controlled Access- Reservation- Polling- Token Passing, Channelization, Frequency- Division Multiple Access (FDMA), Time - Division Multiple Access (TDMA), Code - Division Multiple Access (CDMA), OFDM and OFDMA.

TEXT BOOKS:

1. Data Communication and Computer Networking - B. A. Forouzan, 2nd Ed., 2003, TMH.
2. Advanced Electronic Communication Systems - W. Tomasi, 5th Ed., 2008, PEI.

REFERENCE BOOKS:

1. Data Communications and Computer Networks - Prakash C. Gupta, 2006, PHI.
2. Data and Computer Communications - William Stallings, 8th Ed., 2007, PHI.
3. Data Communication and Tele Processing Systems - T. Housely, 2nd Ed, 2008, BSP.
4. Data Communications and Computer Networks- Brijendra Singh, 2nd Ed., 2005, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

TCP/IP AND ATM NETWORKS

UNIT –I:

Internet Protocol: Internetworking, IPV4, IPV6, Transition from IPV4 to Ipv6. Process to process delivery, UDP, TCP and SCTP.

UNIT –II:

Congestion Control and Quality of Service: Data traffic, congestion, congestion control, two examples, Quality of Service, Techniques to improve QOS, Integrated services, and Differentiated services.

Queue management:-Passive-Drop-tail, Drop front, Random drop, Active –Early Random drop, Random Early Detection.

UNIT –III:

Spread Spectrum: Introduction, Basic Concept, Protection against Jamming, Spreading Codes (PN-Sequence), Generation, Properties, Types of Spread Spectrum Techniques, Application Of Spread Spectrum.

UNIT –IV:

X.25:- X.25 Layers, X.21 protocol,

Frame Relay: Introduction, Frame relay Operation, Frame relay Layers, Congestion Control, Leaky Bucket algorithm.

ATM:-Design Goals, ATM Architecture, Switching, Switch Fabric, ATM Layers, Service Classes, ATM Application.

UNIT –V:

Interconnection Networks: Introduction, Banyan Networks, Properties, Crossbar Switch, Three Stage Class Networks, Rearrangeable Networks, Folding Algorithm, Benes Networks, Lopping Algorithm, Bit Allocation Algorithm.

SONET/SDH:-Synchronous Transport Signals, Physical Configuration, SONET Layers, SONET Frame.

TEXT BOOKS:

1. Data communication and Networking. -B.A. Forouzen, 4th Edition TMH
2. TCP/IP Protocol Suit - B.A. Forouzen, 4th Edition TMH

REFERENCE BOOKS:

1. Wireless Communication System -Abhishek yadav –University Sciences Press, 2009
2. Wireless Digital Communications -Kamilo Feher-1999 PHI
3. High Performance TCP-IP Networking -Mahaboob Hassan -Jain Raj-PHI
4. Data Communication & Networking -B. A. Forouzan 2nd Edition TMH
5. ATM Fundamentals – N.N.Biswas -Adveture book Publishers-1998.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

ADVANCED DIGITAL SIGNAL PROCESSING

UNIT –I:

Review of DFT, FFT, IIR Filters and FIR Filters:

Multi Rate Signal Processing: Introduction, Decimation by a factor D, Interpolation by a factor I, Sampling rate conversion by a rational factor I/D, Multistage Implementation of Sampling Rate Conversion, Filter design & Implementation for sampling rate conversion.

UNIT –II:

Applications of Multi Rate Signal Processing: Design of Phase Shifters, Interfacing of Digital Systems with Different Sampling Rates, Implementation of Narrow Band Low Pass Filters, Implementation of Digital Filter Banks, Subband Coding of Speech Signals, Quadrature Mirror Filters, Transmultiplexers, Over Sampling A/D and D/A Conversion.

UNIT -III:

Non-Parametric Methods of Power Spectral Estimation: of spectra from finite duration observation of signals, Non-parametric Methods: Bartlett, Welch & Blackman-Tukey methods, Comparison of all Non-Parametric methods

UNIT –IV:

Implementation of Digital Filters: Introduction to filter structures (IIR & FIR), Frequency sampling structures of FIR, Lattice structures, Forward prediction error, Backward prediction error, Reflection coefficients for lattice realization, Implementation of lattice structures for IIR filters, Advantages of lattice structures.

UNIT –V:

Parametric Methods of Power Spectrum Estimation: Autocorrelation & Its Properties, Relation between auto correlation & model parameters, AR Models - Yule-Walker & Burg Methods, MA & ARMA models for power spectrum estimation, Finite word length effect in IIR digital Filters – Finite word-length effects in FFT algorithms.

TEXT BOOKS:

1. Digital Signal Processing: Principles, Algorithms & Applications - J.G.Proakis & D. G. Manolakis, 4th Ed., PHI.
2. Discrete Time Signal Processing - Alan V Oppenheim & R. W Schaffer, PHI.
3. DSP – A Practical Approach – Emmanuel C. Ifeachor, Barrie. W. Jervis, 2 ed., Pearson Education.

REFERENCE BOOKS:

1. Modern Spectral Estimation: Theory & Application – S. M .Kay, 1988, PHI.
2. Multi Rate Systems and Filter Banks – P.P.Vaidyanathan – Pearson Education.
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj, C.Gnanapriya, 2000, TMH.
4. Digital Spectral Analysis – Jr. Marple.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

IMAGE AND VIDEO PROCESSING

UNIT –I:

Fundamentals of Image Processing and Image Transforms: Basic steps of Image Processing System Sampling and Quantization of an image, Basic relationship between pixels.

Image Segmentation: Segmentation concepts, Point, Line and Edge Detection, Thresholding, Region based segmentation.

UNIT –II:

Image Enhancement: Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters.

Frequency domain methods: Basics of filtering in frequency domain, image smoothing, image sharpening, Selective filtering.

UNIT –III:

Image Compression: Image compression fundamentals - Coding Redundancy, Spatial and Temporal redundancy, Compression models: Lossy & Lossless, Huffman coding, , Bit plane coding, Transform coding, Predictive coding, Wavelet coding, Lossy Predictive coding, JPEG Standards.

UNIT -IV:

Basic Steps of Video Processing: Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.

UNIT –V:

2-D Motion Estimation: Optical flow, General Methodologies, Pixel Based Motion Estimation, Block-Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.

TEXT BOOKS:

1. Digital Image Processing – Gonzalez and Woods, 3rd Ed., Pearson.
2. Video Processing and Communication – Yao Wang, Joem Ostermann and Ya-quin Zhang. 1st Ed., PH Int.

REFERENCE BOOKS:

1. Digital Image Processing and Analysis-Human and Computer Vision Application with CVIP Tools – Scotte Umbaugh, 2nd Ed, CRC Press, 2011.
2. Digital Video Processing – M. Tekalp, Prentice Hall International.
3. Digital Image Processing – S.Jayaraman, S.Esakkirajan, T.Veera Kumar – TMH, 2009.
4. Multidimensional Signal, Image and Video Processing and Coding – John Woods, 2nd Ed, Elsevier.
5. Digital Image Processing with MATLAB and Labview – Vipula Singh, Elsevier.
6. Video Demystified – A Hand Book for the Digital Engineer – Keith Jack, 5th Ed., Elsevier.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

**DETECTION AND ESTIMATION THEORY
(ELECTIVE - I)****UNIT –I:**

Random Processes: Discrete Linear Models, Markov Sequences and Processes, Point Processes, and Gaussian Processes.

UNIT –II:

Detection Theory: Basic Detection Problem, Maximum A posteriori Decision Rule, Minimum Probability of Error Classifier, Bayes Decision Rule, Multiple-Class Problem (Bayes)- minimum probability error with and without equal a priori probabilities, Neyman-Pearson Classifier, General Calculation of Probability of Error, General Gaussian Problem, Composite Hypotheses.

UNIT –III:

Linear Minimum Mean-Square Error Filtering: Linear Minimum Mean Squared Error Estimators, Nonlinear Minimum Mean Squared Error Estimators. Innovations, Digital Wiener Filters with Stored Data, Real-time Digital Wiener Filters, Kalman Filters.

UNIT –IV:

Statistics: Measurements, Nonparametric Estimators of Probability Distribution and Density Functions, Point Estimators of Parameters, Measures of the Quality of Estimators, Introduction to Interval Estimates, Distribution of Estimators, Tests of Hypotheses, Simple Linear Regression, Multiple Linear Regression.

UNIT –V:

Estimating the Parameters of Random Processes from Data: Tests for Stationarity and Ergodicity, Model-free Estimation, Model-based Estimation of Autocorrelation Functions, Power Spectral Density Functions.

TEXT BOOKS:

1. Random Signals: Detection, Estimation and Data Analysis - K. Sam Shanmugan & A.M. Breipohl, Wiley India Pvt. Ltd, 2011.
2. Random Processes: Filtering, Estimation and Detection - Lonnie C. Ludeman, Wiley India Pvt. Ltd., 2010.

REFERENCE BOOKS:

1. Fundamentals of Statistical Signal Processing: Volume I Estimation Theory– Steven.M.Kay, Prentice Hall, USA, 1998.
2. Fundamentals of Statistical Signal Processing: Volume I Detection Theory– Steven.M.Kay, Prentice Hall, USA, 1998.
3. Introduction to Statistical Signal Processing with Applications - Srinath, Rajasekaran, Viswanathan, 2003, PHI.
4. Statistical Signal Processing: Detection, Estimation and Time Series Analysis – Louis L.Scharf, 1991, Addison Wesley.
5. Detection, Estimation and Modulation Theory: Part – I – Harry L. Van Trees, 2001, John Wiley & Sons, USA.
6. Signal Processing: Discrete Spectral Analysis – Detection & Estimation – Mischa Schwartz, Leonard Shaw, 1975, Mc Graw Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

RANDOM PROCESSES AND TIME SERIES ANALYSIS

(ELECTIVE - I)

UNIT -I:

Stationary Random Processes from a Probability Point of View: Probability Density and Probability Distribution Functions of a Random Variable, Expected Value of Random Variable, Markov and Chebyshev Inequalities, Computer Methods for Generating Random Variables, Multidimensional Random variables, Chi-square tests of hypotheses concerning distribution.

UNIT -II:

Random Processes Analyzed in the Time Domain: Continuous and Discrete Time, Stationarity, Auto Covariance and Auto Correlation functions, Continuity, differentiation, Integrals of Random Processes.

Some special cases: The Poisson process, the Normal (Gaussian) Process.

UNIT -III:

Random Processes Analyzed in the Frequency Domain: The Fourier Transform, Spectral Density, The Cross Power Spectral Density.

Linear Systems with random input: Impulse response, Transfer function, the relation between the spectral density for the input and for the output

UNIT -IV:

Markov Chains: Markov Processes: Discrete time Markov chains, state transition probability matrix, n-step state transition probability, transition diagrams, classification of states, limiting state probabilities, Continuous-time Markov chains, Gambler's ruin as a Markov chains

UNIT -V:

Basic Queuing Theory: Elements of a Queueing System, Little's Formula, M/M/1, Queue- Delay Distribution in M/M/1 System, M/M/1 System with Finite Capacity, M/G/1 Queueing system- Residual Service Time, Mean Delay in M/G/1 Systems.

TEXT BOOKS:

1. Probability, Random Variables, and Random Signal Principles – Peebles, P. Z (1993)- Third edition or later – New York – McGraw-Hill.
2. Fundamentals of Applied Probability and Random Processes – Oliver C. Ibe, Elsevier, 2009.
3. Probability and Random Processes for Electrical Engineering - Alberto Leon-Garcia, 2nd Ed, Pearson.

REFERENCE BOOKS:

1. Probability, Random Variables and Stochastic Processes – Athanasios Papoulis, S. Unnikrishna Pillai – TMH, 2008.
2. Probability and Random Processes with Applications to Signal Processing – Henry Stark, John W. Woods, 3rd Edition, Pearson.
3. Probability and Stochastic Processes – A Friendly Introduction for Electrical and Computer Engineers – Roy D. Yates, David J. Goodman.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

**RF CIRCUIT DESIGN
(ELECTIVE - I)****UNIT -I:**

Introduction to RF Electronics: The Electromagnetic Spectrum, units and Physical Constants, Microwave bands – RF behavior of Passive components: Tuned resonant circuits, Vectors, Inductors and Capacitors - Voltage and Current in capacitor circuits – Tuned RF / IF Transformers.

UNIT -II:

Transmission Line Analysis: Examples of transmission lines- Transmission line equations and Biasing- Micro Strip Transmission Lines- Special Termination Conditions- sourced and Loaded Transmission Lines.

Single And Multiport Networks: The Smith Chart, Interconnectivity networks, Network properties and Applications, Scattering Parameters.

UNIT- III:

Matching and Biasing Networks: Impedance matching using discrete components – Micro strip line matching networks, Amplifier classes of Operation and Biasing networks.

RF Passive & Active Components: Filter Basics – Lumped filter design – Distributed Filter Design – Diplexer Filters- Crystal and Saw filters- Active Filters - Tunable filters – Power Combiners / Dividers – Directional Couplers – Hybrid Couplers – Isolators. RF Diodes – BJTs- FETs- HEMTs and Models.

UNIT -IV:

RF Transistor Amplifier Design: Characteristics of Amplifiers - Amplifier Circuit Configurations, Amplifier Matching Basics, Distortion and noise products, Stability Considerations, Small Signal amplifier design, Power amplifier design, MMIC amplifiers, Broadband High Power multistage amplifiers, Low noise amplifiers, VGA Amplifiers.

UNIT -V:

Oscillators: Oscillator basics, Low phase noise oscillator design, High frequency Oscillator configuration, LC Oscillators, VCOs, Crystal Oscillators, PLL Synthesizer, and Direct Digital Synthesizer.

RF Mixers: Basic characteristics of a mixer - Active mixers- Image Reject and Harmonic mixers, Frequency domain considerations.

TEXT BOOKS:

1. RF Circuit design: Theory and applications by Reinhold Ludwig, Pavel Bretchko. Pearson Education Asia Publication, New Delhi 2001.
2. Radio Frequency and Microwave Communication Circuits – Analysis and Design – Devendra K. Misra, Wiley Student Edition, John Wiley & Sons.

REFERENCE BOOKS:

1. Radio frequency and microwave electronics illustrated Mathew M.Radmangh, 2001, PE Asia Publication.
2. RF Circuit Design – Christopher Bowick, Cheryl Aljuni and John Biyler, Elsevier Science, 2008.
3. Secrets of RF Design by Joseph Carr., 3rd Edition, Tab Electronics.
4. Complete Wireless Design by Cotter W. Sawyer, 2nd Edition, Mc-Graw Hill.
5. Practical RF Circuit Design for Modern Wireless Systems Vol.2 by Less Besser and Rowan Gilmore.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

VOICE OVER INTERNET PROTOCOL (VoIP)
(ELECTIVE - II)**UNIT –I:**

Overview of IP Protocol Suite: The Internet Protocol, The Transmission Control Protocol(TCP), The User Datagram Protocol (UDP), The Real-time Transport Protocol (RTP), IP multicast, IP version 6 (IP v6), Interworking IPv4 and IPv6, The VoIP Market, VoIP Challenges.

UNIT -II:

H.323 and H.245 Standards: The H.323 Architecture, Call Signaling-Call Scenarios, H.245 Control Signaling Conference calls- The Decomposed Gateway.

UNIT –III:

The Session Initiation Protocol (SIP): SIP architecture- Overview of SIP Messaging Syntax- Examples of SIP Message sequences- Redirect Servers- Proxy Servers. The Session Description Protocol (SDP)- Usage of SDP With SIP.

UNIT -IV:

Quality of Service (QoS): Need for QOS – End-to-end QoS, Overview of QOS solutions- The Resource reservation Protocol (RSVP)- Diffserv- The Diffserv Architecture- Multi-protocol Label Switching (MPLS)- The MPLS Architecture- MPLS Traffic Engineering- Label Distribution Protocols and Constraint- Based Routing.

UNIT -V:

VoIP and SS7: The SS7 Protocol Suite- The Message Transfer Part (MTP), ISDN User Part (ISUP) and Signaling Connection Control Part (SCCP), SS7 Network Architecture- Signaling Points(SPs)- Single Transfer Point (STP), - Service Control Point(SCP)- Message Signal Units (MSUs)- SS7 Addressing, ISUP, Performance Requirements for SS7, Sigtran- SigtranArchitecture- SCTP- M3UA Operation- M2UA Operation- M2PA Operation- Interworking SS7 and VoIP Architectures- Interworking Soft switch and SS7- Interworking H.323 and SS7.

TEXT BOOK:

1. Carrier Grade Voice over IP – Daniel Collins, 2nd ed., TMH.

REFERENCE BOOKS:

1. Understanding Voice over IP Technology – Nicholas Wittenberg – Cengage, 1st Ed., 2010.
2. Voice Over WLANS – The Complete Guide – Michael, F. Finnevan, Elsevier, 2008.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

SATELLITE COMMUNICATIONS
(ELECTIVE - II)

UNIT -I:

Communication Satellite: Orbit and Description: A Brief history of satellite Communication, Satellite Frequency Bands, Satellite Systems, Applications, Orbital Period and Velocity, effects of Orbital Inclination, Azimuth and Elevation, Coverage angle and slant Range, Eclipse, Orbital Perturbations, Placement of a Satellite in a Geo-Stationary orbit.

UNIT -II:

Satellite Sub-Systems: Attitude and Orbit Control system, TT&C subsystem, Attitude Control subsystem, Power systems, Communication subsystems, Satellite Antenna Equipment.

Satellite Link: Basic Transmission Theory, System Noise Temperature and G/T ratio, Basic Link Analysis, Interference Analysis, Design of satellite Links for a specified C/N, (With and without frequency Re-use), Link Budget.

UNIT -III:

Propagation Effects: Introduction, Atmospheric Absorption, Cloud Attenuation, Tropospheric and Ionospheric Scintillation and Low angle fading, Rain induced attenuation, rain induced cross polarization interference.

Multiple Access: Frequency Division Multiple Access (FDMA) - Intermodulation, Calculation of C/N, Time Division Multiple Access (TDMA) - Frame Structure, Burst Structure, Satellite Switched TDMA, On-board Processing, Demand Assignment Multiple Access (DAMA) – Types of Demand Assignment, Characteristics, CDMA Spread Spectrum Transmission and Reception.

UNIT -IV:

Earth Station Technology: Transmitters, Receivers, Antennas, Tracking Systems, Terrestrial Interface, Power Test Methods, Lower Orbit Considerations.

Satellite Navigation and Global Positioning Systems: Radio and Satellite Navigation, GPS Position Location Principles, GPS Receivers, GPS C/A Code Accuracy, Differential GPS.

UNIT -V:

Satellite Packet Communications: Message Transmission by FDMA: M/G/1 Queue, Message Transmission by TDMA, PURE ALOHA-Satellite Packet Switching, Slotted Aloha, Packet Reservation, Tree Algorithm.

TEXT BOOKS:

1. Satellite Communications –Timothy Pratt, Charles Bostian, Jeremy Allnutt, 2nd Edition, 2003, John Wiley & Sons.
2. Satellite Communications Engineering – Wilbur, L. Pritchard, Robert A. Nelson and Heuri G. Suyderhoud, 2nd Ed., Pearson Publications.
3. Digital Satellite Communications-Tri.T.Ha, 2nd Edition, 1990, Mc.Graw Hill.

REFERENCE BOOKS:

1. Satellite Communications-Dennis Roddy, 2nd Edition, 1996, McGraw Hill.
2. Satellite Communications: Design Principles – M. Richcharia, 2nd Ed., BSP, 2003.
3. Digital Satellite Communications – Tri. T. Ha, 2nd Ed., MGH, 1990.
4. Fundamentals of Satellite Communications – K. N. Raja Rao, PHI, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

**SPREAD SPECTRUM COMMUNICATIONS
(ELECTIVE –IV)****UNIT -I:**

Introduction to Spread Spectrum Systems: Fundamental Concepts of Spread Spectrum Systems, Pseudo Noise Sequences, Direct Sequence Spread Spectrum, Frequency Hop Spread Spectrum, Hybrid Direct Sequence Frequency Hop Spread Spectrum, Code Division Multiple Access.

Binary Shift Register Sequences for Spread Spectrum Systems:

Introduction, Definitions, Mathematical Background and Sequence Generator Fundamentals, Maximal Length Sequences, Gold Codes.

UNIT -II:

Code Tracking Loops: Introduction, Optimum Tracking of Wideband Signals, Base Band Delay-Lock Tracking Loop, Tau-Dither Non- Coherent Tracking Loop, Double Dither Non-Coherent Tracking Loop.

UNIT -III:

Initial Synchronization of the Receiver Spreading Code: Introduction, Problem Definition and the Optimum Synchronizer, Serial Search Synchronization Techniques, Synchronization using a Matched Filter, Synchronization by Estimated the Received Spreading Code.

UNIT -IV:

Cellular Code Division Multiple Access (CDMA) Principles: Introduction, Wide Band Mobile Channel, The Cellular CDMA System, Single User Receiver in a Multi User Channel, CDMA System Capacity,

Multi-User Detection in CDMA Cellular Radio: Optimal Multi-User Detection, Linear Suboptimal Detectors, Interference Combat Detection Schemes, Interference Cancellation Techniques.

UNIT -V:

Performance of Spread Spectrum Systems in Jamming Environments: Spread Spectrum Communication System Model, Performance of Spread Spectrum Systems without Coding.

Performance of Spread Spectrum Systems with Forward Error Correction: Elementary Block Coding Concepts, Optimum Decoding Rule, Calculation of Error Probability, Elementary Convolution Coding Concepts, Viterbi Algorithm, Decoding and Bit-Error Rate.

TEXT BOOKS:

1. Rodger E Ziemer, Roger L. Peterson and David E Borth - "Introduction to Spread Spectrum Communication- Pearson, 1st Edition, 1995.
2. Mosa Ali Abu-Rgheff – "Introduction to CDMA Wireless Communications." Elsevier Publications, 2008.

REFERENCE BOOKS:

1. George R. Cooper, Clare D. Mc Gillem - "Modern Communication and Spread Spectrum," McGraw Hill, 1986.
2. Andrew j. Viterbi - "CDMA: Principles of spread spectrum communication," Pearson Education, 1st Edition, 1995.
3. Kamilo Feher - "Wireless Digital Communications," PHI, 2009.
4. Andrew Richardson - "WCDMA Design Handbook," Cambridge University Press, 2005.
5. Steve Lee - Spread Spectrum CDMA, McGraw Hill, 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – I Sem. (WMC)

COMMUNICATION LAB - I

Note:

- A. Minimum of 10 Experiments have to be conducted
- B. All Experiments may be Simulated using MATLAB and to be verified using related training kits.

1. Implementation of Matched Filters.
2. Optimum receiver for the AWGN channel.
3. Design FIR (LP/HP/BP) filter using Window method.
4. Measurement of effect of Inter Symbol Interference.
5. Generation of constant envelope PSK signal wave form for different values of M.
6. Simulation of PSK system with M=4
7. Simulation of DPSK system with M=4
8. Design of FSK system
9. Simulation of correlation type demodulation for FSK signal
10. BPSK Modulation and Demodulation techniques
11. QPSK Modulation and Demodulation techniques
12. DQPSK Modulation and Demodulation techniques
13. 8-QAM Modulation and Demodulation techniques
14. DQAM Modulation and Demodulation techniques
15. Verification of Decimation and Interpolation of a given signal
16. Power spectrum estimation using AR models

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M. Tech – I Year – II Sem. (WMC)

CODING THEORY AND TECHNIQUES

UNIT -I:

Coding for Reliable Digital Transmission and Storage: Mathematical model of Information, A Logarithmic Measure of Information, Average and Mutual Information and Entropy, Types of Errors, Error Control Strategies.

Linear Block Codes: Introduction to Linear Block Codes, Syndrome and Error Detection, Minimum Distance of a Block code, Error-Detecting and Error-correcting Capabilities of a Block code, Standard array and Syndrome Decoding, Probability of an undetected error for Linear Codes over a BSC, Hamming Codes. Applications of Block codes for Error control in data storage system

UNIT –II:

Cyclic Codes: Description, Generator and Parity-check Matrices, Encoding, Syndrome Computation and Error Detection, Decoding, Cyclic Hamming Codes, Shortened cyclic codes, Error-trapping decoding for cyclic codes, Majority logic decoding for cyclic codes.

UNIT –III:

Convolutional Codes: Encoding of Convolutional Codes, Structural and Distance Properties, maximum likelihood decoding, Sequential decoding, Majority- logic decoding of Convolution codes. Application of Viterbi Decoding and Sequential Decoding, Applications of Convolutional codes in ARQ system.

UNIT –IV:

Burst –Error-Correcting Codes: Decoding of Single-Burst error Correcting Cyclic codes, Single-Burst-Error-Correcting Cyclic codes, Burst-Error-Correcting Convolutional Codes, Bounds on Burst Error-Correcting Capability, Interleaved Cyclic and Convolutional Codes, Phased-Burst –Error-Correcting Cyclic and Convolutional codes.

UNIT -V:

BCH – Codes: BCH code- Definition, Minimum distance and BCH Bounds, Decoding Procedure for BCH Codes- Syndrome Computation and Iterative Algorithms, Error Location Polynomials and Numbers for single and double error correction

TEXT BOOKS:

1. Error Control Coding- Fundamentals and Applications –Shu Lin, Daniel J. Costello, Jr, Prentice Hall, Inc.
2. Error Correcting Coding Theory- Man Young Rhee- 1989, McGraw-Hill Publishing.

REFERENCE BOOKS:

1. Digital Communications-Fundamental and Application - Bernard Sklar, PE.
2. Digital Communications- John G. Proakis, 5th Ed., 2008, TMH.
3. Introduction to Error Control Codes-Salvatore Gravano-Oxford
4. Error Correction Coding – Mathematical Methods and Algorithms – Todd K. Moon, 2006, Wiley India.
5. Information Theory, Coding and Cryptography – Ranjan Bose, 2nd Edition, 2009, TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

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WIRELESS COMMUNICATIONS AND NETWORKS

UNIT -I:

The Cellular Concept-System Design Fundamentals: Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference, Power Control for Reducing interference, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring.

UNIT –II:

Mobile Radio Propagation: Large-Scale Path Loss: Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, The Three Basic Propagation Mechanisms, Reflection-Reflection from Dielectrics, Brewster Angle, Reflection from perfect conductors, Ground Reflection (Two-Ray) Model, Diffraction-Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- Longley-Ryce Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models-Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT –III:

Mobile Radio Propagation: Small –Scale Fading and Multipath: Small Scale Multipath propagation- Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel- Relationship between Bandwidth and Received power, Small-Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small-Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler spread in Clarke's model, Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT -IV:

Equalization and Diversity: Introduction, Fundamentals of Equalization, Training A Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity Techniques-Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT -V:

Wireless Networks: Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11, IEEE 802.11 Medium Access Control, Comparison of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, Hiper Lan, WLL.

TEXT BOOKS:

1. Wireless Communications, Principles, Practice – Theodore, S. Rappaport, 2nd Ed., 2002, PHI.
2. Wireless Communications-Andrea Goldsmith, 2005 Cambridge University Press.
3. Mobile Cellular Communication – Gottapu Sasibhushana Rao, Pearson Education, 2012.

REFERENCE BOOKS:

1. Principles of Wireless Networks – Kaveh Pah Laven and P. Krishna Murthy, 2002, PE.
2. Wireless Digital Communications – Kamilo Feher, 1999, PHI.
3. Wireless Communication and Networking – William Stallings, 2003, PHI.
4. Wireless Communication – Upen Dalal, Oxford Univ. Press.
5. Wireless Communications and Networking – Vijay K. Gary, Elsevier.

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M. Tech – I Year – II Sem. (WMC)

OPTICAL COMMUNICATIONS TECHNOLOGY

UNIT –I:

Signal Propagation in Optical Fibers: Geometrical Optics approach and Wave Theory approach, Loss and Bandwidth, Chromatic Dispersion, Non Linear effects- Stimulated Brillouin and Stimulated Raman Scattering, Propagation in a Non-Linear Medium, Self Phase Modulation and Cross Phase Modulation, Four Wave Mixing, Principle of Solitons.

UNIT –II:

Fiber Optic Components for Communication & Networking: Couplers, Isolators and Circulators, Multiplexers, Bragg Gratings, Fabry-Perot Filters, Mach Zender Interferometers, Arrayed Waveguide Grating, Tunable Filters, High Channel Count Multiplexer Architectures, Optical Amplifiers, Direct and External Modulation Transmitters, Pump Sources for Amplifiers, Optical Switches and Wavelength Converters.

UNIT –III:

Modulation and Demodulation: Signal formats for Modulation, Subcarrier Modulation and Multiplexing, Optical Modulations – Duobinary, Single Side Band and Multilevel Schemes, Ideal and Practical receivers for Demodulation, Bit Error Rates, Timing Recovery and Equalization, Reed-Solomon Codes for Error Detection and Correction.

UNIT -IV:

Transmission System Engineering: System Model, Power Penalty in Transmitter and Receiver, Optical Amplifiers, Crosstalk and Reduction of Crosstalk, Cascaded Filters, Dispersion Limitations and Compensation Techniques.

UNIT –V:

Fiber Non-linearities and System Design Considerations: Limitation in High Speed and WDM Systems due to Non-linearities in Fibers, Wavelength Stabilization against Temperature Variations, Overall System Design considerations – Fiber Dispersion, Modulation, Non-Linear Effects, Wavelengths, All Optical Networks.

TEXT BOOKS:

1. Optical Networks: A Practical Perspective - Rajiv Ramaswami and Kumar N. Sivarajan, 2nd Ed., 2004, Elsevier Morgan Kaufmann Publishers.
2. Optical Fiber Communications – Gerd Keiser, 3rd Ed., 2000, McGraw Hill.

REFERENCE BOOKS:

1. Optical Fiber Communications: Principles and Practice – John.M.Senior, 2nd Ed., 2000, PE.
2. Fiber Optics Communication – Harold Kolimbris, 2nd Ed., 2004, PEI
3. Optical Networks: Third Generation Transport Systems – Uyles Black, 2nd Ed., 2009, PEI.
4. Optical Fiber Communications – Govind Agarwal, 2nd Ed., 2004, TMH.
5. Optical Fiber Communications and Its Applications – S.C.Gupta, 2004, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

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WIRELESS LANS AND PANS

UNIT –I:

Wireless System & Random Access Protocols: Introduction, First and Second Generation Cellular Systems, Cellular Communications from 1G to 3G, Wireless 4G systems, The Wireless Spectrum; Random Access Methods: Pure ALOHA, Slotted ALOHA, Carrier Sense Multiple Access (CSMA), Carrier Sense Multiple Access with Collision Detection (CSMA/CD), Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA).

UNIT –II:

Wireless LANs: Introduction, importance of Wireless LANs, WLAN Topologies, Transmission Techniques: Wired Networks, Wireless Networks, comparison of wired and Wireless LANs; WLAN Technologies: Infrared technology, UHF narrowband technology, Spread Spectrum technology

UNIT –III:

The IEEE 802.11 Standard for Wireless LANs: Network Architecture, Physical layer, The Medium Access Control Layer; MAC Layer issues: Hidden Terminal Problem, Reliability, Collision avoidance, Congestion avoidance, Congestion control, Security, The IEEE 802.11e MAC protocol

UNIT –IV:

Wireless PANs: Introduction, importance of Wireless PANs, The Bluetooth technology: history and applications, technical overview, the Bluetooth specifications, piconet synchronization and Bluetooth clocks, Master-Slave Switch; Bluetooth security; Enhancements to Bluetooth: Bluetooth interference issues, Intra and Inter Piconet scheduling, Bridge selection, Traffic Engineering, QoS and Dynamics Slot Assignment, Scatternet formation.

UNIT –V:

The IEEE 802.15 working Group for WPANs: The IEEE 802.15.3, The IEEE 802.15.4, ZigBee Technology, ZigBee components and network topologies, The IEEE 802.15.4 LR-WPAN Device architecture: Physical Layer, Data Link Layer, The Network Layer, Applications; IEEE 802.15.3a Ultra wideband.

TEXT BOOKS:

1. Ad Hoc and Sensor Networks - Carlos de Morais Cordeiro and Dharma Prakash Agrawal, World Scientific, 2011.
2. Wireless Communications and Networking - Vijay K.Garg, Morgan Kaufmann Publishers, 2009.

REFERENCE BOOKS

1. Wireless Networks - Kaveh Pahlaram, Prashant Krishnamurthy, PHI, 2002.
2. Wireless Communication- Marks Ciampor, George Olenewa, Cengage Learning, 2007.

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OPTICAL NETWORKS

(ELECTIVE – III)

UNIT –I:

Client Layers of Optical Networks: SONET / SDH – Multiplexing, Frame Structure, Physical Layer, Infrastructure, ATM – Functions, Adaptation layers, QoS, Flow Control Signaling and Routing, IP – Routing, QoS, MPLS, Storage Area Networks – ESCON, Fiber Channel, HIPPI, Gigabit Ethernet.

UNIT -II:

WDM Network Elements and Design: Optical Line Terminals and Amplifiers, Add/Drop Multiplexers, Optical Cross Connects, Cost trade-offs in Network Design, LTD and RWA Problems, Dimensioning – Wavelength Routing Networks, Statistical and Maximum Load Dimensioning Models.

UNIT –III:

Network Control and Management: Network Management Functions, Optical Layer Services and Interfacing, Layers within Optical Layer, Multivendor Interoperability, Performance and Fault Management, Configuration Management, Optical Safety.

UNIT –IV:

Network Survivability: Basic Concepts of Survivability, Protection in SONET/SDH Links and Rings, Protection in IP Networks, Optical Layer Protection – Service Classes, Protection Schemes, Interworking between Layers.

UNIT –V:

Access Networks and Photonic Packet Switching: Network Architecture, Enhanced HFC, FTTC, Photonic Packet Switching – OTDM, Synchronization, Header Processing, Buffering, Burst Switching, Test Beds.

TEXT BOOKS:

1. Optical Networks: A Practical Perspective - Rajiv Ramaswami and Kumar N. Sivarajan, 2 ed., 2004, Elsevier Morgan Kaufmann Publishers (An Imprint of Elsevier).
2. WDM Optical Networks: Concepts, Design and Algorithms – C. Siva Rama Murthy and Mohan Guruswamy 2nd Ed., 2003, PEI.
3. Optical Networks: Third Generation Transport Systems – Uyles Black, 2nd Ed., 2009, PEI.

REFERENCE BOOKS:

1. Optical Fiber Communications: Principles and Practice – John.M.Senior, 2nd Ed.,2000, PE.
2. Fiber Optics Communication – Harold Kolimbris, 2nd Ed., 2004, PEI.
3. Networks – Timothy S. Ramteke, 2nd Ed., 2004, PEI.
4. Optical Fiber Communications – Govind Agarwal, 2nd Ed., 2004, TMH.
5. Optical Fiber Communications and Its Applications – S.C.Gupta, 2004, PHI.
6. Telecommunication System Engineering – Roger L. Freeman, 4th Ed., Wiley-Interscience, John Wiley & Sons, 2004.

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M. Tech – I Year – II Sem. (WMC)

MOBILE COMPUTING TECHNOLOGIES

(ELECTIVE – III)

UNIT –I:

Introduction to Mobile Computing Architecture: Mobile Computing – Dialog Control – Networks – Middleware and Gateways – Application and Services – Developing Mobile Computing Applications – Security in Mobile Computing – Architecture for Mobile Computing – Three Tier Architecture – Design considerations for Mobile Computing – Mobile Computing through Internet – Making existing Applications Mobile Enabled.

UNIT –II:

Cellular Technologies: GSM, GPS, GPRS, CDMA and 3G: Bluetooth – Radio Frequency Identification – Wireless Broadband – Mobile IP – Internet Protocol Version 6 (IPv6) – Java Card – GSM Architecture – GSM Entities – Call Routing in GSM – PLMN Interfaces – GSM addresses and Identifiers – Network aspects in GSM – Authentication and Security – Mobile computing over SMS – GPRS and Packet Data Network – GPRS Network Architecture – GPRS Network Operations – Data Services in GPRS – Applications for GPRS – Limitations of GPRS – Spread Spectrum technology – Is-95 – CDMA Versus GSM – Wireless Data – Third Generation Networks – Applications on 3G

UNIT –III:

Wireless Application Protocol (WAP) and Wireless LAN: WAP – MMS – Wireless LAN Advantages – IEEE 802.11 Standards – Wireless LAN Architecture – Mobility in wireless LAN

Intelligent Networks and Interworking : Introduction – Fundamentals of Call processing – Intelligence in the Networks – SS#7 Signaling – IN Conceptual Model (INCM) – soft switch – Programmable Networks – Technologies and Interfaces for IN

UNIT –IV:

Client Programming, Palm OS, Symbian OS, Win CE Architecture: Introduction – Moving beyond the Desktop – A Peek under the Hood: Hardware Overview – Mobile phones – PDA – Design Constraints in Applications for Handheld Devices – Palm OS architecture – Application Development – Multimedia – Symbian OS Architecture – Applications for Symbian, Different flavors of Windows CE -Windows CE Architecture

J2ME: JAVA in the Handset – The Three-prong approach to JAVA Everywhere – JAVA 2 Micro Edition (J2ME) technology – Programming for CLDC – GUI in MIDP – UI Design Issues – Multimedia – Record Management System – Communication in MIDP – Security considerations in MIDP – Optional Packages

UNIT –V:

Voice Over Internet Protocol and Convergence: Voice over IP- H.323 Framework for Voice over IP – Session Initiation Protocol – Comparison between H.323 and SIP – Real Time protocols – Convergence Technologies – Call Routing – Voice over IP Applications – IP multimedia subsystem (IMS) – Mobile VoIP

Security Issues in Mobile Computing: Introduction – Information Security – Security Techniques and Algorithms – Security Protocols – Public Key Infrastructure – Trust – Security Models – Security frameworks for Mobile Environment

TEXT BOOKS:

1. Mobile Computing – Technology, Applications and Service Creation – Asoke K Talukder, Roopa R Yavagal, 2009, TATA McGraw Hill.

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2. Mobile Communications – Jochen Schiller – 2nd Edition – Pearson Education.

REFERENCE BOOKS:

1. The CDMA 2000 System for Mobile Communications – Vieri Vaughni, Alexander Damn Jaonvic – Pearson.
2. Adalestein : Fundamentals of Mobile & Parvasive Computing, 2008, TMH.

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M. Tech – I Year – II Sem. (WMC)

ADHOC AND WIRELESS SENSOR NETWORKS

(ELECTIVE – III)

UNIT –I:

Wireless LANs: Introduction, Transmission Techniques: Wired and Wireless Networks; Medium Access Control Issues: Hidden Terminal Problem, Reliability, Collision Avoidance, Congestion Control, Energy Efficiency; The IEEE 802.11 standard for Wireless LANs: Network Architecture, The Physical Layer, The MAC Layer

UNIT -II:

Ad-Hoc Wireless Networks: Introduction; Cellular and Ad-Hoc Wireless Networks; Issues in Ad-Hoc Wireless Networks: Medium Access Scheme, Routing, Multicasting, Transport Layer Protocols, Quality of Service Provisioning, Energy Management, Scalability

UNIT -III:

Medium Access Control Protocols: Introduction; Issues in Designing a MAC protocol: Bandwidth efficiency, Quality of Service support, Synchronization, Hidden and exposed terminal problems, Error prone shared broadcast channel, mobility nodes; Design goals of a MAC protocol; Classification of MAC protocols; Contention-based protocols: MACAW, Floor acquisition multiple access protocol, Busy tone multiple access protocols, MACA by invitation, Media access with reduced handshake; Contention-based protocols with reservation mechanisms; Contention-based MAC protocols with scheduling mechanisms; MAC protocols that use directional antennas

UNIT -IV:

Routing and Transport Layer Protocols: Introduction, issues in designing a routing protocol, Classification of routing protocols, Table-driven protocols, On-demand routing protocols, Hybrid routing protocols, Routing with efficient flooding mechanisms, Hierarchical routing protocols, Power-aware routing protocols; Introduction to transport layer protocols, design issues and goals of transport layer protocol, Classification of transport layer solutions

UNIT -V:

Wireless Sensor Networks: Introduction, Sensor Network Architecture, Data Dissemination, Data gathering, MAC protocols for Sensor Networks, Location Discovery, Quality of a Sensor Network

TEXT BOOKS:

1. Ad Hoc Wireless Networks - C. Siva Ram Murthy, 2004, Pearson Education.
2. Ad Hoc and Sensor Networks - Carlos de Moraes Cordeiro and Dharma Prakash Agrawal, 2011, World Scientific.
3. Wireless Sensor Networks - Kazen Sohraby, Daniel Minoli, Taieb Znati, 1991, Wiley Student Edition.

REFERENCE BOOKS:

1. Ad-Hoc Mobile Wireless Networks: Protocols and Systems - C.K. Toh, 1st Edn, Pearson Education.
2. Wireless Sensor Networks - C.S. Raghavendra, Krishna M. SivaLingam, 2004, Springer.
3. Wireless Ad- hoc and Sensor Networks: Protocols, Performance and Control - Jagannathan Sarangapani, CRC Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (WMC)

NETWORK SECURITY AND CRYPTOGRAPHY

(ELECTIVE – IV)

UNIT -I:

Introduction: Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security.

Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Differential and Linear Cryptanalysis, Block Cipher Design Principles and Modes of operations.

UNIT -II:

Encryption Algorithms: Triple DES, International Data Encryption algorithm, Blowfish, RC5, CAST-128, RC2, Characteristics of Advanced Symmetric block ciphers.

Conventional Encryption: Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT -III:

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography.

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT -IV:

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD File, Message digest Algorithm, Secure Hash Algorithm, RIPEMD-160, HMAC.

Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, X.509 directory Authentication service.

Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT -V:

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Combining security Associations, Key Management. Web Security: Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. Cryptography and Network Security: Principles and Practice - William Stallings, Pearson Education.
2. Network Security Essentials (Applications and Standards) - William Stallings Pearson Education.

REFERENCE BOOKS:

1. Fundamentals of Network Security - Eric Maiwald (Dreamtech press).

2. Network Security - Private Communication in a Public World - Charlie Kaufman, Radia Perlman and Mike Speciner, Pearson/PHI.
3. Principles of Information Security - Whitman, Thomson.
4. Network Security: The complete reference - Robert Bragg, Mark Rhodes, TMH.
5. Introduction to Cryptography - Buchmann, Springer.

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M. Tech – I Year – II Sem. (WMC)

PROPAGATION MODELS FOR WIRELESS COMMUNICATION

(ELECTIVE – IV)

UNIT -I:

Basic Propagation Models: Reflection, Refraction and Transmission, Rough surface scattering, Geometrical optics Principles. Diffraction – Principle and Single Knife Edge Diffraction. Definition of pathloss. Noise Modelling, Free Space Loss, Plane Earth loss

UNIT –II:

Macro Cells: Definition of parameters, Empirical Path Loss Models, Physical models, ITU-R models , comparison of models.

Empirical Models, Physical models, line-of-sight models, Non-line-of –sight models.

Pico Cells: Empirical models of propagation with in buildings, physical models of indoor propagation, Models of Propagation into buildings, Ultra wide band indoor propagation, and Indoor link budgets, Introduction of Mega cells.

UNIT –III:

Mega Cells: Shadowing and Fast fading, Empirical Narrowband models, Statistical Models, Physical-Statistical models for built-up areas, and over all mobile satellite channel model.

Shadowing: Statistical characterization, Physical basis for shadowing, Impact on coverage, location variability, correlated shadowing.

UNIT –IV:

Fast Fading: Narrow band fast fading channel, AWGN Channel, Narrow band fading channel, Rayleigh, Rice and Nakagami-m distribution, Wideband fast fading –Effect of wideband fading, wide band channel model and parameters, frequency domain effects and Bello functions.

Overcoming Narrowband Fading: Space diversity, Polarization diversity, Time diversity, frequency diversity, and combining methods

Overcoming Wideband Fading: System modeling, Linear equalizers, Adaptive Equalizers, Non-Linear Equalizers, Rake receivers, and OFDM receivers.

UNIT -V:

Channel Measurements for Mobile Systems: Application for channel measurements, Impact of Measurement in accuracies, Signal sampling issues, measurement systems, equipment calibration and validation, outdoor and indoor measurements.

TEXT BOOKS:

1. Antennas and Propagation for Wireless Communication Systems: Simon R. saunders, Alejandro Aragon-Zavala, 2nd Ed. 2007, Wiley Student Edition.
2. Wireless Communication & Networks – Stallings, 2nd Ed., 2009, Pearson Education.

REFERENCE BOOKS:

1. Wireless Communications – SAHA – 2nd Edition, 2009.
2. Wireless Communications: Principles and Practice – Rappaport, 1st Impression, 2009, PHI.
3. Mobile Cellular Communications - G. Sashibhushana Rao, Pearson Education, 2013.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

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**SOFTWARE RADIO
(ELECTIVE – IV)****UNIT -I:**

Introduction: The Need for Software Radios, What is Software Radio, Characteristics and benefits of software radio- Design Principles of Software Radio, RF Implementation issues- The Purpose of RF Front – End, Dynamic Range- The Principal Challenge of Receiver Design – RF Receiver Front- End Topologies- Enhanced Flexibility of the RF Chain with Software Radios- Importance of the Components to Overall Performance- Transmitter Architectures and Their Issues- Noise and Distortion in the RF Chain, ADC and DAC Distortion.

UNIT -II:

Multi Rate Signal Processing: Introduction- Sample Rate Conversion Principles- Polyphase Filters- Digital Filter Banks- Timing Recovery in Digital Receivers Using Multirate Digital Filters.

Digital Generation of Signals: Introduction- Comparison of Direct Digital Synthesis with Analog Signal Synthesis- Approaches to Direct Digital Synthesis- Analysis of Spurious Signals- Spurious Components due to Periodic jitter- Band Pass Signal Generation- Performance of Direct Digital Synthesis Systems- Hybrid DDS-PLL Systems- Applications of direct Digital Synthesis- Generation of Random Sequences- ROM Compression Techniques.

UNIT -III:

Analog to Digital and Digital to Analog Conversion: Parameters of ideal data converters- Parameters of Practical data converters- Analog to Digital and Digital to Analog Conversion- Techniques to improve data converter performance- Common ADC and DAC architectures.

UNIT -IV:

Digital Hardware Choices: Introduction- Key Hardware Elements- DSP Processors- Field Programmable Gate Arrays- Trade-Offs in Using DSPs, FPGAs, and ASICs- Power Management Issues- Using a Combination of DSPs, FPGAs, and ASICs.

UNIT -V:

Object – Oriented Representation of Radios and Network Resources: Networks- Object Oriented Programming- Object Brokers- Mobile Application Environments- Joint Tactical Radio System.

Case Studies in Software Radio Design: Introduction and Historical Perspective, SPEAK easy- JTRS, Wireless Information Transfer System, SDR-3000 Digital Transceiver Subsystem, Spectrum Ware, CHARIOT.

TEXTBOOKS:

1. Software Radio: A Modern Approach to Radio Engineering - Jeffrey H. Reed, 2002, PEA Publication
2. Software Defined Radio: Enabling Technologies- Walter Tuttle Bee, 2002, Wiley Publications.

REFERENCE BOOKS:

1. Software Defined Radio for 3G - Paul Burns, 2002, Artech House.
2. Software Defined Radio: Architectures, Systems and Functions - Markus Dillinger, Kambiz Madani, Nancy Alonistioti, 2003, Wiley.
3. Software Radio Architecture: Object Oriented Approaches to wireless System Engineering – Joseph Mitola, III, 2000, John Wiley & Sons.
4. R.F.Microelectronics – B.Razavi, 1998, PHI.
5. DSP – A Computer Based Approach – S.K.Mithra, 1998, McGraw-Hill.

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COMMUNICATION LAB – II**Note:**

- A. Minimum of 10 Experiments have to be conducted
 - B. All Experiments may be Simulated using MATLAB and to be verified using related training kits.
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- 1. Measurement of Bit Error Rate using Binary Data
 - 2. Verification of minimum distance in Hamming code
 - 3. Determination of output of Convolutional Encoder for a given sequence
 - 4. Determination of output of Convolutional Decoder for a given sequence
 - 5. Efficiency of DS Spread- Spectrum Technique
 - 6. Simulation of Frequency Hopping (FH) system
 - 7. Determination of Losses in Optical fiber
 - 8. Calculation of Numerical Aperture in Optical fibers
 - 9. Observing the waveforms at various test points of a mobile phone using Mobile Phone Trainer
 - 10. Study of direct sequence spread spectrum modulation and demodulation using CDMA-DSSS-BER trainer
 - 11. Study of ISDN training system with protocol analyzer
 - 12. Characteristics of LASER diode
 - 13. Determination of parameters using global positioning system trainer